Lab 1: Quartus II Tutorial

ECE 380

University of Alabama

Brandon Charles

Rebekah Flowers

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**Lab 1: Quartus Prime Tutorial**

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**Introduction**

The purpose of the laboratory was to become familiar with Quartus II. Students created three

different design entries using schematic capture, VHDL, and Schematic Entry at the Top Level.

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**Procedure**

1. We went through tutorials about quartus prime to make ourselves familiar with the software before getting into the lab.
2. We followed the lab #1 instruction manual using debugging techniques as posted by the Professor.
3. After each successful build we started compilation and showed the demo to the TA

**Results**

The first two test cases showed similar wavelengths, However the third one revealed a different wavelength.

Image 1:- example\_schematic.bdf

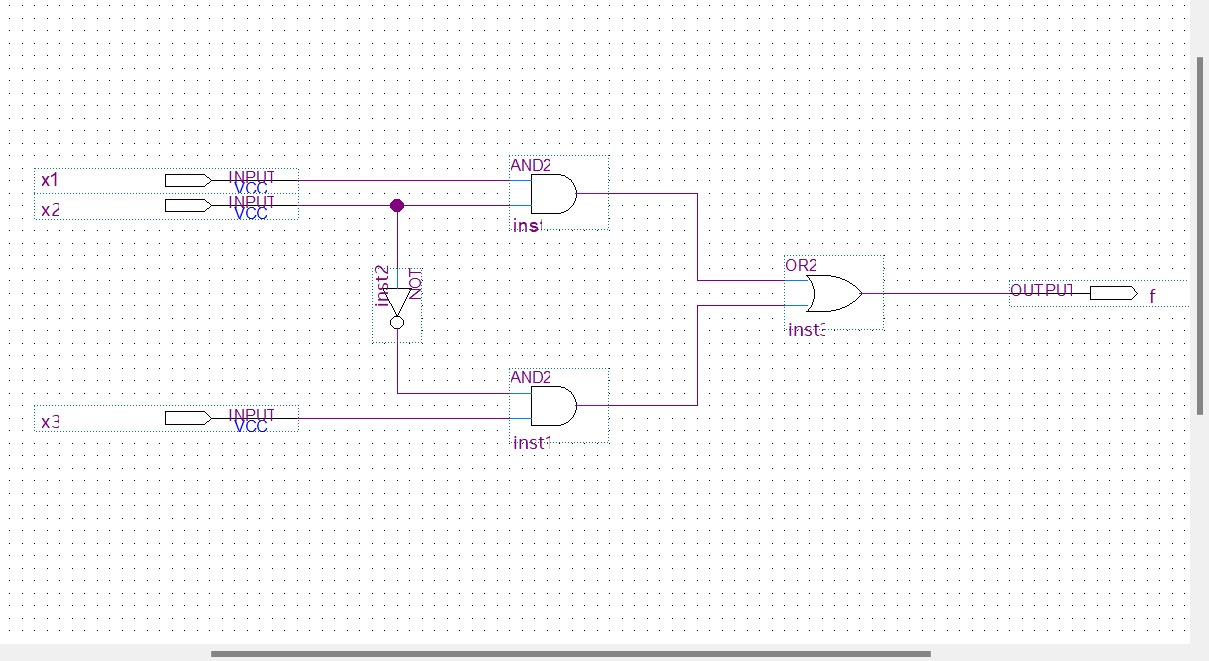


Image 2:- example\_schematic.vwf

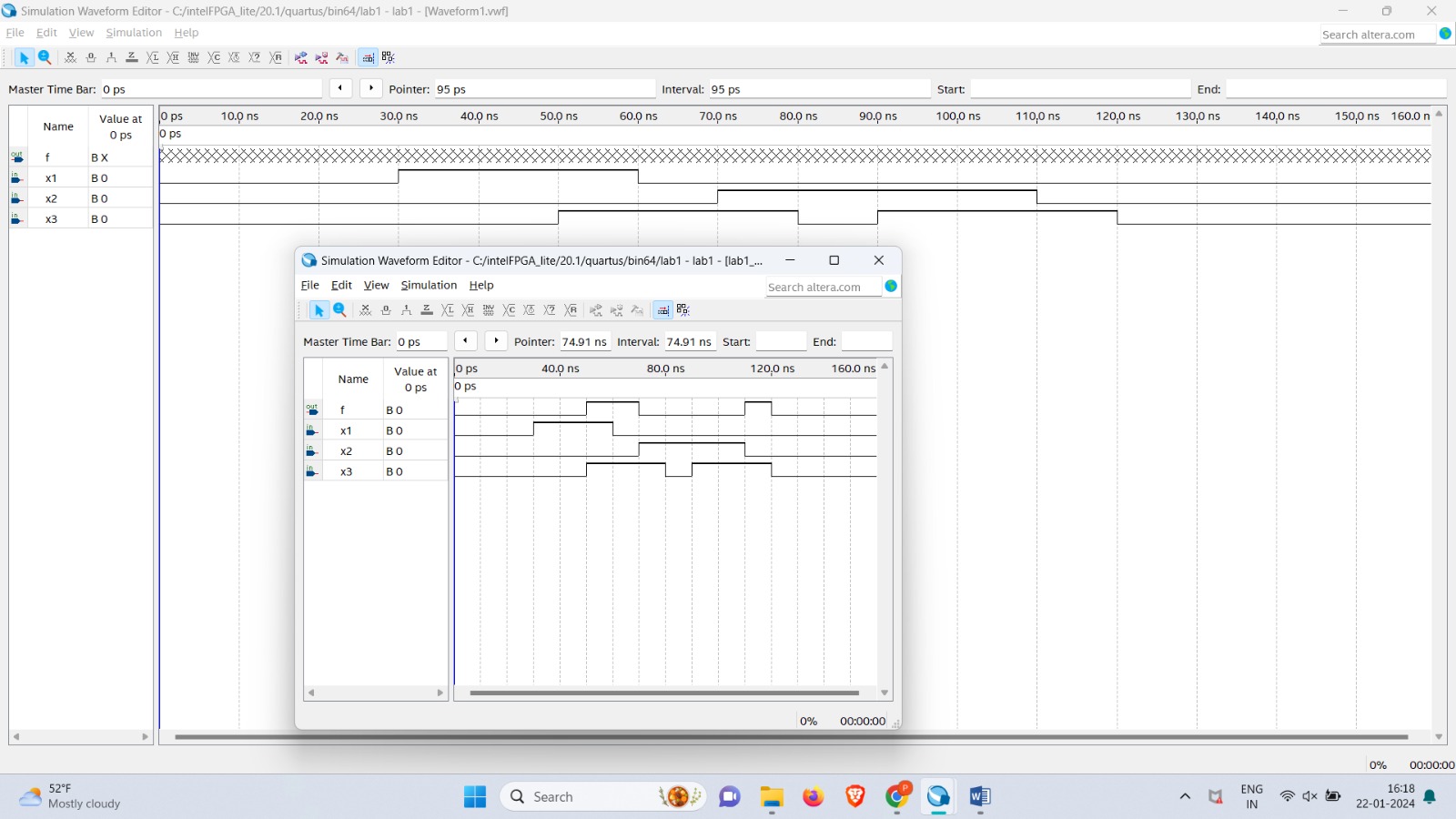


Image 3:- example\_vhdl.vhd

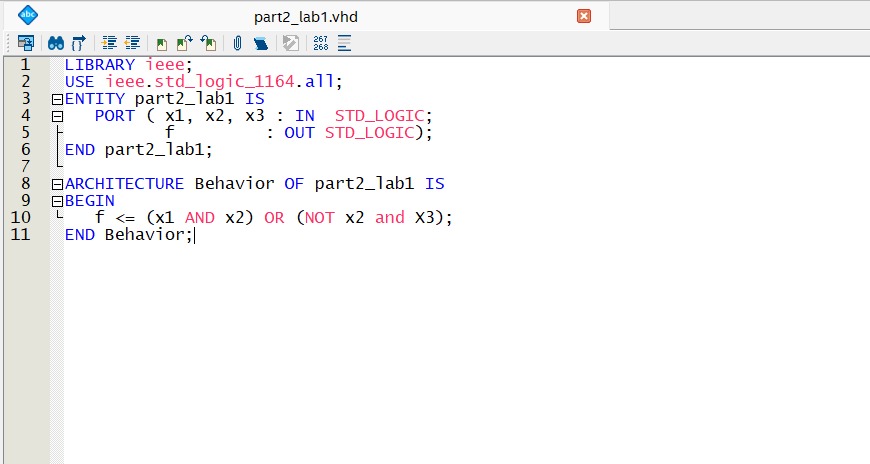


Image 4:- example\_vhdl.vwf

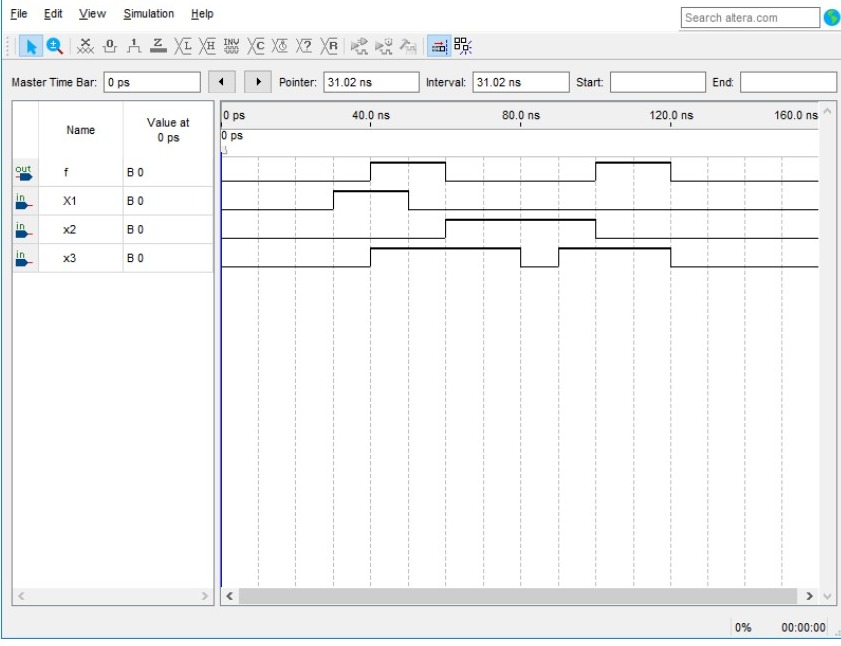


Image 5:- vhdlfunctions.vhd

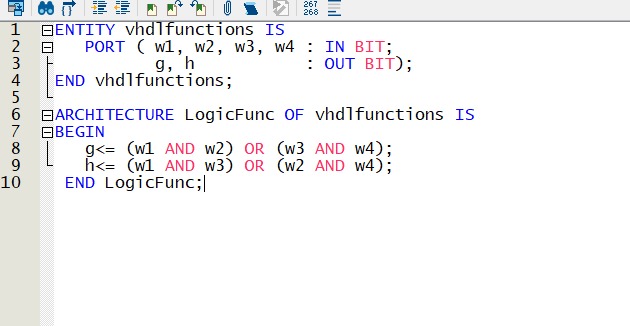


Image 6:- example\_mixed1.bdf

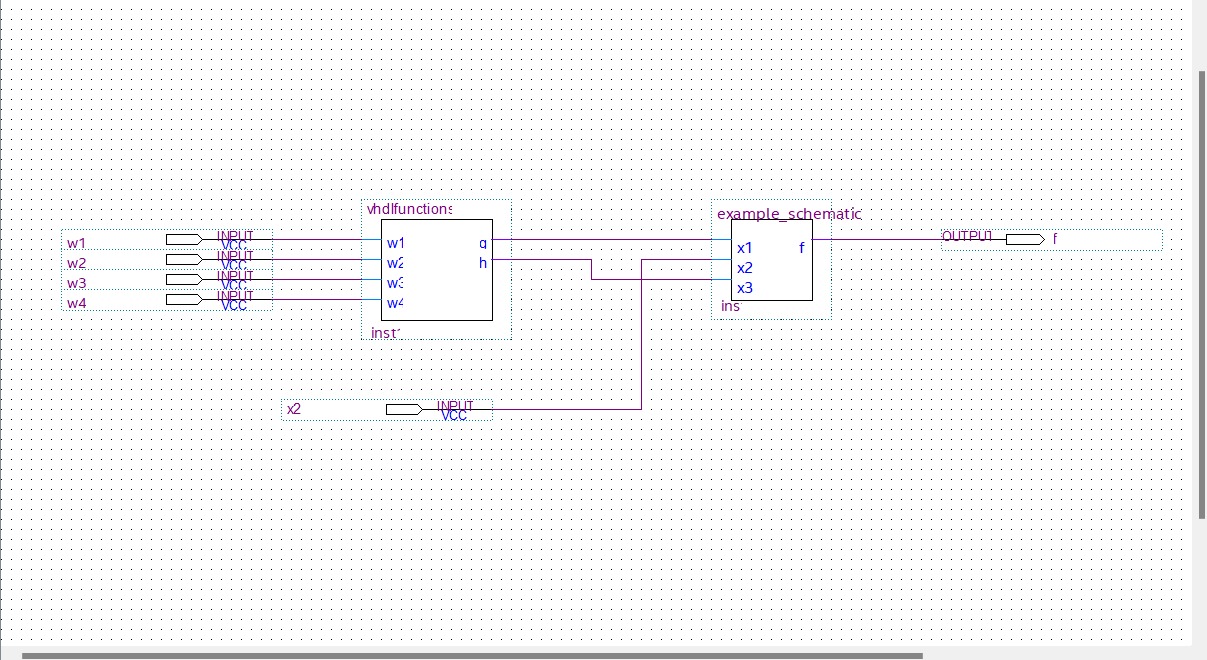


Image 7:- example\_mixed1.vwf

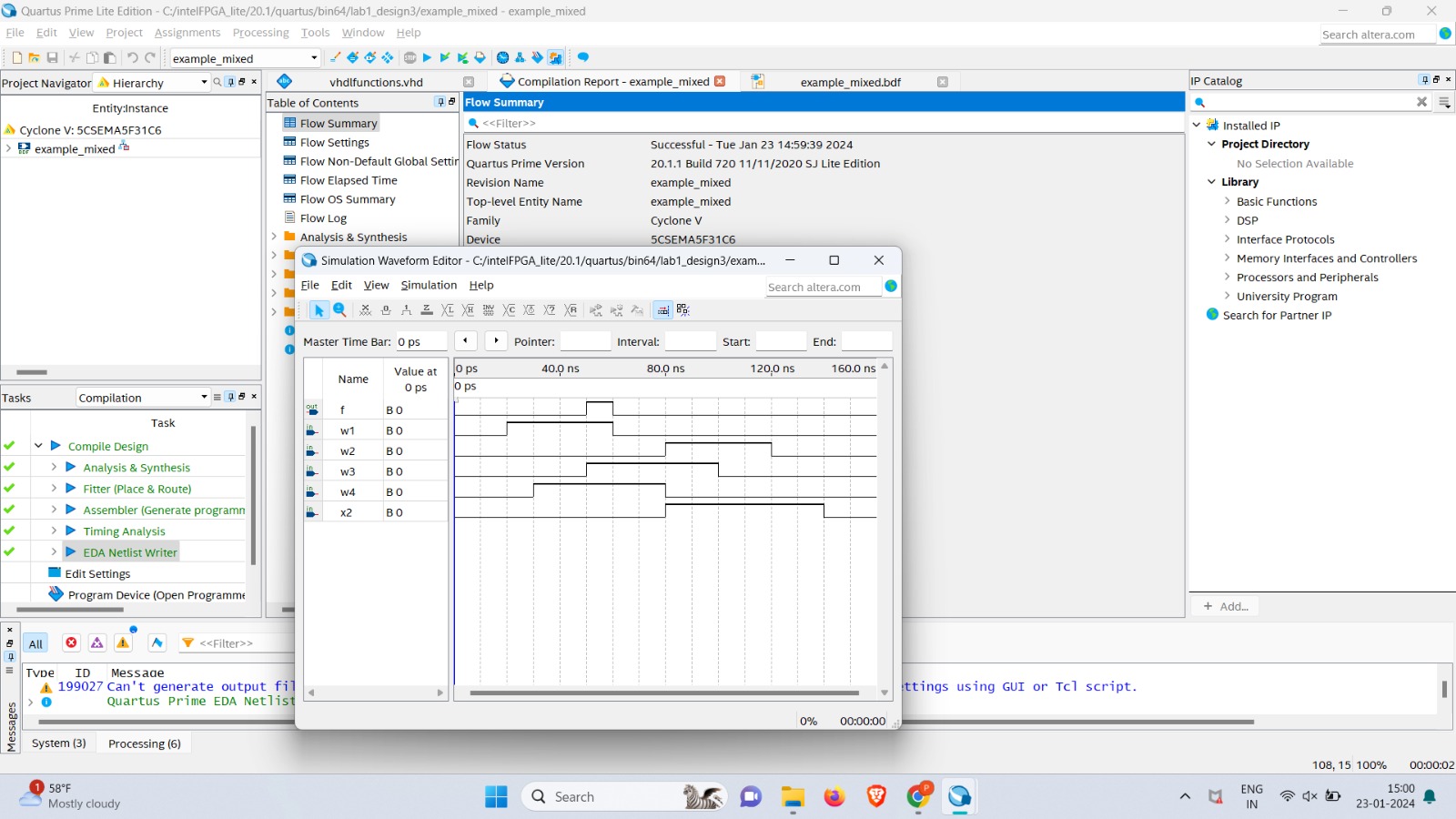


Image 8:- vhdlfunctions.bsf

