

Memory

Collection of hardware elements in a computer into which we store information

Byte-Addressable Memory - each byte has a separate address

CPU -> Memory - during a *write* cycle

Memory -> CPU - during a *read* cycle

Read Only Memory (ROM)

- programmed into device, only allows reads during operation
- *Nonvolatile* - contents remain even if power is removed
- Can quickly read from ROM. Not the case for writing
- Much denser than RAM (Can fit more ROM than RAM on a microcontroller)

Random Access Memory (RAM)

- stores temporary information, allows read/write during operation
- *Volatile* - lost when power removed

Create the bits that make up a register with flip-flops

Value	SI Decimal	SI Decimal	-----	Value	IEC Binary	IEC Binary
1000 ¹	k	kilo-		1024 ¹	Ki	kibi-
1000 ²	M	mega-		1024 ²	Mi	mebi-
1000 ³	G	giga-		1024 ³	Gi	gibi-
1000 ⁴	T	tera-		1024 ⁴	Ti	tebi-
1000 ⁵	P	peta-		1024 ⁵	Pi	pebi-
1000 ⁶	E	exa-		1024 ⁶	Ei	exbi-
1000 ⁷	Z	zetta-		1024 ⁷	Zi	zebi-
1000 ⁸	Y	yotta-		1024 ⁸	Yi	yobi-

Endianness

Big Endian - MSB is stored at lower address

Little Endian - LSB is stored at lower address

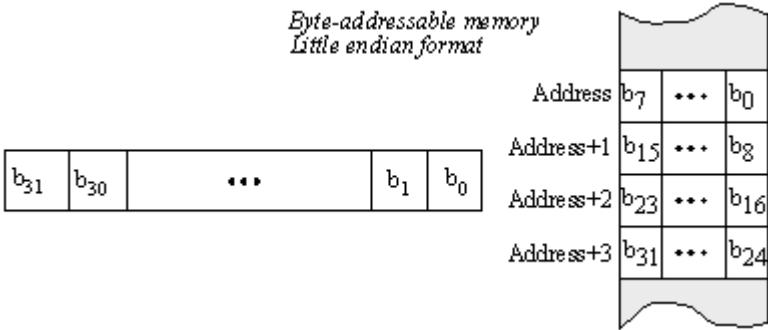
Address	Data
0x2000.0850	0x12
0x2000.0851	0x34
0x2000.0852	0x56
0x2000.0853	0x78

Big Endian

Address	Data
0x2000.0850	0x78
0x2000.0851	0x56
0x2000.0852	0x34
0x2000.0853	0x12

Little Endian

32 bit Number



Little Endian