Extended Syllabus

(2024 1ST Semester)

Course Title	컴퓨터아키텍쳐	Course Number	CSE4010
Credit	3학점	Enrollment Eligibility	3학년
Class Time	수13:30~14:45, 금13:30~14:45	Classroom	추후 공지

	Name:	김주호 (Juho Kim)	Homepage:	cslab.sogang.ac.kr
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Photo	Office: R906 Office Hours: Refer to SAINT office hour (make appointments by e-mail)			

I. Course Overview

1. Description

This course is linked courses of "Introduction of Digital circuit"

The course covers understanding the overall structure of the system, hardware components and memory.

2. Prerequisites

Requires reconfirm following contents of prerequisite lecture "Introduction of Digital Circuit"

- Understanding the computer system's design via the logic circuit that the base of computer system and learning ability to analyze the working principle of real logic circuits.
- Design and combine the each logic circuit.
- Design digital logic circuit that meets the conditions and obtain the desired result with each logic circuit.

3. Course Format (%)

Lecture	Discussion	Experiment/Practicum	Field study	Presentations	Other
100 %	%	%	%	%	%

4. Evaluation (%)

mid-term Exam	า	Final exa	m	Quizzes	Presentations	Projects	Assignments	Participation	Other
30 9	%	40	%	20 %	%	%	10 %	%	%





I. Course Objectives

- 1. Through an understanding of the computer system's basic hardware components such as the CPU, memory, and I/O, acquire the structure and the configuration principle of the computer system to analysis computer architecture.
- 2. Learning ability to design a simple system structure such as CPU, memory, etc.
- 3. Learning practical problem solving methods through the analysis of hardware components
- 4. Developing international capabilities through an English presentation on the study of the latest trends in technology.

ш	Courge	Format

(* In detail)

- Interaction lecture: Communicate with students
- Q&A and Discussions: Lecture to proceed freely with Q&A and discussions

IV. Course Requirements and Grading Criteria

- Examination (3)

Mid-term exam, Final exam, and Quiz

- Assignment
- 1. Assignment (3)
- 2. Solve exercise each chapter
- 3. Show a calculation and solution process when solved exercises.





V. Course Policies

1. Earnest attitude

In every lectures, students have to prepare and review thoroughly. Because the students, that talk, sleep in class and don't take class hard, can interrupt other students and ruin class climate, they must keep from those behavior.

2. Manners preservation

a. A relationship with professor

A relationship between professors and students is not defined as short-term and individual. Then, students should be careful in one's speech and behavior every time and interact others as a member of "Sogang university community" and "Educational specialist community".

b. A relationship with colleague

A relationship with other students in the class is not defined as short-term, temporary and emotional, and students should be careful in one's speech and behavior to be relationship that can encourage and evaluate each other positively.

VI. Materials and References

- Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
- Logic and Computer Design Fundamentals, 2008, M. Mano, Prentice Hall, 4th Edition

VII. Course Schedule

(* Subject to change)

	Learning Objectives	Computer Abstraction and Technology I
	Topics	The trend of computer technology and performance
Week	Class Work (Methods)	Lecture
1	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	





	Learning Objectives	Computer Abstraction and Technology II
	Topics	The trend of computer technology and performance
Week 2	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	Instructions: Language of the Computer I
	Topics	
Week 3	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	Instructions: Language of the Computer II
	Topics	
Week 4	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	Arithmetic for Computers I
	Topics	
Week 5	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition





	Assignments	
	Learning Objectives	Arithmetic for Computers II
	Topics	
Week 6	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	Arithmetic for Computers III
	Topics	
Week 7	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	Mid-Term Exam
	Topics	Mid-Term Exam
Week 8	Class Work (Methods)	Mid-Term Exam
	Materials (Required Readings)	Mid-Term Exam
	Assignments	Mid-Term Exam
Week	Learning Objectives	The Processor I
9	Topics	The structure/operation of data path and control Unit in CPU





	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	The Processor II
	Topics	The Pipelining processing of data path in CPU
Week 10	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	The Processor III
	Topics	At pipelining, methods to solve the hazard
Week 11	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
'	Learning Objectives	Large and Fast: Exploiting Memory Hierarchy I
	Topics	The locality and cache in memory hierarchy
Week 12	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	





	Learning Objectives	Large and Fast: Exploiting Memory Hierarchy II
	Topics	The various structure and performance of cache
Week 13	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	Large and Fast: Exploiting Memory Hierarchy III
	Topics	Virtual memory
Week	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	Storage and Other I/O Topics
	Topics	Storage and IO device
Week 15	Class Work (Methods)	Lecture
	Materials (Required Readings)	Computer Organization and Design, 2010, Patterson and Hennessy, Morgan Kaufmann Pub, 4th Edition
	Assignments	
	Learning Objectives	Final exam
Week 16	Topics	Final exam
	Class Work (Methods)	Final exam





Materials (Required Readings)	Final exam
Assignments	Final exam

. Special Accommodations

Please contact me for some helps if you have some physical problems in taking this course.



