CSE 436/536

Digital Integrated Circuits

Assignment 2

Due Date 06/11/2020 17:00

In this assignment you will design a 2-input NAND gate using Magic Layout Tool and will prove that it Works as a NAND gate using Spice simulator.

For that purpose use the best form of plot in your spice tutorial and record the resultant plot in your report.

The report should contain your magic layout, your extracted Spice circuit and the resultant plot proving the functionality of a NAND gate.

Include your magic file, spice circuit and plot image in the zip file and submit that file to Teams submission page.

Name your zip file as:

StudentName_StudentSurname_StudentId_Assignment2.zip

Be careful in naming conventions. Otherwise you lose 10 points.