

CSE 436/536
Digital Integrated Circuits
Assignment 1
Due Date 28/10/2020 17:00

1. In this assignment you will go through the Spice tutorial and plot every graph inside the tutorial.

The Spice Tutorial is under Files > Class Materials of Teams Class Page.

Take the screenshot of your Spice plots and put them inside a zip file and post that zip file before the due date.

2. Install Magic on your computer and take a screenshot of Magic. Put that in the zip file.
3. Draw the transistor level schematic for the **oai321** logic cell given below:

$$Z = \overline{(A + B + C) \cdot (D + E) \cdot F}$$

Add the screenshot of your schematic to the zip file.

Name your zip file as
StudentName_StudentSurname_StudentId_Assignment1.zip