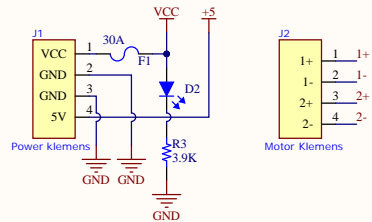
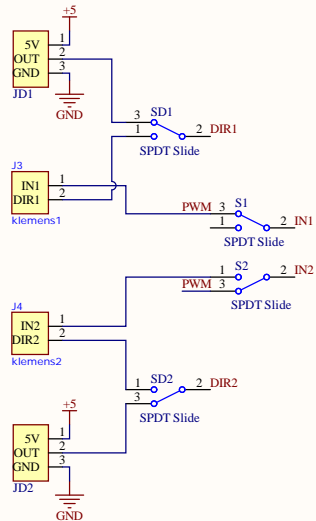


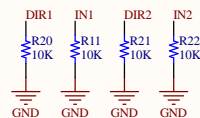
TERMINAL



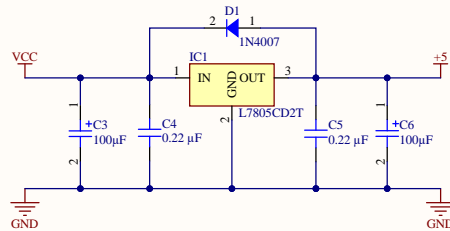
SWITCHES



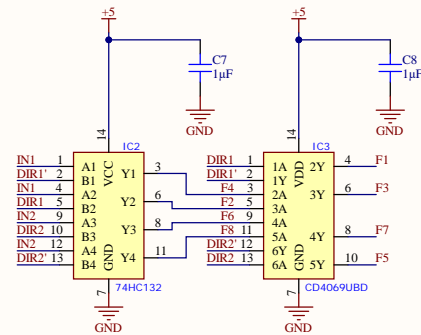
PULL DOWN RES



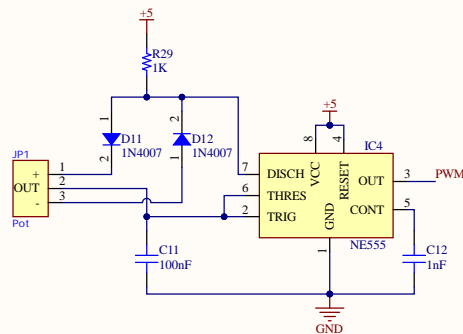
VOLTAGE REGULATOR



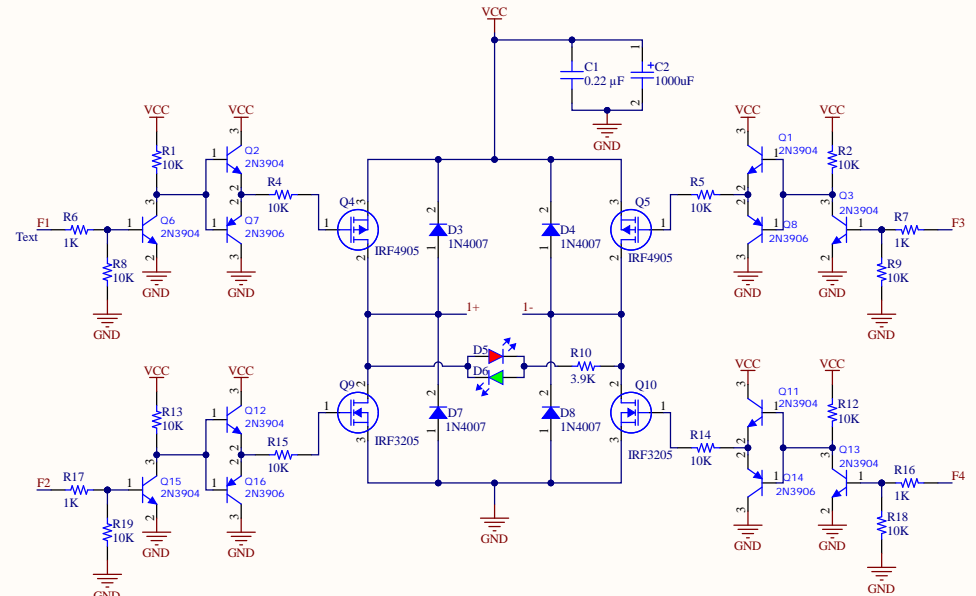
LOGIC GATES



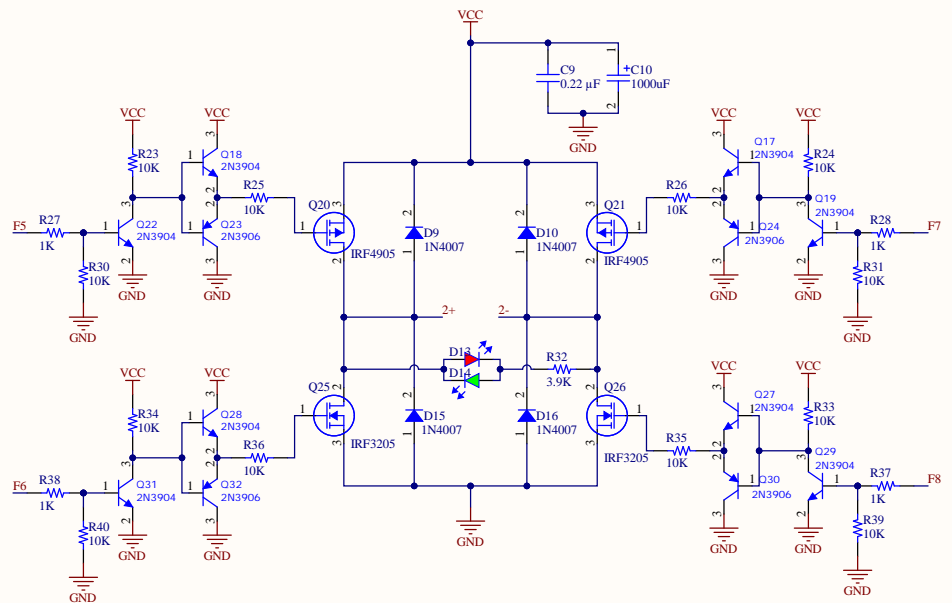
PWM OSCILLATOR



H BRIDGE 1



H BRIDGE 2



Title		
DC MOTOR DRIVERS		
Size	Number	Revision
A3		05/11/2023
Date:	4.18.2024	Sheet of
File:	C:\Users\H_bridge.SchDoc	Drawn By: NEDİM SEZER AYDIN