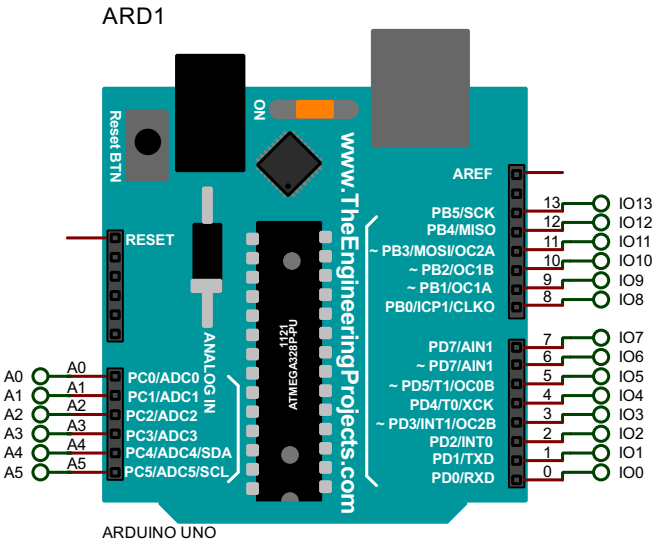
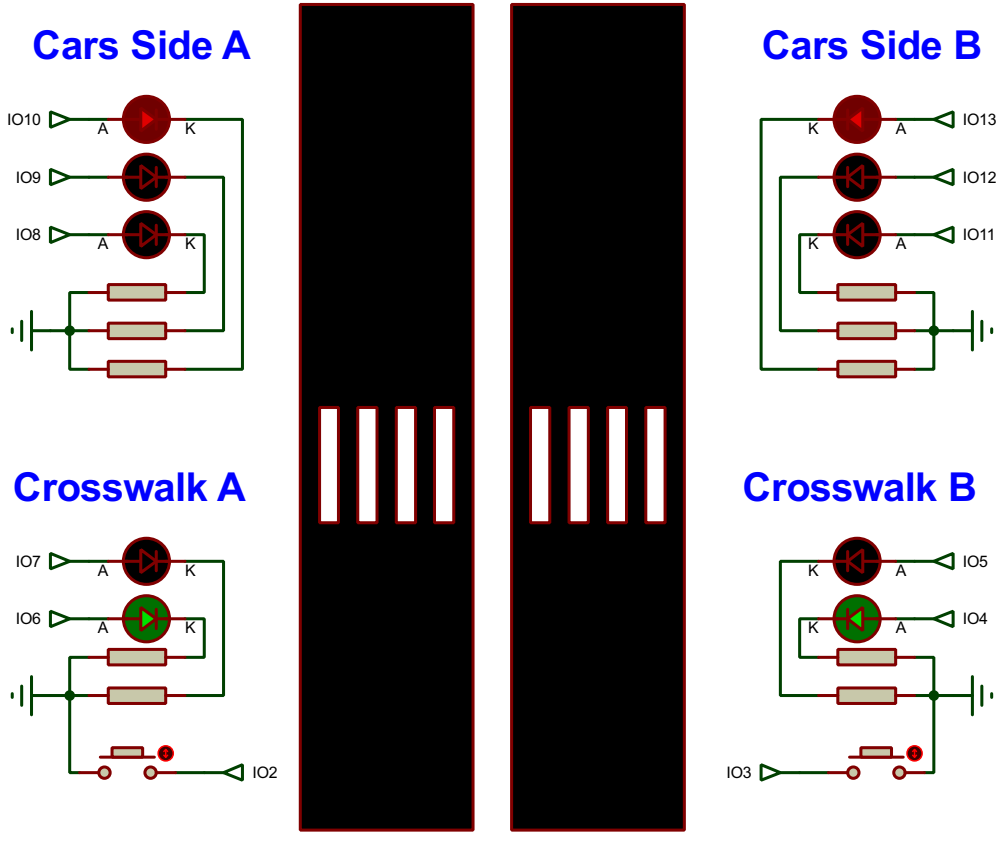

ARD1
Reset BTN
ON
RESET
ANALOG IN
www.TheEngineeringProjects.com
ATMEGA328P-PU
AREF
PB5/SCK
PB4/MISO
~ PB3/MOSI/OC2A
~ PB2/OC1B
~ PB1/OC1A
PB0/ICP1/CLKO
PD7/AIN1
~ PD7/AIN1
~ PD5/T1/OC0B
PD4/T0/XCK
~ PD3/INT1/OC2B
PD2/INT0
PD1/TXD
PD0/RXD
13 IO13
12 IO12
11 IO11
10 IO10
9 IO9
8 IO8
7 IO7
6 IO6
5 IO5
4 IO4
3 IO3
2 IO2
1 IO1
0 IO0
A0 A0
A1 A1
A2 A2
A3 A3
A4 A4
A5 A5
PC0/ADC0
PC1/ADC1
PC2/ADC2
PC3/ADC3
PC4/ADC4/SDA
PC5/ADC5/SCL
ARDUINO UNO


Cars Side A
IO10
IO9
IO8
A
K
Cars Side B
IO13
IO12
IO11
Crosswalk A
IO7
IO6
IO2
Crosswalk B
IO5
IO4
IO3