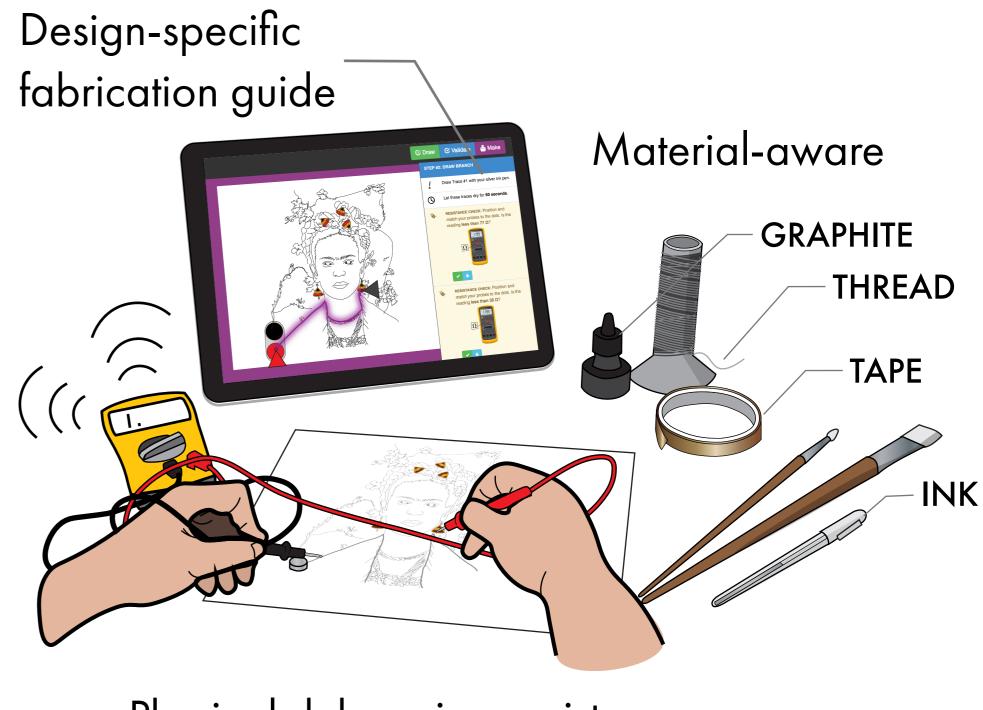


Circuit validation



Physical debugging assistance