Design Rules Verification ReportFilename : C:\Users\Gaming\Documents\TeamPhantom\high_voltage\AIL\AIL_REV2\AIL_REV

Warnings 0 Rule Violations 0

Warnings	
Total	0

Clearance Constraint (Gap=5mil) (IsStitchingVia and InNet(HV_GND')),((IsVia and (Not IsStitchingVia)) Or IsPad) Olearance Constraint (Gap=5mil) (All),(All) Short-Circuit Constraint (Allowed=No) (All),(All) Un-Routed Net Constraint ((All)) Modified Polygon (Allow modified: No), (Allow shelved: No) Width Constraint (Min=7.874mil) (Max=1000mil) (Preferred=16mil) (All) Power Plane Connect Rule(Relief Connect) ((Expansion=11.811mil) (Conductor Width=10mil) (Air Gap=5mil) (Entries=4) Power Plane Connect Rule(Direct Connect) ((Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Power Plane Connect Rule(Direct Connect) ((Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Ole Ninimum Annular Ring (Minimum=3mil) (All) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Ole Hole To Hole Clearance (Gap=9.842mil) (All),(All) Minimum Solder Mask Sliver (Gap=0mil) (All),(All) Ole Silk To Solder Mask (Clearance=5mil) (IsPad),(All) Ole Net Antennae (Tolerance=0mil) (All),(All) Ole Net Antennae (Tolerance=0mil) (All) Ole Net Antennae (Tolerance=0mil) (All) Ole Solder Ole Straint (Gap=0mil) (All) Ole Solder Clearance Constraint (Gap=0mil) (All)		
Clearance Constraint (Gap=5mil) (AII), (AII) Short-Circuit Constraint (Allowed=No) (AII), (AII) Un-Routed Net Constraint ((AII)) Modified Polygon (Allow modified: No), (Allow shelved: No) Width Constraint (Min=7.874mil) (Max=1000mil) (Preferred=16mil) (AII) Power Plane Connect Rule(Relief Connect) (Expansion=11.811mil) (Conductor Width=10mil) (Air Gap=5mil) (Entries=4) Power Plane Connect Rule(Direct Connect) (Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Minimum Annular Ring (Minimum=3mil) (AII) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (AII) Hole To Hole Clearance (Gap=9.842mil) (AII), (AII) Minimum Solder Mask Sliver (Gap=0mil) (AII), (AII) Silk To Solder Mask (Clearance=5mil) (IsPad), (AII) Onumer Solder Mask (Clearance=0mil) (AII), (AII) Onumer Antennae (Tolerance=0mil) (AII), (AII) Onumer Antennae (Tolerance=0mil) (AII), (AII)	Rule Violations	
Short-Circuit Constraint (Allowed=No) (All), (All) Un-Routed Net Constraint ((Allo) Modified Polygon (Allow modified: No), (Allow shelved: No) Width Constraint (Min=7.874mil) (Max=1000mil) (Preferred=16mil) (All) Power Plane Connect Rule(Relief Connect) (Expansion=11.811mil) (Conductor Width=10mil) (Air Gap=5mil) (Entries=4) Power Plane Connect Rule(Direct Connect) (Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Minimum Annular Ring (Minimum=3mil) (All) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Hole To Hole Clearance (Gap=9.842mil) (All), (All) Minimum Solder Mask Sliver (Gap=0mil) (All), (All) Silk To Solder Mask (Clearance=5mil) (IsPad), (All) Olisilk to Silk (Clearance=0mil) (All), (All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Clearance Constraint (Gap=5mil) (IsStitchingVia and InNet('HV_GND')), ((IsVia and (Not IsStitchingVia)) Or IsPad)	0
Un-Routed Net Constraint ((All)) Modified Polygon (Allow modified: No), (Allow shelved: No) Width Constraint (Min=7.874mil) (Max=1000mil) (Preferred=16mil) (All) Power Plane Connect Rule(Relief Connect)(Expansion=11.811mil) (Conductor Width=10mil) (Air Gap=5mil) (Entries=4) Power Plane Connect Rule(Direct Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Minimum Annular Ring (Minimum=3mil) (All) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Hole To Hole Clearance (Gap=9.842mil) (All), (All) Minimum Solder Mask Sliver (Gap=0mil) (All), (All) Silk To Solder Mask (Clearance=5mil) (IsPad), (All) Silk to Silk (Clearance=0mil) (All), (All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All) O	Clearance Constraint (Gap=5mil) (All),(All)	0
Modified Polygon (Allow modified: No), (Allow shelved: No) Width Constraint (Min=7.874mil) (Max=1000mil) (Preferred=16mil) (All) Power Plane Connect Rule(Relief Connect) (Expansion=11.811mil) (Conductor Width=10mil) (Air Gap=5mil) (Entries=4) Power Plane Connect Rule(Direct Connect) (Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Minimum Annular Ring (Minimum=3mil) (All) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Hole To Hole Clearance (Gap=9.842mil) (All), (All) Minimum Solder Mask Sliver (Gap=0mil) (All), (All) Silk To Solder Mask (Clearance=5mil) (IsPad), (All) Silk to Silk (Clearance=0mil) (All), (All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Short-Circuit Constraint (Allowed=No) (All),(All)	0
Width Constraint (Min=7.874mil) (Max=1000mil) (Preferred=16mil) (All) Power Plane Connect Rule(Relief Connect)(Expansion=11.811mil) (Conductor Width=10mil) (Air Gap=5mil) (Entries=4) Power Plane Connect Rule(Direct Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Minimum Annular Ring (Minimum=3mil) (All) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Hole To Hole Clearance (Gap=9.842mil) (All),(All) Minimum Solder Mask Sliver (Gap=0mil) (All),(All) Silk To Solder Mask (Clearance=5mil) (IsPad),(All) Silk to Silk (Clearance=0mil) (All),(All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Un-Routed Net Constraint ((All))	0
Power Plane Connect Rule(Relief Connect) (Expansion=11.811mil) (Conductor Width=10mil) (Air Gap=5mil) (Entries=4) Power Plane Connect Rule(Direct Connect) (Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Minimum Annular Ring (Minimum=3mil) (All) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Hole To Hole Clearance (Gap=9.842mil) (All),(All) Minimum Solder Mask Sliver (Gap=0mil) (All),(All) Silk To Solder Mask (Clearance=5mil) (IsPad),(All) Silk to Silk (Clearance=0mil) (All),(All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All) O	Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Power Plane Connect Rule(Direct Connect) (Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) Minimum Annular Ring (Minimum=3mil) (All) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Hole To Hole Clearance (Gap=9.842mil) (All),(All) Minimum Solder Mask Sliver (Gap=0mil) (All),(All) Silk To Solder Mask (Clearance=5mil) (IsPad),(All) Silk to Silk (Clearance=0mil) (All),(All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Width Constraint (Min=7.874mil) (Max=1000mil) (Preferred=16mil) (All)	0
Minimum Annular Ring (Minimum=3mil) (All) Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Hole To Hole Clearance (Gap=9.842mil) (All),(All) Minimum Solder Mask Sliver (Gap=0mil) (All),(All) Silk To Solder Mask (Clearance=5mil) (IsPad),(All) Silk to Silk (Clearance=0mil) (All),(All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Power Plane Connect Rule(Relief Connect)(Expansion=11.811mil) (Conductor Width=10mil) (Air Gap=5mil) (Entries=4)	0
Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All) Hole To Hole Clearance (Gap=9.842mil) (All),(All) Minimum Solder Mask Sliver (Gap=0mil) (All),(All) Silk To Solder Mask (Clearance=5mil) (IsPad),(All) Silk to Silk (Clearance=0mil) (All),(All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Power Plane Connect Rule(Direct Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4)	0
Hole To Hole Clearance (Gap=9.842mil) (All),(All) Minimum Solder Mask Sliver (Gap=0mil) (All),(All) Silk To Solder Mask (Clearance=5mil) (IsPad),(All) Silk to Silk (Clearance=0mil) (All),(All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Minimum Annular Ring (Minimum=3mil) (All)	0
Minimum Solder Mask Sliver (Gap=0mil) (All), (All) Silk To Solder Mask (Clearance=5mil) (IsPad), (All) Silk to Silk (Clearance=0mil) (All), (All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Hole Size Constraint (Min=11.811mil) (Max=248.031mil) (All)	0
Silk To Solder Mask (Clearance=5mil) (IsPad),(All) Silk to Silk (Clearance=0mil) (All),(All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All)	Hole To Hole Clearance (Gap=9.842mil) (All), (All)	0
Silk to Silk (Clearance=0mil) (All),(All) Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All) 0	Minimum Solder Mask Sliver (Gap=0mil) (All),(All)	0
Net Antennae (Tolerance=0mil) (All) Board Clearance Constraint (Gap=0mil) (All) 0	Silk To Solder Mask (Clearance=5mil) (IsPad),(All)	0
Board Clearance Constraint (Gap=0mil) (All)	Silk to Silk (Clearance=0mil) (All),(All)	0
	Net Antennae (Tolerance=0mil) (All)	0
	Board Clearance Constraint (Gap=0mil) (All)	0
Height Constraint (Min=0mil) (Max=71497.938mil) (Prefered=500mil) (All) 0	Height Constraint (Min=0mil) (Max=71497.938mil) (Prefered=500mil) (All)	0
Total 0	Total	0

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