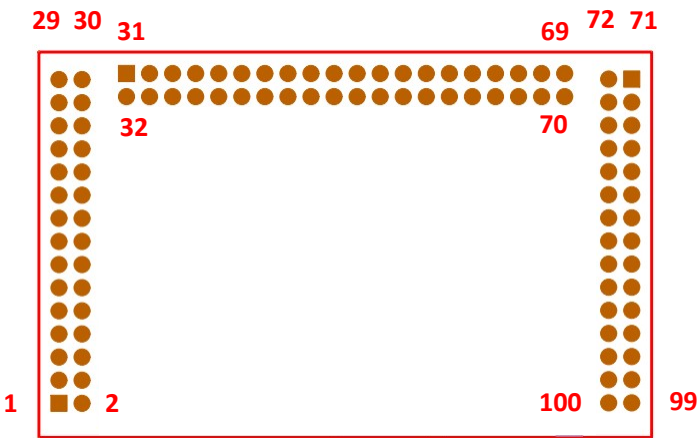
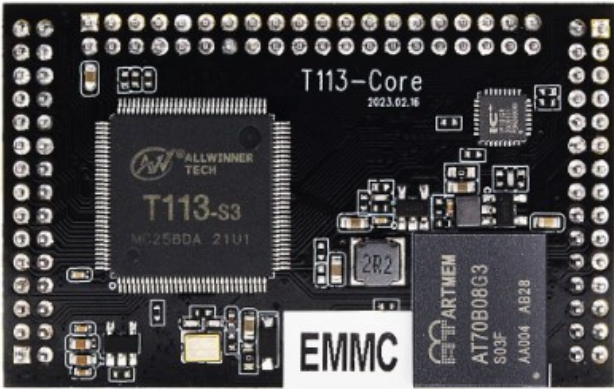


RER-T113-Core 管脚定义

RER-T113-Core 核心板接口采用三组 2.0 双排插针形式,共 100 个引脚,尺寸:53mm*33.4mm



引脚定义表:

(用户可以使用硬件资料里的核心板插座封装原理图和 PCB 做底板设计)

No.	Net Name	No.	Net Name
1	TVIN0	2	TVIN1
3	PG11/TP-RST	4	PG10/TP-INT
5	PG9/UART3-RX	6	PG8/UART3-TX
7	PG7/UART1-RX	8	PG6/UART1-TX
9	PG15/SDA2	10	PG14/SCK2
11	PG13/SDA0	12	PG12/SCK0
13	PG4	14	PG5/PWM4
15	PG3	16	PG0
17	PG2	18	PG1/PWM6
19	GND	20	GND
21	USB0-DP	22	USB0-DM
23	USB1-DM	24	USB1-DP
25	GND	26	GND
27	TP-Y2	28	TP-Y1
29	TP-X2	30	TP-X1
31	GPADC0	32	GND
33	HPOUTR	34	HPOUTL
35	LINEINL	36	LINEINR
37	MIC3N	38	GND
39	GND	40	MIC3P
41	PB2/UART4-TX	42	TVOUT
43	PB3/UART4-RX	44	PB4/UART5-TX/CAN-TX
45	PB5/UART5-RX/CAN-RX	46	PB6/CAN-MODE
47	PB7/LCD-BL-EN	48	LCD-DE/LVDS1-V3N
49	LCD-CLK/LVDS1-V3P	50	LCD-D23/LVDS1-CKN
51	LCD-D22/LVDS1-CKP	52	LCD-D21/LVDS1-V2N

53	LCD-D20/LVDS1-V2P	54	LCD-D19/LVDS1-V1N
55	LCD-D18/LVDS1-V1P	56	LCD-D15/LVDS1-V0N
57	LCD-D14/LVDS1-V0P	58	LCD-D13/LVDS0-V3N
59	LCD-D12/LVDS0-V3P	60	LCD-D11/LVDS0-CKN
61	LCD-D10/LVDS0-CKP	62	LCD-D7/LVDS0-V2N
63	LCD-D6/LVDS0-V2P	64	LCD-D5/LVDS0-V1N
65	LCD-D4/LVDS0-V1P	66	LCD-D3/LVDS0-V0N
67	LCD-D2/LVDS0-V0P	68	LCD-HSYNC
69	LCD-VSYNC	70	LCD-PWM
71	NET-TD+	72	NET-TD-
73	NET-RD+	74	NET-RD-
75	NET-LED0	76	NET-LED3
77	GND	78	GND
79	DC5V	80	DC5V
81	DC5V	82	DC5V
83	GND	84	GND
85	GND	86	GND
87	GND	88	PE12
89	GND	90	PE11
91	RESET	92	FEL
93	GND	94	SDC0-DET
95	SDC0-D2	96	SDC0-D3
97	SDC0-CMD	98	SDC0-CLK
99	SDC0-D0	100	SDC0-D1