

INTRODUCTION TO COMPUTER SYSTEMS

EX. 4 PEN AND PAPER

1 Memory hierarchy

1.1 Basic question

1) Why do memory hierarchies work?

ANS: Because of the Memory Hierarchy, Programs tend to access the data at level k more often than they access the data at level $k+1$. So the storage at level $k+1$ can be slower, larger, cheaper.

2) List three types of cache misses.

ANS: Compulsory Miss, Conflict Miss, Capacity Miss

1.2 Read-Write Transaction

1) Suppose there is no latency, and all processes are executed sequentially without being overlapped. Express the time it takes for memory read transaction and memory write transaction in traditional bus structure with the following given symbols.

Description	Symbol
Time it takes for the value to be placed on the memory bus	t_{place}
Time taken to read the value to the memory bus	t_{read}
Time it takes to place a value in the register	t_{reg}
Time taken to store the read value in Main Memory on the Memory Bus	t_{mem}

Table 1: Descriptions of symbols

ANS: $4 * t_{place} + 4 * t_{read} + t_{reg} + t_{mem}$

1.3 Disk capacity / Access time calculation

1) Calculate disk capacity by using the following information.

<Information of disk>
 - 8 platters per 1 disk
 - 400,000 tracks per 1 disk
 - 20,000,000 sectors per 1 platter
 - 4,096,000,000 bytes per 1 surface
 - 10,000 tracks per 1 surface

ANS: 163.84GB

2) Calculate disk access time by using the following additional information. Assume that the disk has same information of problem 1).

<Additional Information of Disk>
- Rotational rate : 3600RPM
- Average Seek Time : 5ms

ANS: 13.375ms

1.4 Locality

1) Briefly describe temporal locality and spatial locality.

ANS: 1. Temporal Locality : Recently referenced items are likely to be referenced again in the near future.
2. Spatial Locality : Items with nearby addresses tend to be referenced close together in time.

2) Fill in the blank so that the function scans the 4-d array with a stride-1 reference pattern (and thus has good spatial locality)?

ANS: `a[i][j][k][l]`

2 Virtual memory

2.1 VM as a tool for caching

1) Briefly describe why we use Virtual Memory.

ANS: We can use main memory more efficiently by using Virtual Memory. Also, it simplifies memory management and isolates address spaces.

2) The table below is a initial state of the page table. Suppose that the index of physical memory is 0 to 3, and the index of virtual memory is 0 to 7. When a request comes in in the following order, find the page table after the last request is processed.

* when the index of the virtual memory is small, it is assumed that the priority of eviction in the physical memory is high.

** PPX means Index X in physical memory, VPX means Index X in virtual memory

<Request Order>
1. Allocate VP4
2. reference VP2
3. reference VP4
4. reference VP7
5. allocate VP5
6. reference VP5

valid	Physical Page number or disk address
1	PP2
1	PP0
0	VP3
1	PP1
0	null
0	null
1	PP3
0	VP7

Table 2: Page Table

ANS: The answers are the table below.

valid	Physical Page number or disk address
0	VP0
0	VP1
0	VP2
0	VP3
1	PP0
1	PP1
1	PP3
1	PP2

Table 3: Page Table - Answer