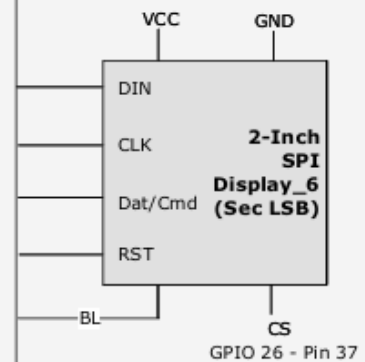
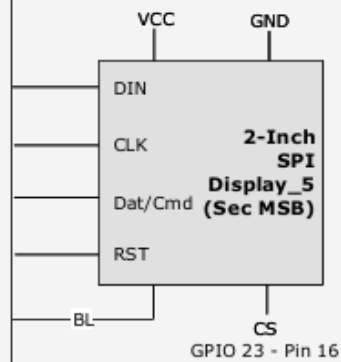
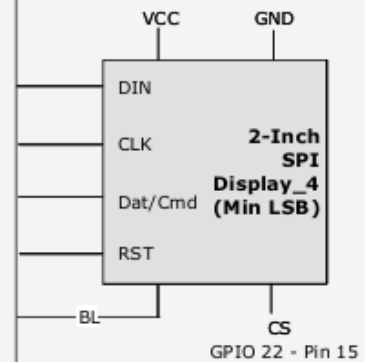
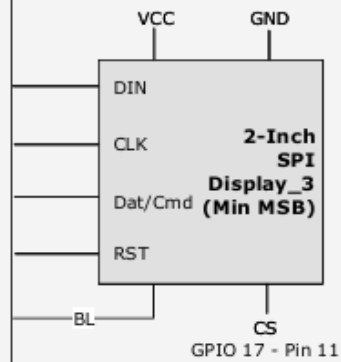
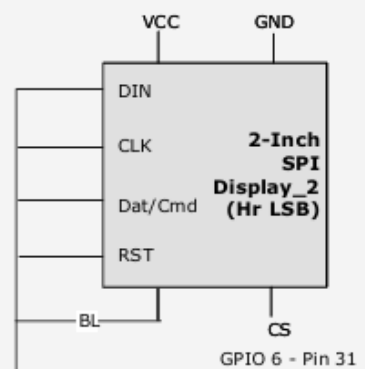
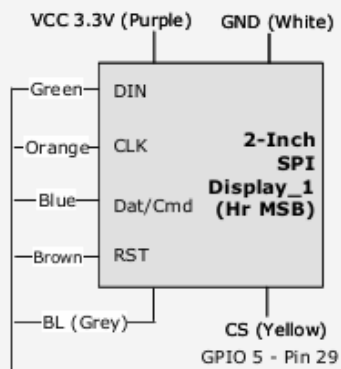
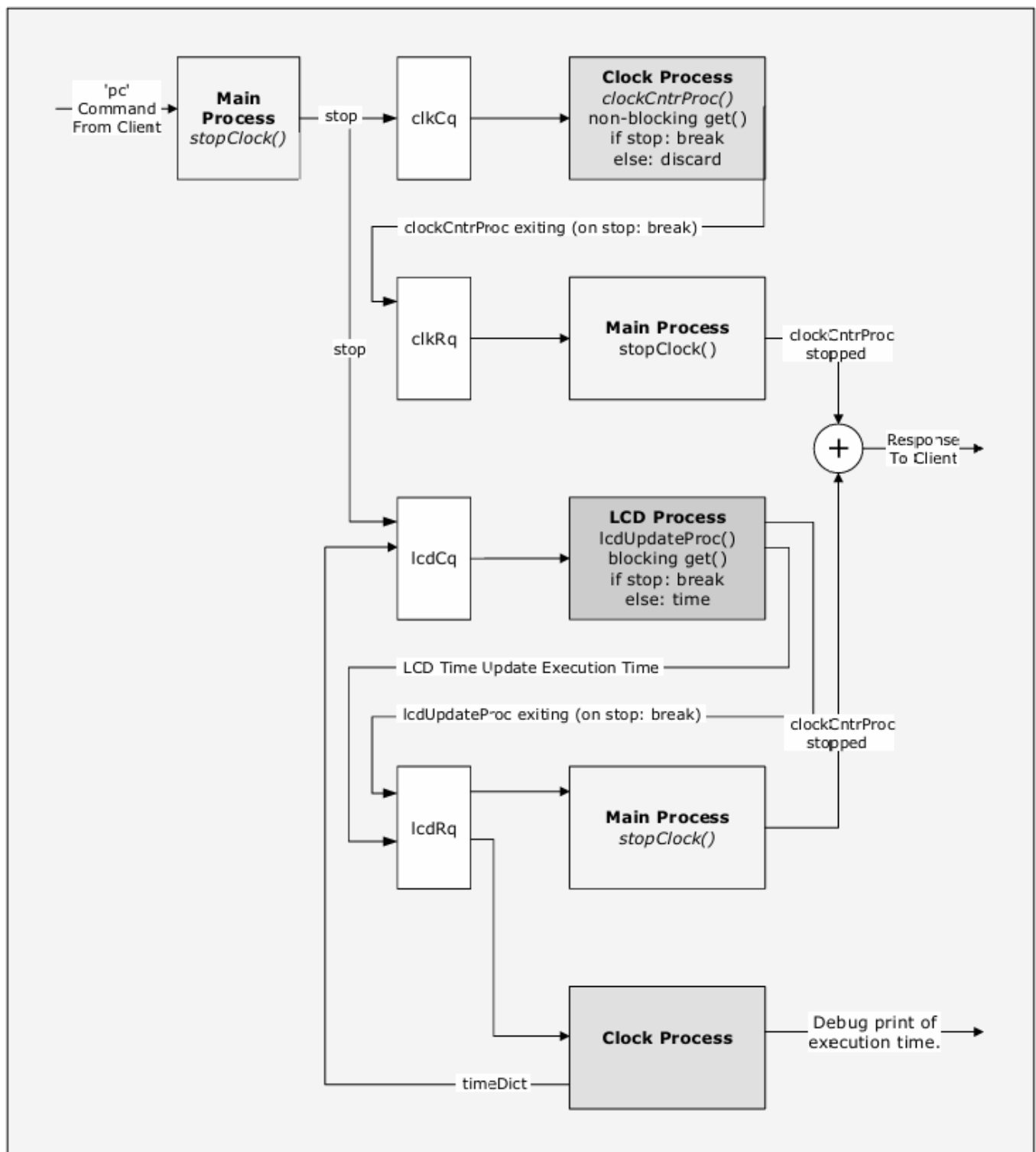


Raspberry Pi 4B

GPIO 18- Pin 12 — BL
MOSI - Pin 19 — DIN
SCLK - Pin 23 — CLK
GPIO 25 - Pin 22 — Dat/Cmd
GPIO 27 - Pin 13 — RST

LCD Display Board





The following will work to allow you to use as many devices as you can find spare GPIO.

Find 8 spare GPIO (i.e. not being used by SPI or anything else in your project). Do not use or connect CE0.

I will refer to them as G1 to G8.

Connect the slave select (or whatever it is called for your chips) of ADC1 to G1, ADC2 to G2, ..., ADC8 to G8.

Connect the other SPI signals (MISO/MOSI/SCLK) in parallel to the ADCs as normal.

Initialise each of G1 to G8 as a high output.

Open the SPI device associated with CE0 (/dev/spidev0.0). Note, nothing should be connected to CE0.

To communicate with ADCx do the following.

1. Set Gx low.
2. Do a SPI transfer to the opened SPI device.
3. Set Gx high.

Repeat as needed for each ADC.

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