

# SOURADIP GHOSH

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## EDUCATION

Carnegie Mellon University

Ph.D in Computer Science

*Advisors:* Brandon Lucia and Nathan Beckmann

Pittsburgh, PA

Aug '21 – Present

Northwestern University

B.A. in Computer Science

*Advisors:* Peter Dinda and Simone Campanoni

Evanston, IL

Sep '17 – Jun '21

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## HONORS AND AWARDS

Department of Energy Computational Science Graduate Fellowship (DOE CSGF)

Sep '21 – Present

NSF REU Fellowship, Northwestern University

Jun '19 – Aug '21

Outstanding Senior in CS, Weinberg School of Arts and Sciences

Jun '21

Outstanding Undergraduate Researcher – Honorable Mention, CRA

Dec '20

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## PUBLICATIONS

1. **Ripple: Asynchronous Programming for Spatial Dataflow Architectures** PLDI '25  
**Souradip Ghosh**, Yufei Shi, Brandon Lucia, Nathan Beckmann.
2. **NUPEA: Optimizing Critical Loads on Spatial Dataflow Architectures via Non-Uniform Processing-Element Access** ISCA '25  
**Souradip Ghosh**, Graham Gobieski, Keyi Zhang, Brandon Lucia, Nathan Beckmann  
Tony Nowatzki.
3. **The TYR Dataflow Architecture: Improving Locality by Taming Parallelism** MICRO '24  
Nikhil Agarwal, Mitchell Fream, **Souradip Ghosh**, Brian C. Schwedock, Nathan Beckmann.
4. **UDIR: Towards a Unified Compiler Framework for Reconfigurable Dataflow Architectures** CAL '24  
Nikhil Agarwal, Mitchell Fream, **Souradip Ghosh**, Brian C. Schwedock, Nathan Beckmann.
5. **Pipestitch: An Energy-Minimal Dataflow Architecture With Lightweight Threads** MICRO '23  
Nathan Serafin, **Souradip Ghosh**, Harsh Desai, Nathan Beckmann, Brandon Lucia.
6. **UDIR: Towards a Unified Compiler Framework for Reconfigurable Dataflow Architectures** WDDSA '23  
Nikhil Agarwal, Mitchell Fream, **Souradip Ghosh**, Brian C. Schwedock, Nathan Beckmann.
7. **RipTide: A Programmable, Energy-Minimal Dataflow Compiler and Architecture** MICRO '22  
Graham Gobieski, **Souradip Ghosh**, Marijn Heule, Todd C. Mowry, Tony Nowatzki,  
Nathan Beckmann, Brandon Lucia.
8. **FPVM: Towards a Floating Point Virtual Machine** HPDC '22  
Peter Dinda, Nick Wanninger, Jiacheng Ma, Alex Bernat, Charles Bernat, **Souradip Ghosh**,  
Christopher Kraemer, Yehya Elmasry.

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| 9. WARio: Efficient Code Generation for Intermittent Computing   | PLDI '22   |
| Vito Kortbeek, <b>Souradip Ghosh</b> , Josiah Hester, Simone Campanoni, Przemysław Pawełczak.  |            |
| 10. CARAT CAKE: Replacing Paging via Compiler/Kernel Cooperation   | ASPLOS '22 |
| Brian Suchy, <b>Souradip Ghosh</b> , Drew Kersnar, Siyuan Chai, Zhen Huang, Aaron Nelson, Michael Cuevas, Gaurav Chaudhary, Alex Bernat, Nikos Hardavellas, Simone Campanoni, Peter Dinda.                           |            |
| 11. NOELLE Offers Empowering LLVM Extensions   | CGO '22    |
| Angelo Matni, Enrico Armenio Deiana, Yian Su, Lukas Gross, <b>Souradip Ghosh</b> , Sotiris Apostolakis, Ziyang Xu, Zujun Tan, Ishita Chaturvedi, Brian Homerding, Tommy McMichen, David I. August, Simone Campanoni. |            |
| 12. Compiler-Based Timing For Extremely Fine-Grain Preemptive Parallelism  | SC '20     |
| <b>Souradip Ghosh</b> , Michael Cuevas, Simone Campanoni, Peter Dinda.   |            |
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## TALKS AND POSTERS

1. NUPEA: Optimizing Critical Loads on Spatial Dataflow Architectures via Non-Uniform Processing-Element Access  
*ISCA*, June '25. Tokyo, Japan.
  2. Ripple: Asynchronous Programming for Spatial Dataflow Architectures  
*PLDI*, June '25. Seoul, South Korea.
  3. Ripple: Asynchronous Programming for Spatial Dataflow Architectures  
*DOE CSGF Program Review*, July '24. Washington, D.C.
  4. Ripple: Asynchronous Programming for Energy-Minimal Edge Devices  
*DOE CSGF Program Review*, July '23. Washington, D.C.
  5. RipTide: A Programmable, Energy-Minimal Dataflow Compiler and Architecture  
*SRC Artificial Intelligence and Hardware Annual Review*, August '22. San Diego, CA.
  6. RipTide: A Programmable, Energy-Minimal Dataflow Compiler and Architecture  
*DOE CSGF Program Review*, July '22. Arlington, VA.
  7. Compiler-Based Timing For Extremely Fine-Grain Preemptive Parallelism  
*SC*, November '20. Virtual.
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## PROFESSIONAL EXPERIENCE

### Graduate Research Assistant

*Computer Science Department, Carnegie Mellon University*

Aug '21 – Present  
Pittsburgh, PA

- *Topic*: Post-von Neumann computer architectures.
- Designing and implementing spatial dataflow architectures and dataflow programming languages.

### Research Intern

*Efficient Computer Company*

May '24 – Present  
Pittsburgh, PA

- Architecting highly energy-efficient and general-purpose dataflow processors.

<b>Visiting Researcher</b> <i>Pacific Northwest National Lab (PNNL)</i>	Jun '23 – Sep '23 Richland, WA
<ul style="list-style-type: none"> <li>Extended the SODA-OPT compiler and HLS toolchains for sparse tensor algebra workloads.</li> <li>Supervised by Antonino Tumeo.</li> </ul>	
<b>Undergraduate Researcher</b> <i>Department of Computer Science, Northwestern University</i>	Jun '19 – Aug '21 Evanston, IL
<ul style="list-style-type: none"> <li>Built optimizing compilers co-designed with operating systems and embedded devices.</li> <li>Contributed to the Interweaving Project, Nautilus, Noelle, TimeSqueezer, and more.</li> </ul>	
<b>Lead Software Developer, IT Manager</b> <i>Karen Lynn + Associates Inc.</i>	Nov '18 – Sep '21 Evanston, IL
<b>Programming Aide</b> <i>Office of Graduate Studies, Department of Computer Science, Northwestern University</i>	Mar '21 – Jun '21 Evanston, IL
<b>Front-End Web Developer</b> <i>Lurie Medical Research Center</i>	Mar '19 – Sep '19 Chicago, IL
<b>Technical Computing Aide</b> <i>IT Department, Kellogg School of Management, Northwestern University</i>	Mar '19 – Jun '19 Evanston, IL

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## TEACHING EXPERIENCE

<b>Graduate Teaching Assistant</b> <i>Department of Computer Science, Carnegie Mellon University</i>	Fall '24 Pittsburgh, PA
<ul style="list-style-type: none"> <li>15-749 – Post-Von Neumann Computer Architectures</li> </ul>	
<b>Student Instructor</b> <i>Department of Computer Science, Northwestern University</i>	Winter '21 Evanston, IL
<ul style="list-style-type: none"> <li>Student-led course – “Crash Course on UNIX and Systems Tools”</li> </ul>	
<b>Peer Mentor</b> <i>Department of Computer Science, Northwestern University</i>	Jan '20 – Present Evanston, IL
<ul style="list-style-type: none"> <li>CS 322 – Compiler Construction, Winter '21</li> <li>CS 323 – Code Analysis and Transformation, Fall '20</li> <li>CS 343 – Operating Systems, Winter '20</li> </ul>	
<b>Academic Mentor – Project Excite</b> <i>School of Education, Northwestern University / Evanston Township High School</i>	Oct '17 – Jun '19 Evanston, IL
<b>Private Tutor</b> <i>Greater Chicago and St. Louis Area</i>	Jun '16 – Present