CS 162 Fall 2021

Operating Systems and Systems Programming

MIDTERM 2

INSTRUCTIONS

This is your exam. Complete it either at exam.cs61a.org or, if that doesn't work, by emailing course staff with your solutions before the exam deadline.

This exam is intended for the student with email address <EMAILADDRESS>. If this is not your email address, notify course staff immediately, as each exam is different. Do not distribute this exam PDF even after the exam ends, as some students may be taking the exam in a different time zone.

For questions with circular bubbles, you should select exactly one choice.

You must choose either this option
Or this one, but not both!

For questions with square checkboxes, you may select multiple choices.

You could select this choice.

You could select this one too!

You may start your exam now. Your exam is due at <DEADLINE> Pacific Time. Go to the next page to begin.

Preliminaries

(a)	Full name
(b)	Student ID number
(c)	Autograder login (e.g. student123)

1. (27.0 points) True or False

Please pick whether each statement is true or false and	explain why. Explanations must be two sentences or less.
You may not use semicolons to circumvent this limit.	Any answer longer than two sentences will receive a 0.

(a)	(3.0	points)

i.	An unsafe state means that a deadlock is guaranteed to happen.						
	○ True						
	○ False						
ii.	Explain.						

(b)	(3.0	points)
	i.	Processes may use the same virtual address but will actually access different physical addresses.
		○ True
		○ False
	ii.	Explain.

(c)	(3.0	points)
	i.	Compulsory cache misses can be fixed by increasing the cache size or associativity.
		○ True
		○ False
	ii.	Explain.

(d)	(3.0	0 points)
	i.	If the goal of a preemptive scheduler is to minimize the response time, it should use SJF.
		○ True
		○ False
	ii.	Explain.

(e)	(3.0	points)
	i.	The main reason why performance degrades when a process thrashes is due to heavy context-switching
		○ True
		○ False
	ii.	Explain.

(f) (3.0	points)
i.	In a one-level paging scheme, a single page table and a single TLB are shared by all processes.
	O True
	○ False
ii.	Explain.

(g)	(3.0	points)
	i.	SRTF scheduler can lead to starvation.
		○ True
		○ False
	ii.	Explain.

(h)	(3.0	points)	
	i.	When a system approaches a steady-state 100% utilization, its queue length will approach in	nfinity.
		○ True	
		○ False	
	ii.	Explain.	

(i) (3.0 points)

i.	When using demand paging, adding more memory to a system will always reduce the number faults.	of page
	○ True	
	○ False	
ii.	Explain.	

2. (18.0 points) Select All

For the following questions, select all that apply. If none should be selected, don't select any. Selecting an incorrect choice or not selecting a correct choice will lead to point deductions, but the overall score for each question will be a minimum of 0.

(a)	(3.0 pt) Which of the following are true about I/O?
()	☐ Direct memory access (DMA) requires the processor to transfer the data between devices and memory.
	☐ A PCI bus (not PCI Express bus) can handle a single request at a time.
	□ Polling is always more efficient than interrupts.
	☐ A blocking I/O operation leads to the process being suspended until the operation completes.
	☐ An asynchronous read operation is blocking.
	The asynchronous read operation is blocking.
(b)	(3.0 pt) Which of the following are true about I/O devices?
	$\hfill \square$ Port mapped I/O interacts with a device via special privileged in/out instructions.
	\square A memory-mapped display controller allows the process to change the image on the screen by using memory store operations.
	\square Seek time is the time it takes for the desired sector to move under the disk head.
	\square Magnetic disks have much faster sequential access than random access.
	$\hfill \Box$ Copy-on-write enable SSDs to reduce the write overhead.
(c)	(3.0 pt) Which of the following are true about deadlock?
(c)	(3.0 pt) Which of the following are true about deadlock?It is possible to have a deadlock when there's only one process in the system, and that process is single threaded.
(c)	☐ It is possible to have a deadlock when there's only one process in the system, and that process is single
(c)	☐ It is possible to have a deadlock when there's only one process in the system, and that process is single threaded.
(c)	 It is possible to have a deadlock when there's only one process in the system, and that process is single threaded. A thread in a critical section cannot be preempted by the OS.
(c)	 □ It is possible to have a deadlock when there's only one process in the system, and that process is single threaded. □ A thread in a critical section cannot be preempted by the OS. □ Banker's algorithm ensures some deadlock-free execution order exists at all times
	 □ It is possible to have a deadlock when there's only one process in the system, and that process is single threaded. □ A thread in a critical section cannot be preempted by the OS. □ Banker's algorithm ensures some deadlock-free execution order exists at all times □ A cycle in a resource allocation graph always implies deadlock. □ Mutual exclusion, hold and wait, no preemption, and circular waiting guarantee that a deadlock will
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	 □ It is possible to have a deadlock when there's only one process in the system, and that process is single threaded. □ A thread in a critical section cannot be preempted by the OS. □ Banker's algorithm ensures some deadlock-free execution order exists at all times □ A cycle in a resource allocation graph always implies deadlock. □ Mutual exclusion, hold and wait, no preemption, and circular waiting guarantee that a deadlock will happen. (3.0 pt) Which of the following are true about demand paging? □ Belady's anomaly states that with certain access patterns, LRU can have a worse hit rate than FIFO.
	 □ It is possible to have a deadlock when there's only one process in the system, and that process is single threaded. □ A thread in a critical section cannot be preempted by the OS. □ Banker's algorithm ensures some deadlock-free execution order exists at all times □ A cycle in a resource allocation graph always implies deadlock. □ Mutual exclusion, hold and wait, no preemption, and circular waiting guarantee that a deadlock will happen. (3.0 pt) Which of the following are true about demand paging? □ Belady's anomaly states that with certain access patterns, LRU can have a worse hit rate than FIFO. □ The working set of a process is the set of memory locations that it accesses throughout its lifetime.

(e)	(3.0 pt) Which of the following are true about synchronization and deadlock?
	\square A deadlock implies starvation but the converse is not true.
	$\hfill \square$ Most modern operating systems use Banker's algorithm to avoid deadlocks.
	$\hfill \square$ A deadlock can occur with resources such as memory or sockets.
	\square A thread can be blocked on more than one condition variable.
	\square A preemptive CPU scheduler would eliminate the possibility of deadlock in the system.
(f)	(3.0 pt) Which of the following are true about schedulers?
` '	☐ FCFS has throughput at least as good as RR.
	\square Gang scheduling is a method to avoid deadlocks by scheduling all threads of a process at once.
	\square Lottery scheduling will not result in starvation in expectation.
	\square EDF schedules the task with the shortest completion time.
	☐ RR is the fairest scheduler with regards to wait time for CPU.

3. (32.0 points	s) Short	Answer
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Answer each question	in three	sentences	or less	unless	otherwise	specified.	You may	not use	semicolons	to
circumvent this limit.	Any ansv	ver longer	than th	ree sen	tences will	receive a	0.			

(40 4) 117	4 : 41 1 :	9 II 1	, •,•				
(4.0 pt) W	nat is thrashing	How do yo	ou prevent it	<u>'</u>			
	terms of speed				vantage and	one disadv	rantage of u
	terms of speed				vantage and	one disadv	rantage of u
					rantage and	one disadv	rantage of u
					vantage and	one disadv	rantage of u
					vantage and	one disadv	rantage of u
					antage and	one disadv	rantage of u
					antage and	one disadv	rantage of u
					vantage and	one disadv	rantage of u
(4.0 pt) In have a lower		pared to a must $N>1$ jobs o	iltilevel page	table?	ive at the sa	ame time,	will FCFS ε
(4.0 pt) In have a lower	a system with a average respon	pared to a must $N>1$ jobs o	iltilevel page	table?	ive at the sa	ame time,	will FCFS ε
(4.0 pt) In have a lower	a system with a average respon	pared to a must $N>1$ jobs o	iltilevel page	table?	ive at the sa	ame time,	will FCFS ε
(4.0 pt) In have a lower	a system with a average respon	pared to a must $N>1$ jobs o	iltilevel page	table?	ive at the sa	ame time,	will FCFS ε
(4.0 pt) In have a lower	a system with a average respon	pared to a must $N>1$ jobs o	iltilevel page	table?	ive at the sa	ame time,	will FCFS ε

(e)	(4.0 pt) Does a direct-mapped cache always have a lower hit rate than a fully associative cache for the same access pattern? Explain.
(f)	(4.0 pt) Explain priority inversion and how to correct a priority scheduler to avoid priority inversion.
(g)	(4.0 pt) Under what scenario is LRU a poor approximation for MIN?
(h)	(4.0 pt) Can a user program unfairly manipulate priority to take advantage of the MLFQ scheduler Explain.

4. (18.0 points) Design Doc Cram

Jieun just realized that her Project 2 design doc is due in 1 hour! She has yet to start on it as s	she's been	busy
releasing her new album. She is panicking, but you've reassured you can help her.		

Fill in the blank for each question with a word or phrase that is the most appropriate based on your design review. Each blank should consist of no more than 10 words. Any answers longer than 10 words will receive a 0

(3.0 pt) Any method that unblocks thread(s) needs to check if the unblocked thread(s) for strict priority scheduler.	
(3.0 pt) To accomplish strict priority scheduling, all scheduling decisions should be made usin of each thread.	g the
(3.0 pt) The data structure for user level locks and semaphores is similar to theProject 1.	from
(3.0 pt) With multiple process-level synchronization primitives, deadlock should be avoided by	
(3.0 pt) When the main thread calls pthread_exit, it must threads.	
(3.0 pt) process_wait and pthread_join need to before blocking on the seman	phore.
	(3.0 pt) To accomplish strict priority scheduling, all scheduling decisions should be made usin of each thread. (3.0 pt) The data structure for user level locks and semaphores is similar to the Project 1. (3.0 pt) With multiple process-level synchronization primitives, deadlock should be avoided by (3.0 pt) When the main thread calls pthread_exit, it must threads.

5. (36.0 points) Replacement O'Clock

Inspired by the scheduling homework, Michael and Nathan decide to implement the clock algorithm. They've started to write the skeleton, but they need your help to finish the rest of it!

You are given the following data structures and methods.

```
struct clock {
                         /* Pintos list of blocks in this clock */
 struct list entries;
 struct entry* curr_entry;
                        /* Current entry the clock hand is pointing to. */
};
struct entry {
                          /* Page this entry holds, -1 if empty or invalid. */
 int page;
 int use;
 struct list_elem elem;
};
/* Evicts CLOCK->CURR_ENTRY. If entry is empty or invalid, does nothing. */
void evict_entry(struct clock* clock);
/* Brings in PAGE but does not update CLOCK->CURR_ENTRY. */
void admit_page(struct clock* clock, int page);
/* Advances CLOCK->CURR_ENTRY to next entry, wrapping around to the first entry if necessary.*/
void advance_clockhand(struct clock* clock);
/* Runs an iteration of the clock algorithm.
  Return 1 on a cache hit and 0 on a miss. */
int clock_iteration(struct clock* clock, int target_page) {
 struct list_elem* e;
 for (_____) {
   struct entry* entry = _____[B]_____;
   if (_____) {
     _____[D]____;
     _____(E]____;
 }
 while (1) {
   _____[F]____;
   if (______) {
     ______[H]_____;
   } else {
     if (_____) {
       _____[J]____;
     _____;
     _____;
     _____[L]____;
     return 0;
 }
 return -1;
```

Your job is to implement clock_iteration. Assume clock and each entry in it are already initialized. clock's capacity must stay fixed (i.e. not allowed to change the number of entries).

You are only allowed to write one line of code per line (fill in **two** lines for [L]). You may not use semicolons to write multiple lines (except for filling in the blank of a **for** loop). You are only allowed to use methods and data structures provided to you in this problem and the reference sheet.

(a)	(2.0 pt) [A]
(b)	(2.0 pt) [B]
(c)	(3.0 pt) [C]
(d)	(3.0 pt) [D]
(e)	(2.0 pt) [E]
(f)	(3.0 pt) [F]
(g)	(3.0 pt) [G]
(h)	(3.0 pt) [H]

(i)	(3.0 pt) [I]
(j)	(3.0 pt) [J]
(k)	(3.0 pt) [K]
(1)	(6.0 pt) [L]

6. (28.0 points) MarcOS

Marcus is building a virtual memory scheme for his new operating system MarcOS. He designs a virtual memory paging scheme using single level page tables with 24-bit virtual addresses, 32-bit physical addresses, 64 KiB page size, and a page table entry of size 4 bytes. Assume each page table fits in a single page and memory addresses are byte-addressed.

1	(a)	6.	0	points)
١	a	 v.	v	pomis

For the following questions, write a single decimal integer. Any answer not matching this format (e.g. using mathematical expressions) will receive a 0.

1.	(2.0 pt) What is the least number of entries necessary per page table?	
	(2.0 pt) How many levels of page tables would be required to map the entire virtual address space the physical addresses?	pace t
iii.	(2.0 pt) How many bits of metadata can you fit in each page table entry?	

(b) (10.0 points)

Suppose the first six page table entries are as follows: 0x123442C3, 0x4320E8C4, 0xAD007DA2, 0x20200D6A, 0x7A511594,0x6000CCE3. Assume all six are valid and the rest of the page table entries are invalid.

For the following questions, write the virtual address that maps to the given physical address as a hexadecimal integer with the 0x prefix (x lowercase) without any leading zeros (e.g. 0x1 not 0x01). Any hex letters must be written in capital letters (e.g. 0xC). If no such virtual address exists, write NONE in capital letters. Any answers not matching this format will receive a 0.

i.	(2.0 pt) 0x20001234
ii.	(2.0 pt) 0x20207A51
iii.	(2.0 pt) 0x1234AD00
iv.	(2.0 pt) 0x2020AD00
v .	(2.0 pt) 0xAD002000

(c) (12.0 poi	nts)
---------------	------

For the following questions, give an explanation in three sentences or less. Any answer longer than three sentences will receive a 0.

oolicy.	tadata bits tha	are specified	lly needed to	implement	the N th chan	ce page repla
Vhat is ONE	cus observes the change he can ver dirty pages?	n make to the				
- /	cus adds a hard ations are requi			~ -	-	
lemory opera						
emory opera						

7. (40.0 points) Hallowbean Party

After a tiring first half of the semester, Sean and Edward decide to throw a large bean potluck party with their CS 162 students in the Wozniak patio.

(a) Edward is organizing the food. However, there are only 3 slots on the table to serve all the bean dishes that CS 162 students have cooked. If a student wants eat a bean dish different than the three currently on the table, they need to ask a TA to make space on the table by paging a dish out to the refrigerator.

Luckily, we studied demand paging during the semester. Therefore, whenever a student would like to eat a dish that is not currently ready to serve, a dish fault (i.e page fault) will occur. The TAs will evict a dish from the table and place it back in the refrigerator. Then, the TA will bring the requested dish from the refrigerator onto the newly freed table space.

To ensure the food is served quickly, Edward asks you to examine different page replacement policies including LRU, MIN, and clock.

Say we have an access pattern of

Pinto, Black, Kidney, Lima, Black, Edamame, Pinto, Kidney, Lima, Pinto

with 3 slots on the table. A chart is given for your convenience using the first letter of each bean.

	Р	В	K	L	В	Е	Р	K	L	Р
1										
2										
3										

For each replacement policy, fill in the slot number each dish requested is placed on (i.e. answer 1, 2, or 3). If a dish is already on the table answer 0.

Then calculate the effective access time (EAT) in seconds to get served a dish assuming it takes 10 seconds to serve a dish on a table and 60 seconds to replace a dish on the table with a dish from the refrigerator. Show your work.

(8.	0 points) LRU
Α.	(0.5 pt) Pinto
в.	(0.5 pt) Black
С.	(0.5 pt) Kidney

D.	(0.5 pt) Lima
Ε.	(0.5 pt) Black
F.	(0.5 pt) Edamame
G.	(0.5 pt) Pinto
н.	(0.5 pt) Kidney
I.	(0.5 pt) Lima
J.	(0.5 pt) Pinto
к.	(3.0 pt) EAT

ii.	(8.	0 points) MIN
	Α.	(0.5 pt) Pinto
	_	
	В.	(0.5 pt) Black
	C.	(0.5 pt) Kidney
	D.	(0.5 pt) Lima
	Ε.	(0.5 pt) Black
	_	
	F.	(0.5 pt) Edamame
	G.	(0.5 pt) Pinto
	н.	(0.5 pt) Kidney
	I.	(0.5 pt) Lima

J.	(0.5 pt) Pinto
K.	(3.0 pt) EAT

	O points) Clock
	ume the clock hand that starts at 3 and moves in the direction of increasing page number. (0.5 pt) Pinto
в.	(0.5 pt) Black
C.	(0.5 pt) Kidney
D.	(0.5 pt) Lima
Ε.	(0.5 pt) Black
F.	(0.5 pt) Edamame
$\mathbf{G}.$	(0.5 pt) Pinto
н.	(0.5 pt) Kidney

I.	(0.5 pt) Lima
J.	(0.5 pt) Pinto
к.	(3.0 pt) EAT

(b) After all the students have enjoyed their beans, Sean decides to host an axe throwing competition. All CS 162 students gather to compete, and the student who hits the target the most number of times will win. To ensure the game is safe, Sean adds several locks to prevent students from throwing the axe until they hold the lock for both the target and the axe. Until then, students remain blocked as they wait until it's safe to throw.

Complete the methods compete and tidy_axes to ensure a safe and deadlock free throwing environment. You are only allowed to write one line of code per line (fill in two lines for [B], [C], [E], [F]). You may not use semicolons to write multiple lines. You are only allowed to use methods and data structures provided to you in this problem and the reference sheet.

```
typedef struct {
 pthread_mutex_t axe_lock;
 pthread_mutex_t target_lock;
} game_data_t;
typedef struct {
 int id;
 int num_targets_hit;
 pthread_t thread;
 game_data_t* game_data;
} thread_data_t;
/* Throws axe and returns 1 if target is hit, 0 otherwise. */
int throw_axe();
/* Sharpens an axe and cleans up the target. */ */
void cleanup();
void* compete(void* arg) {
 _____[A]_____;
 _____[B]____;
 _____[B]____;
 thread_data->num_targets_hit += throw_axe();
 thread_data->num_targets_hit += throw_axe();
 thread_data->num_targets_hit += throw_axe();
 _____[C]____;
 _____[C]_____;
 return NULL;
}
void* tidy_axes(void* arg) {
 _____[D]____;
 _____[E]____;
 _____[E]____;
 cleanup();
 _____[F]____;
 _____[F]____;
```

```
return NULL;
}
int main(int argc, char* argv[]) {
  int N = atoi(argv[1]);
  game_data_t game_data;
  pthread_mutex_init(&game_data.axe_lock, NULL);
  pthread_mutex_init(&game_data.target_lock, NULL);
  thread_data_t thread_data[N];
  for (int i = 0; i < N; i++) {
    thread_data[i].id = i;
    thread_data[i].num_targets_hit = 0;
    thread_data[i].game_data = &game_data;
    pthread_create(&thread_data[i].thread, NULL, compete, &thread_data[i]);
  }
  pthread_t tidy_threads[N / 10];
  for (int i = 0; i < N; i++) {
    pthread_join(thread_data[i].thread, NULL);
    printf("Competitor %d hit %d targets\n", i, thread_data[i].num_targets_hit);
    if (i % 10 == 0) {
      pthread_create(&tidy_threads[i / 10], NULL, tidy_axes, &game_data);
      pthread_join(tidy_threads[i / 10], NULL);
  }
  return 0;
 i. (2.0 pt) [A]
 ii. (3.0 pt) [B]
iii. (3.0 pt) [C]
```

iv.	$(2.0 ext{ pt}) ext{ [D]}$
v.	(3.0 pt) [E]
vi.	(3.0 pt) [F]

8. References

(a) C API

```
/* Semaphore */
int sem_init(sem_t *sem, int pshared, unsigned int value);
int sem_wait(sem_t *sem);
int sem_post(sem_t *sem);
/* Lock */
int pthread_mutex_init(pthread_mutex_t *mutex, const pthread_mutexattr_t *attr);
int pthread_mutex_lock(pthread_mutex_t *mutex);
int pthread_mutex_unlock(pthread_mutex_t *mutex);
/* Condition Variable */
int pthread_cond_init(pthread_cond_t *cond, const pthread_condattr_t *attr);
int pthread_cond_signal(pthread_cond_t *cond);
int pthread_cond_broadcast(pthread_cond_t *cond);
int pthread_cond_wait(pthread_cond_t *cond, pthread_mutex_t *mutex);
/* Process */
pid_t fork(void);
pid_t wait(int *status);
pid_t waitpid(pid_t pid, int *status, int options);
int execv(const char *path, char *const argv[]);
/* Thread */
int pthread_create(pthread_t *thread, const pthread_attr_t *attr,
                    void *(*start_routine) (void *), void *arg);
int pthread_join(pthread_t thread, void **retval);
void pthread_exit(void *retval);
/* High-Level IO */
FILE *fopen(const char *pathname, const char *mode);
int fclose(FILE *stream);
size_t fread(void *ptr, size_t size, size_t nmemb, FILE *stream);
size_t fwrite(const void *ptr, size_t size, size_t nmemb, FILE *stream);
fprintf(FILE * restrict stream, const char * restrict format, ...);
/* Low-Level IO */
int open(const char *pathname, int flags);
int close(int fd);
ssize_t read(int fd, void *buf, size_t count);
ssize_t write(int fd, const void *buf, size_t count);
off_t lseek(int fd, off_t offset, int whence);
int dup(int oldfd);
int dup2(int oldfd, int newfd);
int pipe(int pipefd[2]);
int close(int fd);
/* Socket */
int socket(int domain, int type, int protocol);
int bind(int sockfd, struct sockaddr *addr, socklen_t addrlen);
int listen(int sockfd, int backlog);
int accept(int sockfd, structure sockaddr *addr, socklen_t *addrlen);
int connect(int sockfd, struct sockaddr *addr, socklen_t addrlen);
```

```
ssize_t send(int sockfd, const void *buf, size_t len, int flags);

/* Pintos List */
void list_init(struct list *list);
struct list_elem *list_head(struct list *list);
struct list_elem *list_tail(struct list *list);
struct list_elem *list_begin(struct list *list);
struct list_elem *list_next(struct list_elem *elem);
struct list_elem *list_end(struct list *list);
struct list_elem *list_remove(struct list_elem *elem);
bool list_elem *list_remove(struct list_elem *elem);
bool list_empty(struct list *list);
#define list_entry(LIST_ELEM, STRUCT, MEMBER) ...
void list_insert(struct list_elem *before, struct list_elem *elem);
void list_push_front(struct list *list, struct list_elem *elem);
void list_push_back(struct list *list, struct list_elem *elem);
```

(b) Unit Conversions

Prefix	Factor
Gibi (Gi)	2^{30}
Mebi (Mi)	2^{20}
Kibi (Ki)	2^{10}
milli (m)	10^{-3}
micro (μ)	10^{-6}
nano (n)	10^{-9}

(c) Policy Acronyms

Acronym	Term
SJF	Shortest Job First
SRTF	Shortest Remaining Time First
FIFO	First In First Out
FCFS	First Come First Serve
RR	Round Robin
EDF	Earliest Deadline First
MLFQ	Multi-Level Feedback Queue
LRU	Least Recently Used
MIN	Minimum

No more questions.