MsgStereoSIMD< float16 \_t, float16x4\_t, beliefprop ::kStereoSetsToProcess[0].num \_disp\_vals > MsgStereoSIMD< float16 \_t, float16x4\_t, beliefprop ::kStereoSetsToProcess[1].num \_disp\_vals > MsgStereoSIMD < float16 t, float16x4\_t, beliefprop ::kStereoSetsToProcess[2].num \_disp\_vals > MsgStereoSIMD< float16 \_t, float16x4\_t, beliefprop ::kStereoSetsToProcess[3].num \_disp\_vals > MsgStereoSIMD < float16 t, float16x4\_t, beliefprop ::kStereoSetsToProcess[4].num \_disp\_vals > MsgStereoSIMD< float16 \_t, float16x4\_t, beliefprop ::kStereoSetsToProcess[5].num \_disp\_vals > MsgStereoSIMD< float16 \_t, float16x4\_t, beliefprop ::kStereoSetsToProcess[6].num \_disp\_vals > MsgStereoSIMD< halftype, m128i, beliefprop:: kStereoSetsToProcess[0] .num\_disp\_vals > MsgStereoSIMD< halftype, \_m128i, beliefprop:: kStereoSetsToProcess[1] .num\_disp\_vals > MsgStereoSIMD< halftype, m128i, beliefprop:: kStereoSetsToProcess[2] .num\_disp\_vals > MsqStereoSIMD< halftype, beliefprop\_cpu::MsgStereo m128i, beliefprop:: SIMDProcessing kStereoSetsToProcess[3] .num\_disp\_vals > MsgStereoSIMD< halftype, m128i, beliefprop:: kStereoSetsToProcess[4] .num\_disp\_vals > MsgStereoSIMD< halftype, m128i, beliefprop:: kStereoSetsToProcess[5] .num\_disp\_vals > MsgStereoSIMD< halftype, \_m128i, beliefprop:: kStereoSetsToProcess[6] .num\_disp\_vals > MsgStereoSIMD< halftype, m256i, beliefprop:: kStereoSetsToProcess[0] .num\_disp\_vals > MsgStereoSIMD< halftype, m256i, beliefprop:: kStereoSetsToProcess[1] .num\_disp\_vals > MsgStereoSIMD< halftype, m256i, beliefprop:: kStereoSetsToProcess[2] .num\_disp\_vals > MsgStereoSIMD< halftype, m256i, beliefprop:: kStereoSetsToProcess[3] .num\_disp\_vals > MsgStereoSIMD< halftype,

\_\_m256i, beliefprop:: kStereoSetsToProcess[6] .num\_disp\_vals >

MsgStereoSIMD< halftype,

\_\_m256i, beliefprop:: kStereoSetsToProcess[4] .num\_disp\_vals >