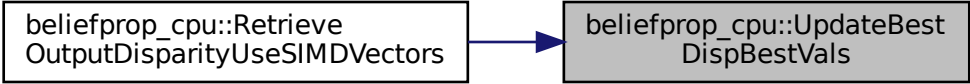


beliefprop\_cpu::Retrieve  
OutputDisparityUseSIMDVectors



```
graph LR; A[beliefprop_cpu::RetrieveOutputDisparityUseSIMDVectors] --> B[beliefprop_cpu::UpdateBestDispBestVals]
```

beliefprop\_cpu::UpdateBest  
DispBestVals