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Design tip

SETUP RIDE7 TO USE ALL RAM AVAILABLE ON THE “STNRG” FAMILY

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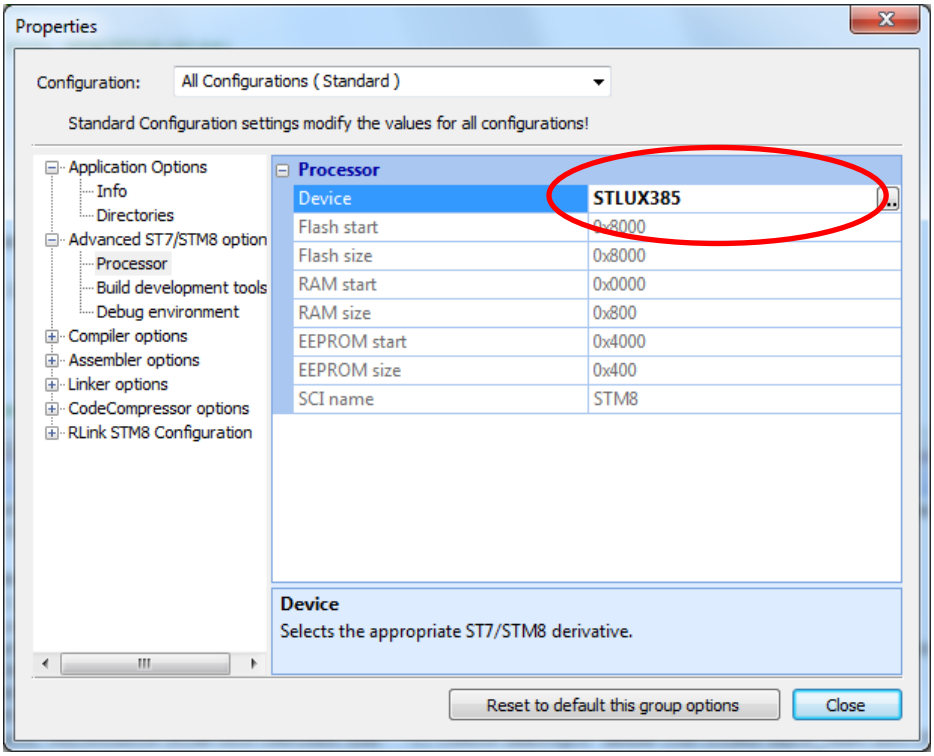
Main components	
STNRG “family”	Digital controller for lighting and power supply applications

Purpose and benefits

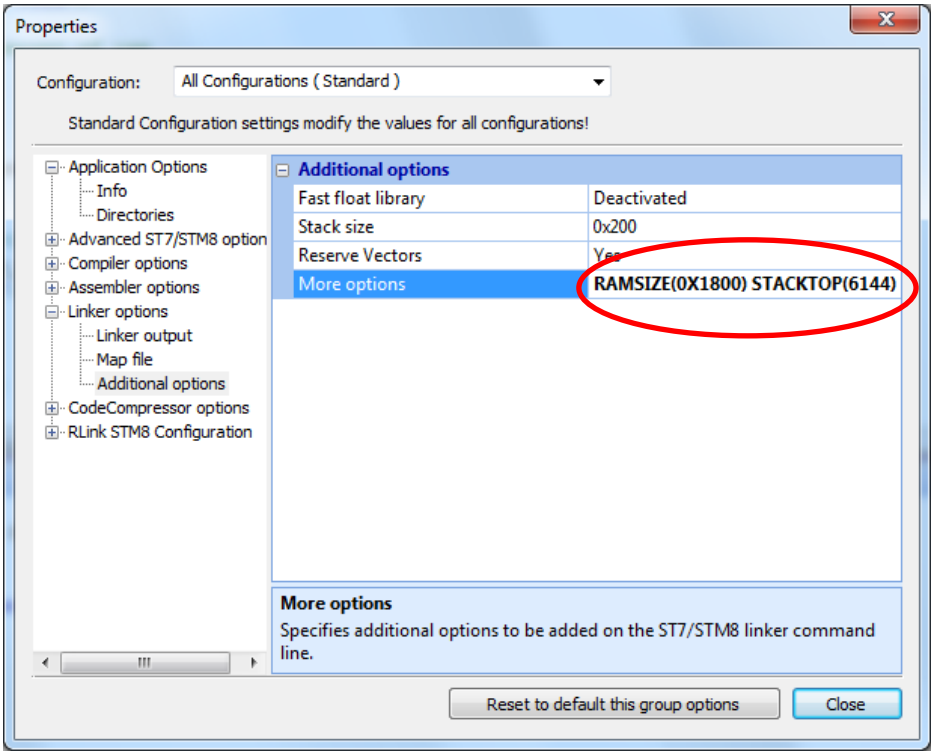
Add STNRG 6Kbyte ram features on Raisonance Ride7 IDE tools

Implementation in the RIDE7

Select STLUX385 chip:



Add the following option on the linker.



Now write a main code like this to setup only the STACK.
NOTE: Do not allocate variable into this code because the stack relocation.

```
// this the normal entry
void main ( void )
{
   // relocate the stack to the end of RAM of STNRG family
   _setSP_(0x17FF);
   main_new();
}
```

Write the new main_new routine. It is possible to use stack and local variable.

```
// This is the new main entry
void main_new( void )
{
// check the amount of ram
u16 l;
u16 limit;
   limit = _getSP_();
   while(1){
      for(l=0;l<((limit/2)-8);l++){
         small_call( l );
         if(l >= ((0x1400/2)-1)){
            nop();
         }
      }
      nop();
   }
}
```

Support material

List any related support material such as evaluation boards, Gerber files etc. and any documents which might be useful to the customer, for example the datasheets, the evaluation board user manual etc.

Related design support material
RIDE7 manual available on Raisonance
Documentation
Datasheet, STNRG,

Revision history

Date	Version	Changes
03-Oct-2016	1	Initial release

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