2022.1 Multicore Computing, Project #4

Problem 2

Document

소프트웨어학부

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**- (a) execution environment (OS/CPU/GPU type or Colab?)**

**I use my Computer to run Project #4 code.**

CPU : AMD Ryzen 5 5600X Six-Core Processor (12 CPUs), 3.7GHz

Memory : DDR4 16384MB RAM

OS : Windows 10

**(b) how to compile**

* thrust\_ex.cu

Use Microsoft visual studio Community 2017 version.

Install cuda 11.7 version

텍스트이(가) 표시된 사진

자동 생성된 설명

Set Windows SDK version to 10.0.17763.0

텍스트이(가) 표시된 사진

자동 생성된 설명

Set CUDA Toolkit Custom Dir like above picture

1>C:\Users\song\source\repos\CUDA 11.7 Runtime1\CUDA 11.7 Runtime1>"..\..\..\..\..\..\Program Files\NVIDIA GPU Computing Toolkit\CUDA\v11.7\bin\nvcc.exe" -gencode=arch=compute\_52,code=\"sm\_52,compute\_52\" --use-local-env -ccbin "C:\Program Files (x86)\Microsoft Visual Studio\2017\Community\VC\Tools\MSVC\14.16.27023\bin\HostX86\x64" -x cu -I"..\..\..\..\..\..\Program Files\NVIDIA GPU Computing Toolkit\CUDA\v11.7\include" -I"..\..\..\..\..\..\Program Files\NVIDIA GPU Computing Toolkit\CUDA\v11.7\include" -G --keep-dir x64\Debug -maxrregcount=0 --machine 64 --compile -cudart static -g -DWIN32 -DWIN64 -D\_DEBUG -D\_CONSOLE -D\_MBCS -Xcompiler "/EHsc /W3 /nologo /Od /Fdx64\Debug\vc141.pdb /FS /Zi /RTC1 /MDd " -o "C:\Users\song\source\repos\CUDA 11.7 Runtime1\CUDA 11.7 Runtime1\x64\Debug\cuda\_ray.cu.obj" "C:\Users\song\source\repos\CUDA 11.7 Runtime1\CUDA 11.7 Runtime1\cuda\_ray.cu"

And compile project like above. Nvcc ~~~ thrust\_ex.cu

**(c) how to execute**

‘thrust\_ex.exe’ (no parameter)

**Entire source code**

#include "cuda\_runtime.h"

#include "device\_launch\_parameters.h"

#include <stdio.h>

#include <stdio.h>

#include <string.h>

#include <stdlib.h>

#include <time.h>

#include <math.h>

#include <omp.h>

#include <cuda.h>

#include <thrust/host\_vector.h>

#include <thrust/device\_vector.h>

#include <thrust/transform.h>

#include <thrust/transform\_reduce.h>

#define CUDA 0

#define OPENMP 1

#define rnd( x ) (x \* rand() / RAND\_MAX)

#define INF 2e10f

#define STEPS 1000000000

#define STEP 1/STEPS

struct saxpy\_functor // define functor to calculate pie

{

    \_\_host\_\_ \_\_device\_\_

        double operator()(const int& x) const {

        double temp = (x + 0.5)\*STEP;

        return (4.0 / (1.0 + temp \* temp));

    }

};

int main()

{

    clock\_t start, end;

    start = clock();

    double x, pi, sum = 0.0;

    thrust::counting\_iterator<int> a(0); //define counting iterator

    double result = thrust::transform\_reduce(a,a+STEPS,saxpy\_functor(), 0.0, thrust::plus<double>()); // reduce all result (add) with functor

    pi = result \* STEP; // calculate pi

    printf("pi=%.8lf\n", pi);

    end = clock();//measure program execution time

    printf("PI Calculation: %lf sec   with step size = %d \n", (double)(end - start) / 1000.0,STEPS );

}

**Program output result**

* thrust\_ex.cu

텍스트이(가) 표시된 사진

자동 생성된 설명

* omp\_pi\_serial.c

텍스트이(가) 표시된 사진

자동 생성된 설명

**Experimental results**

**Thrust\_ex**

|  |  |
| --- | --- |
| Exec time(unit: sec) |  |
|  | 3.844 |

**Omp\_pi\_serial**

|  |  |
| --- | --- |
| Exec time(unit: sec) | 1 |
|  | 2.664 |

**Interpretation/explanation**

We calculated the pie value with n as 1 billion and calculated it through integration using openmp and 'cuda thrust'.

I think the reason why the performance of the open mp is better is because my cpu is the latest model, but I think it came out like this because the gpu is old.

And in the case of cuda thrust, the overhead of moving the value from the host vector to the device vector and moving the computation result back from the device vector to the host vector is too large, so if it is optimized, I think we can reduce the bus bottleneck and speed it up.