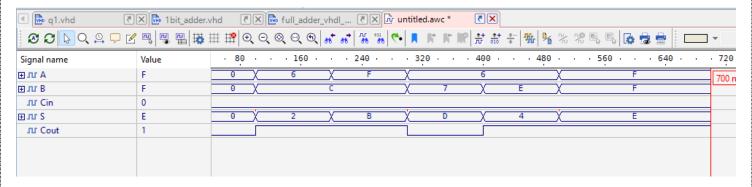
طراحی سیستم های دیجیتال 1402-1403

تمرین 4 سیده شکیبا انارکی فیروز 99442047

(Q1

Waveform:



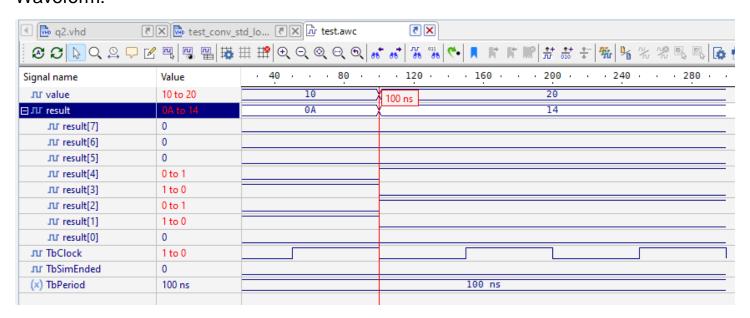
Used links:

https://allaboutfpga.com/4-bit-ripple-carry-adder-vhdl-code/

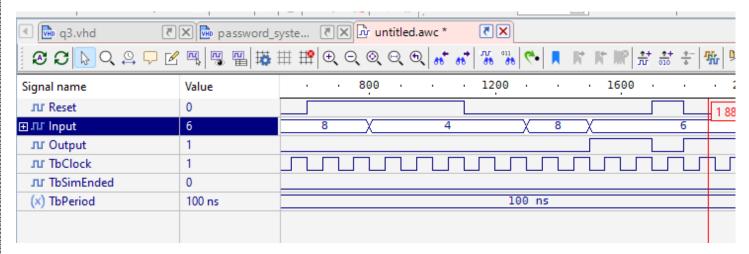
https://allaboutfpga.com/vhdl-code-for-full-adder/

(Q2

Waveform:



Waveform for taking input as array:



Waveform for taking input bit by bit:

