

Problem 1. (10 points):

Consider the source code below, where M and N are constants declared with #define.

```
int mat1[M][N];
int mat2[N][M];

int sum_element(int i, int j)
{
    return mat2[i][j] += mat1[j][i];
}
```

A. Suppose the above code generates the following assembly code:

```
sum_element:
    movslq %edi, %rdi
    movslq %esi, %rsi
    leaq 0(%rdi, 8), %rax
    subq %rdi, %rax
    leaq (%rdi, %rax, 4), %rdx
    leaq (%rsi, %rsi, 8), %rax
    leaq (%rax, %rax, 2), %rax
    addq %rsi, %rdx
    leaq (%rax, %rdi), %rdi
    movl mat2(%rdx, 4), %eax
    addl mat1(%rdi, 4), %eax
    movl %eax, mat2(%rdx, 4)
    ret
```

Handwritten notes for assembly code:

- $\text{return} = 8j$
- $\text{return} = \text{return} - i$ ($\text{ret} = 7i$)
- $\text{rdx} = i + 4 \cdot \text{ret}$
- $\text{ret} = j + (8j)$
- $\text{ret} = (j + 2 \cdot \text{ret})$
- $\text{rdx} = \text{rdx} + j$
- $i = (\text{ret} + j)$
- $\text{ret}[30] = \text{mat1} + (4i)$
- $\text{ret}[30] = \text{ret}[30] + \text{mat1} + 4i$
- $\text{mat2} + 4(\text{rdx}) = \text{ret}[30]$

Handwritten notes for assembly code:

- $\text{rdi} = i$ $\text{rsi} = j$
- rdi rsi rax rdx
- 1: i
- 2: j
- 3: $8j$
- 4: $7i$
- 5: $i + 4(7i)$
- 6: $4j$
- 7: $2(4j) + j$
- 8: $i + 4(7i) + j$
- 9: $2(4j) + i + j$
- $14j + i$ $24j + j$

What are the values of M and N?

M = 29

N = 19

$$\text{mat2}[i][j] = 24j + j$$

$$\text{mat1}[j][i] = 14j + i$$

$$j = 29$$

$$i = 19$$

Problem 2. (10 points):

Consider the following assembly code for a C for loop:

```

decode_me:
    1  cmpl    %esi, %edi
    2  jle     .L5
    3  leal    (%rdi,%rdi), %edx
    4  movl    $1, %eax
    5  subl    %esi, %edx
.L4:
    6  subl    $1, %edi
    7  addl    $4, %esi
    8  addl    %edx, %eax
    9  subl    $6, %edx
   10  cmpl    %esi, %edi
   11  jg      .L4
   12  addl    $46, %eax
   13  ret
.L5:
   14  movl    $47, %eax
   15  ret

```

	y	x	temp	result
1		Compare		
2				
3			2x	
4				1
5			2x - y	
6		x--		
7	y + 4			
8				result + temp
9			temp - 6	
10		Compare		
11				
12				result + 46
13				
14				result = 47
15				

Based on the assembly code above, fill in the blanks below in its corresponding C source code. (Note: you may only use the symbolic variables x, y, and result in your expressions below — do not use register names.)

```

int decode_me(int x, int y)
{
    int result;

    for (result = 1; y < x; x--, y = y + 4 ) {
        result += (2x - y);
    }

    return result + 46;
}

```

$a=rdi$ $b=rsi$ $b^*=rax$ $temp=rax$

Problem 3. Stack Discipline (20 points)

Examine the following recursive function:

```
long sunny(long a, long *b) {
    long temp;
    if (a < 1) {
        return *b - 8;
    } else {
        temp = a - 1;
        return temp + sunny(temp - 2, &temp);
    }
}
```

Here is the x86_64 assembly for the same function:

```
0000000000400536 <sunny>:
400536: test    %rdi,%rdi
400539: jg      400543 <sunny+0xd>
40053b: mov     (%rsi),%rax
40053e: sub     $0x8,%rax
400542: retq
400543: push    %rbx           jvc prev rax
400544: sub     $0x10,%rsp
400548: lea     -0x1(%rdi),%rbx
40054c: mov     %rbx,0x8(%rsp)
400551: sub     $0x3,%rdi
400555: lea     0x8(%rsp),%rsi
40055a: callq   400536 <sunny>
40055f: add     %rax,%rax
400562: add     $0x10,%rsp
400566: pop     %rbx
400567: retq
```

Breakpoint

We call `sunny` from `main()`, with registers `%rsi = 0x7ff...ffad8` and `%rdi = 6`. The value stored at address `0x7ff...ffad8` is the long value 32 (0x20). We set a breakpoint at "`return *b - 8`" (i.e. we are just about to return from `sunny()` without making another recursive call). We have executed the `sub` instruction at `40053e` but have not yet executed the `retq`.

Fill in the register values on the next page and draw what the stack will look like when the program hits that breakpoint. Give both a description of the item stored at that location and the value stored at that location. If a location on the stack is not used, write "unused" in the Description for that address and put "----" for its Value. You may list the Values in hex or decimal. Unless preceded by `0x` we will assume decimal. It is fine to use `f...f` for sequences of `f`'s as shown above for `%rsi`. Add more rows to the table as needed. Also, fill in the box on the next page to include the value this call to `sunny` will finally return to `main`.

	a rdi	b rsi	temp rbx	b* rax
1	6	factg	4	32
2				24
3				
4			5	
5				
6	3			
7		fact 0		
8				
9				
10				

² Sunny (3, fact 0)

	a rdi	b rsi	temp rbx	b* rax
1	3	factg		5
2				-3
3				
4			2	
5				
6	0			
7		fact 0		
8				
9				
10				

³ Sunny (0, fact 0)

	a rdi	b rsi	temp rbx	b* rax
0	0	fact 0		2
				-6

Register	Original Value	Value at Breakpoint
rsp	0x7ff...ffad0	0x7f...fa90
rdi	6	0
rsi	0x7ff...ffad8	0x7f...fa90
rbx	4	2
rax	5	-6

DON'T FORGET

What value is finally returned to **main** by this call?

1

push
mov

callq

push

mov

callq

Memory address on stack	Name/description of item	Value
0x7fffffffffffffad8	Local var in main	0x20
0x7fffffffffffffad0	Return address back to main	0x400827
0x7fffffffffffffac8	rbx (saved)	4
0x7fffffffffffffac0	temp	5
0x7fffffffffffffab8	~~~~~	~~~~~
0x7fffffffffffffab0	return address	0x40055f
0x7fffffffffffffaa8	rbx (saved)	5
0x7fffffffffffffaa0	temp	2
0x7fffffffffffffa98	~~~~~	~~~~~
0x7fffffffffffffa90	return address	0x40055f
0x7fffffffffffffa88		
0x7fffffffffffffa80		
0x7fffffffffffffa78		
0x7fffffffffffffa70		
0x7fffffffffffffa68		
0x7fffffffffffffa60		

Problem 4. (4+6 points):

A. Consider an implementation of a processor where the combinational circuit latency is α ns (nanosecond). You are going to pipeline this implementation, and in your system the latency of each pipeline register is β ns. If you are to divide this entire combinational circuit in k stages, what is the throughput of your pipelined implementation? What about the total latency of a single instruction?

$$\text{throughput: } \frac{1}{(\beta + \alpha k)}$$

$$\text{latency} = \beta$$

$$k \text{ stages}$$

$$\text{Comb} = \alpha$$

$$\text{Latency: } k\beta + \alpha$$

B. Now, assume that the parameters α , β and k described above have the following relation:

$$\beta = \begin{cases} \alpha/10 & \text{if } k \leq 5, \\ \alpha/10 + k/50 \cdot 9\alpha/10 & \text{if } k > 5. \end{cases} \quad (1)$$

Based on the equation above, clearly present an analysis when would you pipeline this processor. Please consider three different values of k in your analysis: (i) $k = 5$; (ii) $k = 50$; and (iii) $k = 500$.

pipeline when $\beta < \alpha$

$k=5$: pipeline

$$\beta = \frac{\alpha}{10} = 0.1\alpha$$

$k=50$: don't pipeline

$$\beta = \frac{\alpha}{10} + \frac{50}{50} \cdot \frac{9\alpha}{10} = \frac{\alpha}{10} + \frac{9\alpha}{10} = \frac{10\alpha}{10} = \alpha$$

$k=500$: don't pipeline

$$\beta = \frac{\alpha}{10} + \frac{500}{50} \cdot \frac{9\alpha}{10} = \frac{\alpha}{10} + 9\alpha = 9.1\alpha$$