

Name: Riaz Mehadi

ID : 1931746042

ALUOP		Functional field		ALUOP
Binvert	Ainvert	F ₁	F ₀	O ₂ O ₁ O ₀
1	0	0	0	010 (add)
1	0	1	0	110 (Sub)
1	1	1	0	011 (Multiply)
0	0	0	1	000 (and)
0	0	1	0	001 (or)
0	0	1	1	111 (x-or)

K-map for O_0

BA \ Ff0	00	01	11	10
00	X	0	1	1
01	X	X	X	X
11	X	X	0	1
10	0	X	X	0

Let $B_{in} = A, A_{in} = \bar{B}, F_0 = D$

Group (2, 3, 6, 7)

(4, 6, 12, 14)

$$O_0 = \bar{A}C + B\bar{D}$$

K-map for O_1

BA \ Ff0	00	01	11	10
00	X	0	1	0
01	X	X	X	X
11	X	X	0	1
10	1	X	X	1

Let

$B_{in} = A, A_{in} = B$

$F_0 = E, F_1 = D$

Group = (8, 10, 12, 14)

(3, 7)

$$O_1 = A\bar{D} + \bar{A}CD$$

Q₂ - K-map

Let Bin = A, Ain = B
F₁ = C, F₀ = D

BA \ F ₁ F ₀	00	01	11	10
00	X	0	1	0
01	X	X	X	X
11	X	X	1	0
10	0	X	X	1

Group (3, 7, 11, 15)
(10, 11)

$$O_2 = A\bar{B}C + CD$$

Discussion:

In this project, I make 32 bit ALU. Firstly, I make 16 bit project which is 16 bit. Then I make it 32 bit by cascading two 16 bit ALU.

For making 16 bit ALU, I have to use three 16 bit ie which I make in

the lab 1, 2, and 3. One is ^{for} multiplier which is 8 bit. The rest of the two is Arithmetic and Logic unit. In Arithmetic unit, I use adder for doing add and subtraction. In the logic unit is, I use AND, OR, X-OR Gate for doing these operation.

Then I design a ALU control unit which is mainly use for controlling the whole project. I make ALU control unit by equation of 0_0 , 0_1 and 0_2 which I got from the K-map. I use splitter for merging the the three equation in one output. Then I make a ic of ALU control unit. Then I use it for run the project smoothly.