

0001 0001 0000 0001
 0001 0001

0001 0010 0000 0001
 ←————→

0000 0000

0001 0011 0000 0011

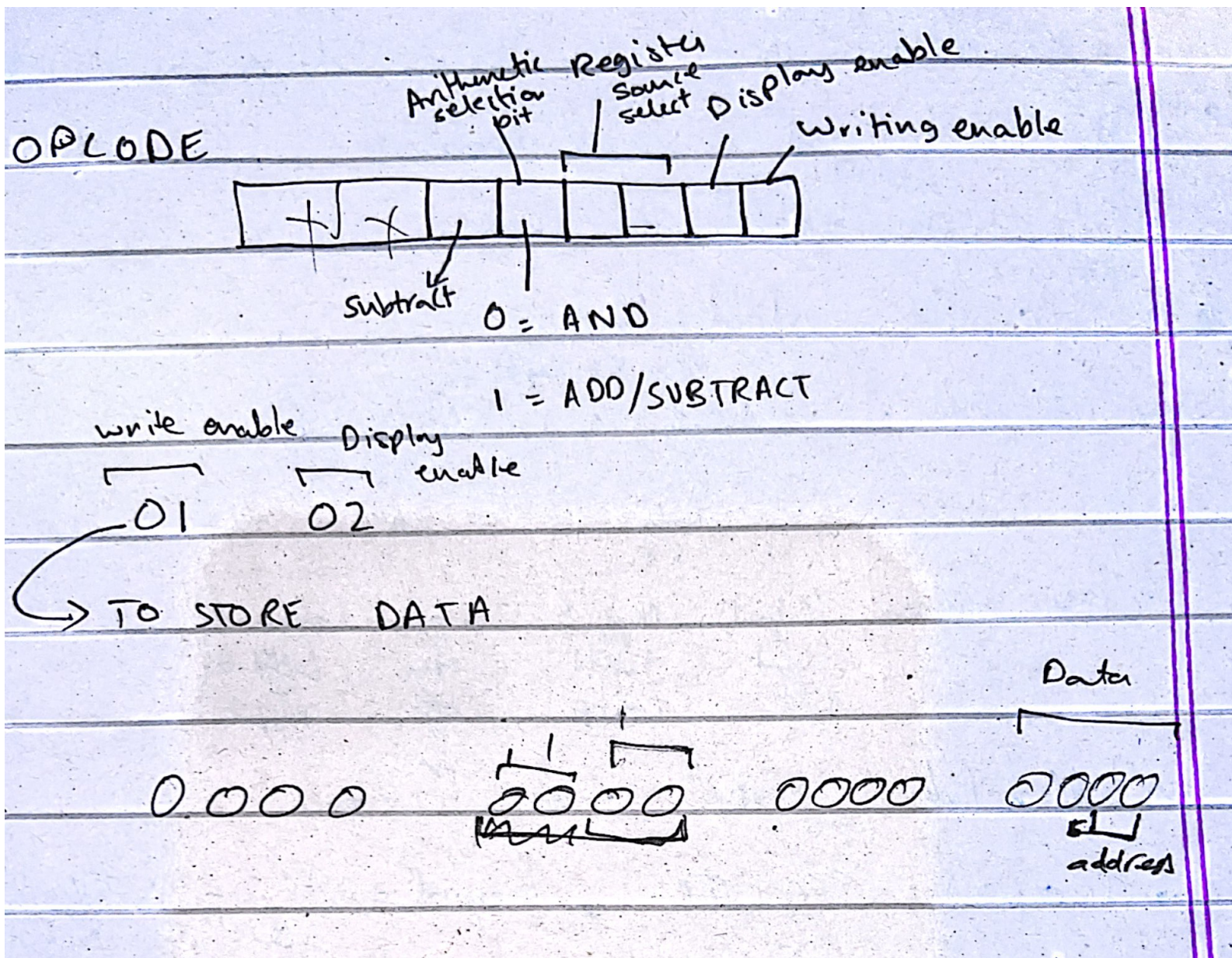
0001 0000 0010 0011
 A B

000

0000 0000

00

↳ And in AFS



PROM bits

0-1 Add B

2-3 Data

4-5 Address A

8-9 Address Data

12-15 Opcode

OpCode - bits

0 → Writing enable

1 → Display enable

2-3 → Register source select

4 → ASB

5 → Subst.