

OPCODE for PROM.

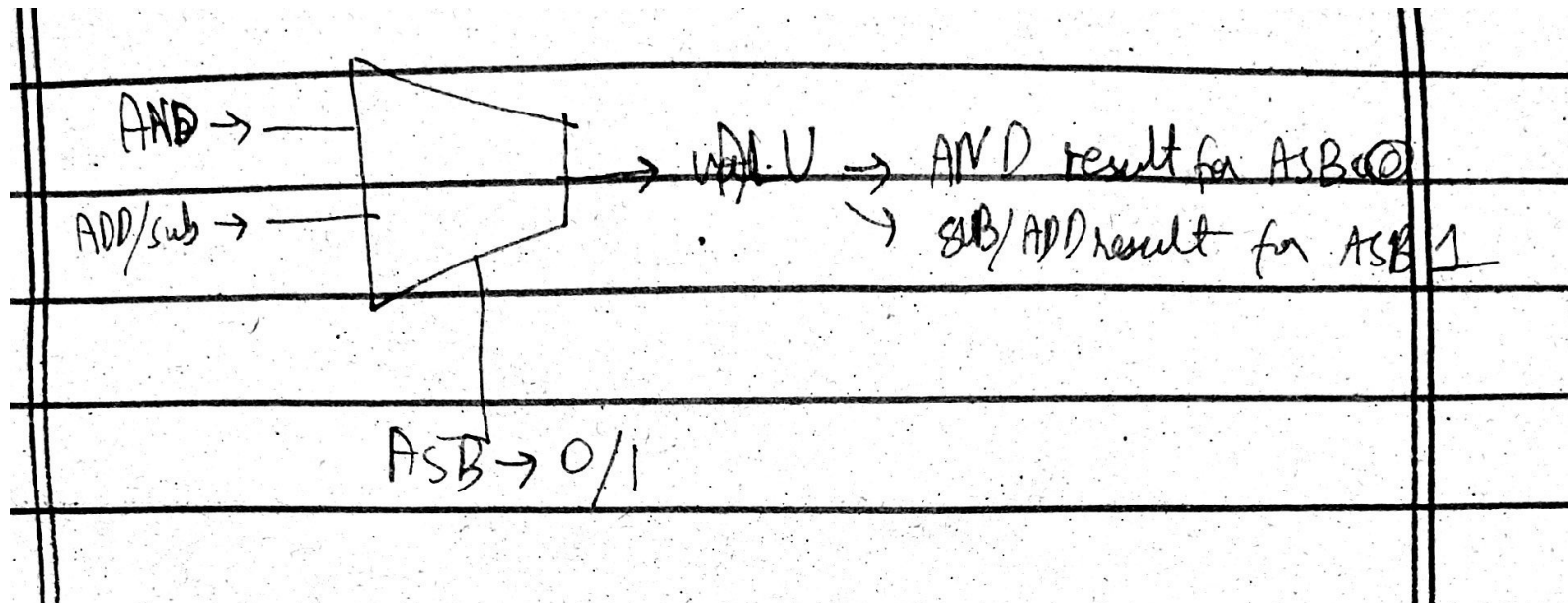
1st instruction  $\rightarrow 00 \rightarrow$  nothing on.

2nd : 01  $\rightarrow 0000\ 0001 \rightarrow$  so writing enable on

3rd : 09  $\rightarrow 0000\ 1001 \rightarrow$  Register source select  
and writing enable on. Register source

4th : 02  $\rightarrow 0000\ 0010 \rightarrow$  Display enable on.

Data stored will now be displayed.



→ IN PROM.

→ ~~First~~<sup>12<sup>th</sup></sup> bit for opcode.

eg if first bit 1, then sup on 1st bit of instruction PkOn.

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IN PROM	OPCODE	data address	data
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IN PROM	OPCODE	data address	data
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IN PLOM OILCODE Dabaldem Pater

→ if 1000 → 50 0001 0000 0000 0000

→ if 1000 → 50 0001 0000 0000 0000

→ if 1000 → 50 0001 0000 0000 0000 0000  
address

→ if 1000 → 50 0001 0000 0000 0000 0000  
~~1000 0000~~  
 address A address B

→ if 1000 → 52 0001 0000 0000 0000 0000  
~~0001 0000~~  
 address  
 A  
 address  
 B