Alfred V Aho, M S. Lam, R Sethi, Jeffrey D. Ullman. Compilers-Principles, Techniques and Tools, Pearson book:

Ch-1, 3 and 4 complete.

CH-5

Introduction,

5.1- Syntax-Directed Definitions

5.2- Evaluation Orders for SDD's

5.4- Syntax-Directed Translation Schemes,

5.4.1- Postfix Translation Schemes

Examples solved in class and assignments

CH-6 Intermediate-Code Generation Introduction
6.1 Variants of Syntax Trees
6.1.1 Directed Acyclic Graphs for Expressions (Exercise 6.1.1, 6.1.2)
6.2 Three Address Code (complete) expressions, if-else, switch case, procedure calls and definitions, for, while, do-while loop, one-dimensional array, pointer assignments – Refer all constructs covered in class

Ch-8 Code generation
Introduction
8.1 Issues in the design of a code generator
8.2The target language (prepare instruction set of any one assembly language- 8086 preferred)
8.4 Basic blocks and flow graphs- complete
8.5 Optimization of basic blocks
8.6 A simple code generator- generate target code for basic constucts
8.7 Peephole optimization

Ch-9 Machine-Independent optimizations
Introduction
9.1 Complete

Dhamdhere book:

Ch-4 Assemblers (pdf uploaded in shared drive)

LOADER AND LINKER:

Loaders and Linkers: Basic Loader Functions, Machine Dependent Loader Features, Machine Independent Loader Features, Loader Design Options, Implementation Examples.