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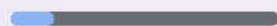
Q1

A computer system has a cache memory of 4KB with 4-way set associative mapping having 4 blocks per set and 128 Bytes per block. Calculate number of sets (write only the required number). *

Your answer

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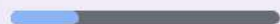
Q2

If you are using a 32KB direct mapped cache memory with a block size of 32 B, ^{*} then calculate the number of bits required to represent total no. of blocks (write only the required number).

Your answer

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Q4

A primary memory system having a size of 1 GB is prepared with some memory chip of 128 MB. Suppose you are trying to design this memory system with the help of a decoder to select any one of the RAM chip. How many pins from the address bus should be used as the decoder input? (Write only the required number) *

Your answer

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Q5

Considering R1 as a register, the instruction, **Add #45, R1** does which one of the followings: *

- ☐ Adds the value 45 to the address of R1 and stores 45 in that address
- ☐ Adds 45 to the value of R1 and stores it in R1
- ☐ Finds the memory location 45 and adds that content to that of R1
- ☐ None of the options are correct

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Q6

In a microprogrammed control unit the Opcode for current instruction is provided *
by

- ☐ Control Data Register
- ☐ Flag Register
- ☐ Program Counter
- ☐ Instruction Register

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Q7

In a S-R flip flop if you disable the set and enable the reset then the output will be

Your answer

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Q8

If a Digital Computer has a memory unit of $32K \times 32$ and a cache memory of $4K$ words then find the number of address lines in the Cache memory. *

Your answer

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Q9

A Clocked-Latch is an *

- ☐ (a) Synchronous Device
- ☐ (b) Edge sensitive Device
- ☐ (c) Both (a) and (b)
- ☐ None of the given options are correct

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Q10

Suppose you are trying to perform the flip flop conversion. The available flip flop to you is a J-K flip flop. In that situation maximum how many flip flop conversion is possible from J-K to others? (Write only the numbers) *

Your answer

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Q11

Which one of the following is true for a Latch? *

- ☐ (A) Latch works in Synchronous mode
- ☐ (B) Latch works in Asynchronous mode
- ☐ Neither A nor B is correct
- ☐ Both (A) and (B) is correct

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Q12

Find the content of register t6 after the execution of the following codes:

```
li $t3, 6
li $t4, 7
li $t6, 5
add $t4, $t4, $t6
add $t6, $t3, $t4
li $t6, 9
```

Your answer

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