

# UNIT-2\_1



**Stream ciphers and block ciphers**

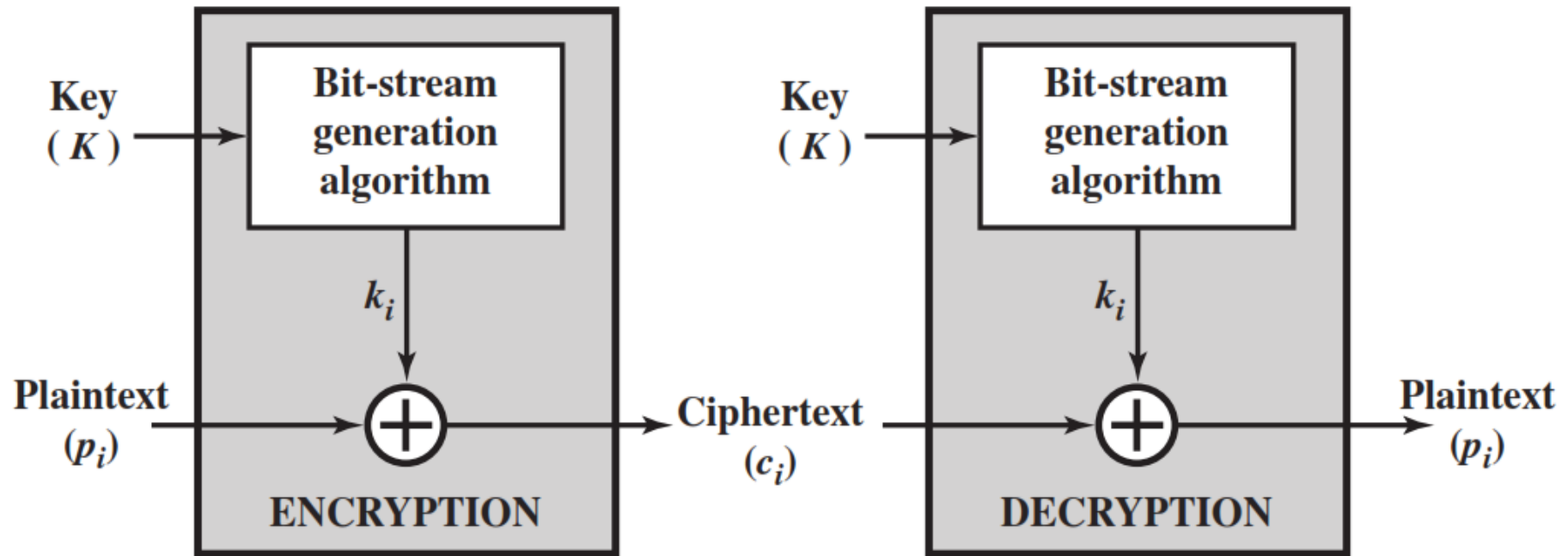
# Unit-2

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- Stream ciphers and block ciphers
- Block Cipher structure
- Data Encryption standard (DES)
- Design principles of block cipher
- AES with structure
- AES Transformation functions
- Key expansion

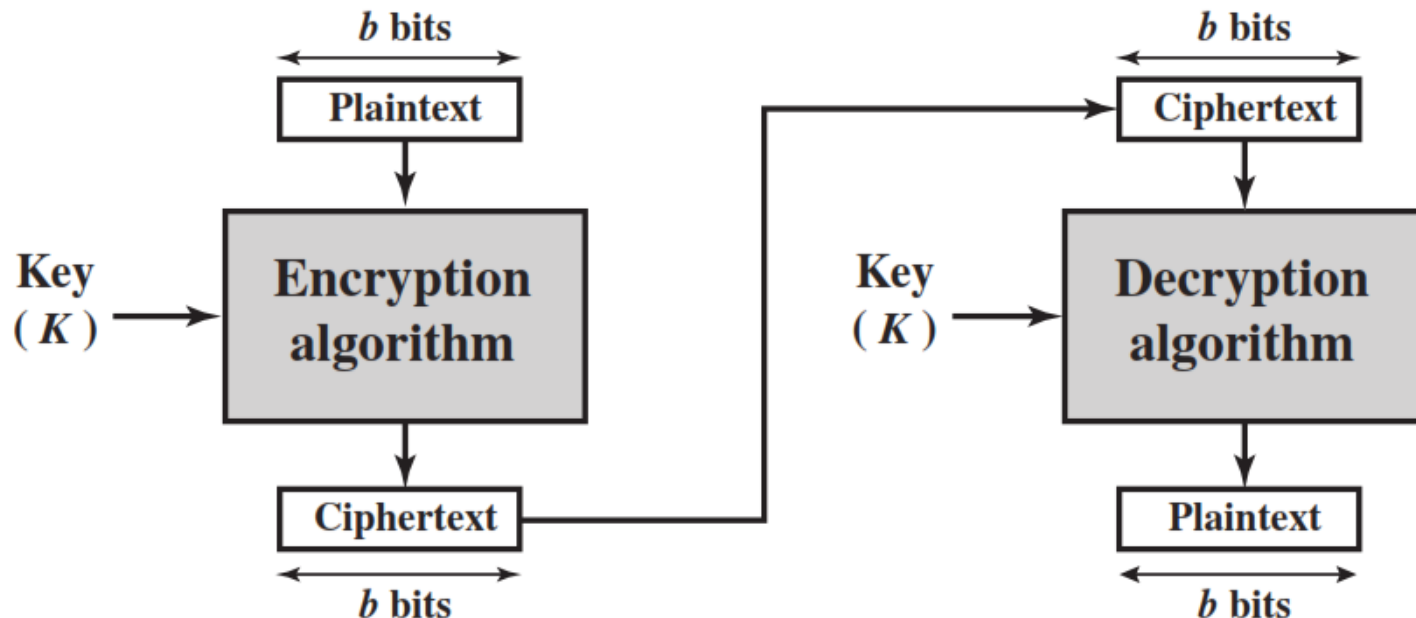
# Stream Cipher

- A **stream cipher** is one that encrypts a digital data stream one bit or one byte at a time.
- Examples of classical stream ciphers are Autokeyed Vigenère cipher, A5/1, RC4 and Vernam cipher.



# Block Cipher

- A **block cipher** is one in which a block of plaintext is treated as a whole and used to produce a ciphertext block of equal length.
- Typically, a block size of **64 or 128** bits is used.
- Examples are Feistel Cipher, DES, Triple DES and AES

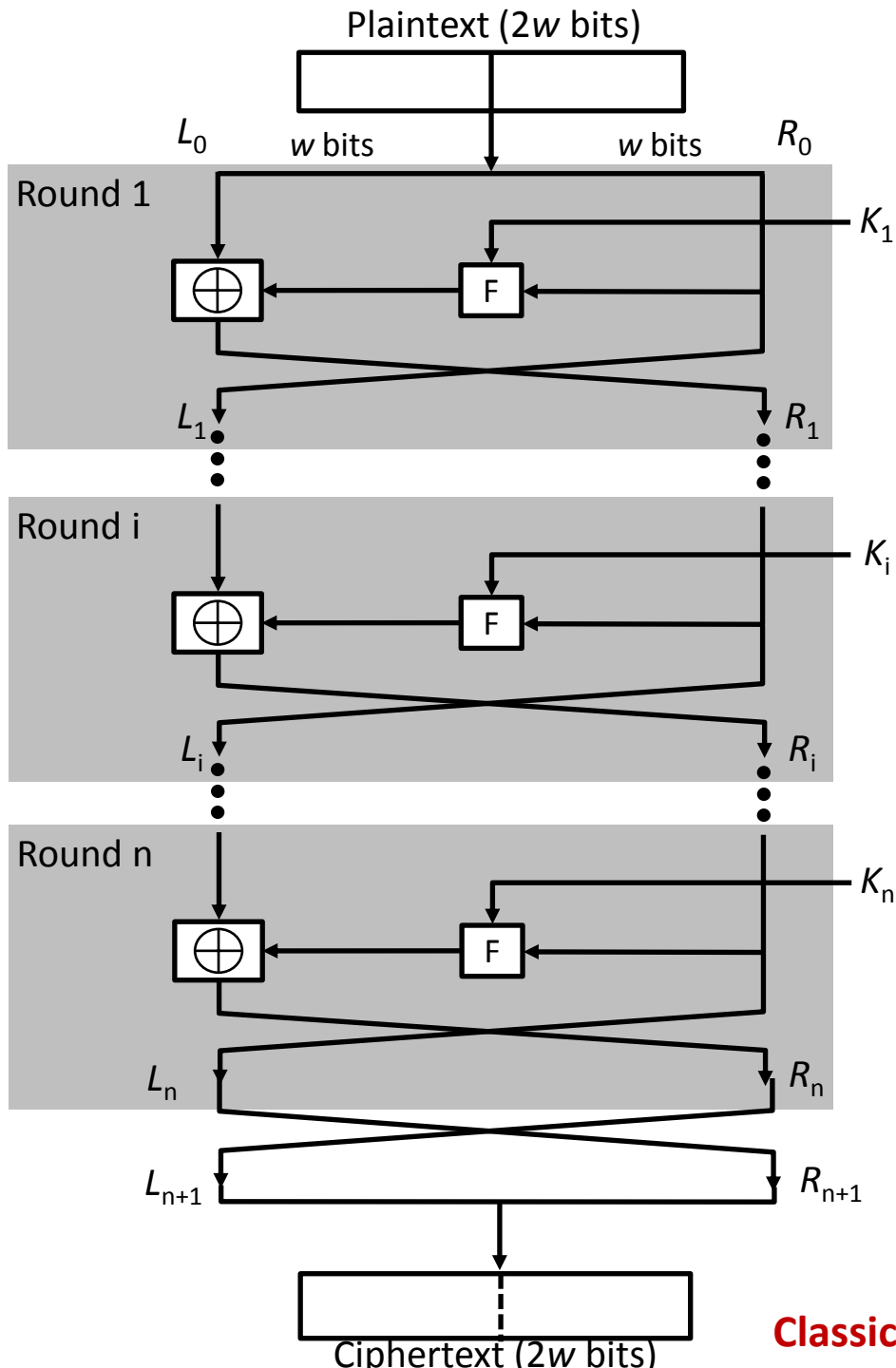


# Diffusion and Confusion

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- **Diffusion** hides the relationship between the ciphertext and the plaintext.
- This is achieved by having each plaintext digit affect the value of many ciphertext digits.
- **Confusion** hides the relationship between the ciphertext and the key.
- This is achieved by the use of a complex substitution algorithm.

# Feistel Cipher Structure Or Block Cipher Structure



# Feistel Cipher Structure

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- Input plaintext block of length  $2w$  bits
- key  $K = n$  bits , Sub-keys:  $K_1, K_2, \dots, K_n$  (Derived from  $K$ )
- All rounds have the same structure.
- A **substitution** is performed by taking exclusive-OR on left half( $L_i$ ) of the data and the output of round function  $F$  which has inputs right half( $R_i$ ) and sub key  $k_i$ .
- A **permutation** is performed that consists of interchange of two halves of data.
- This structure is called **Substitution-Permutation Network** (SPN)

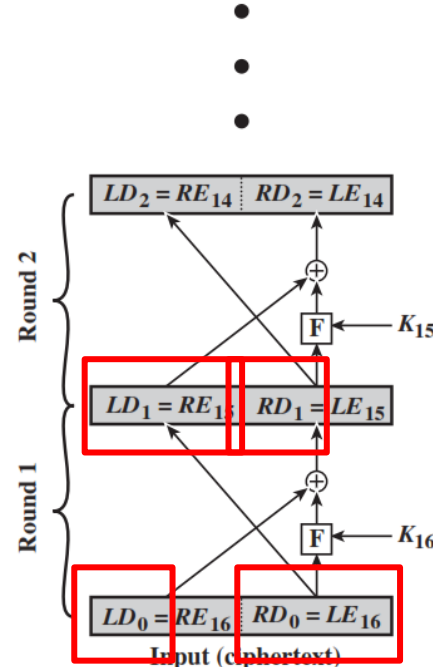
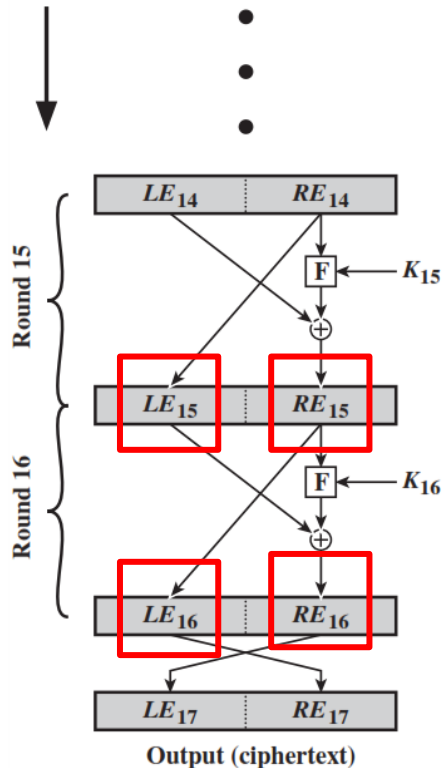
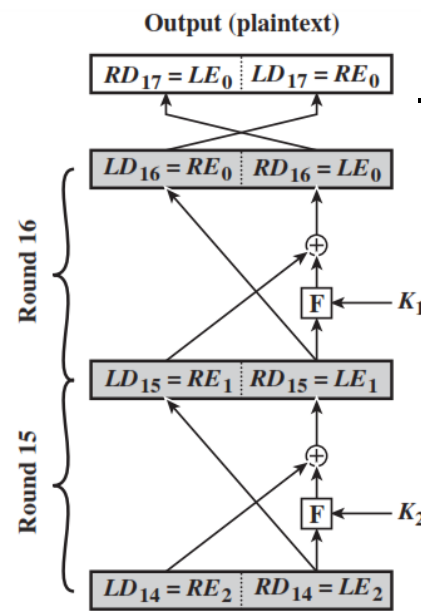
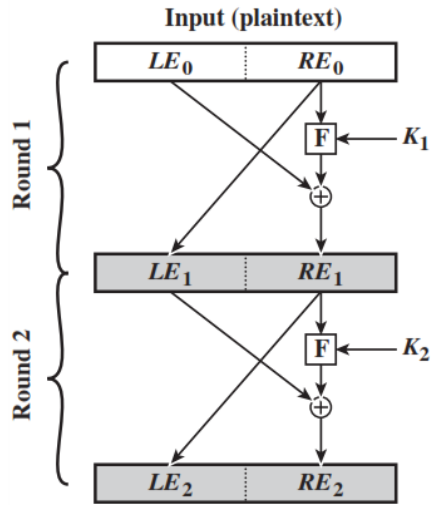
# Feistel Network Factors

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- **Block size:** Common block size of 64-bit. However, the new algorithms uses a 128-bit, 256-bit block size.
- **Key size:** Key sizes of 64 bits or less are now widely considered to be insufficient, These days at least 128 bit, more better, e.g. 192 or 256 bit
- **Number of rounds:** A typical size is 16 rounds.
- **Round function F:** Again, greater complexity generally means greater resistance to cryptanalysis.
- **Subkey generation algorithm:** Greater complexity in this algorithm should lead to greater difficulty of cryptanalysis.



# Feistel Encryption & Decryption



- Prove that o/p of first round of Decryption is equal to 32-bit swap of i/p of 16<sup>th</sup> round of Encryption

- $LD_1 = RE_{15}$  &  $RD_1 = LE_{15}$

- On Encryption Side:

$$LE_{16} = RE_{15}$$

$$RE_{16} = LE_{15} \oplus F(RE_{15}, K_{16})$$

- On Decryption Side:

$$LD_1 = RD_0 = LE_{16} = RE_{15}$$

$$RD_1 = LD_0 \oplus F(RD_0, K_{16})$$

$$= RE_{16} \oplus F(RE_{15}, K_{16})$$

$$= [LE_{15} \oplus F(RE_{15}, K_{16})] \oplus F(RE_{15}, K_{16})$$

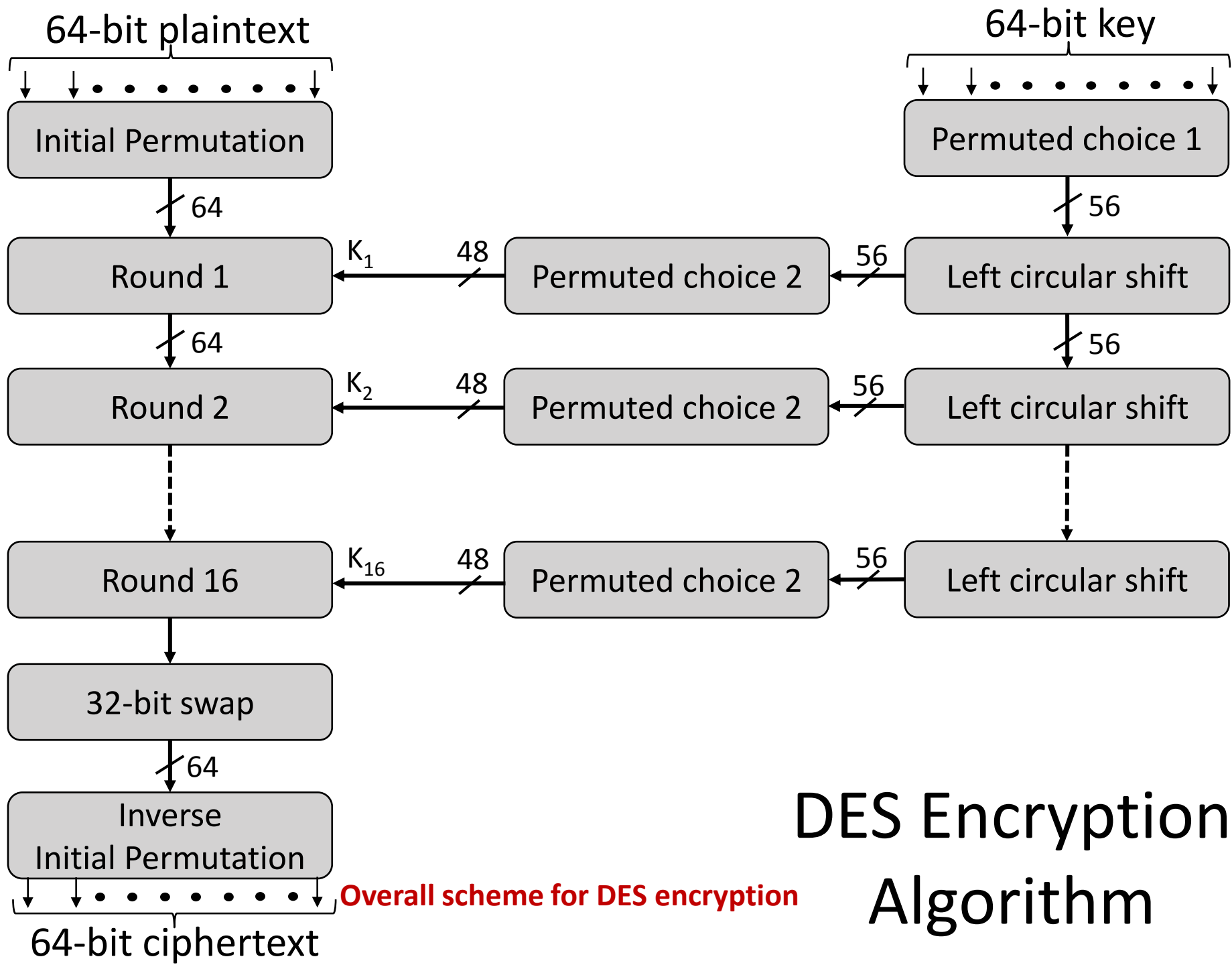
Thus,

$$LD_1 = RE_{15} \text{ \& \; } RD_1 = LE_{15} \oplus C$$

# Data Encryption Standard (DES)

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- Type: Block Cipher
- Block Size : 64-bit
- Key Size: 64-bit, with only 56-bit effective
- Number of Rounds: 16

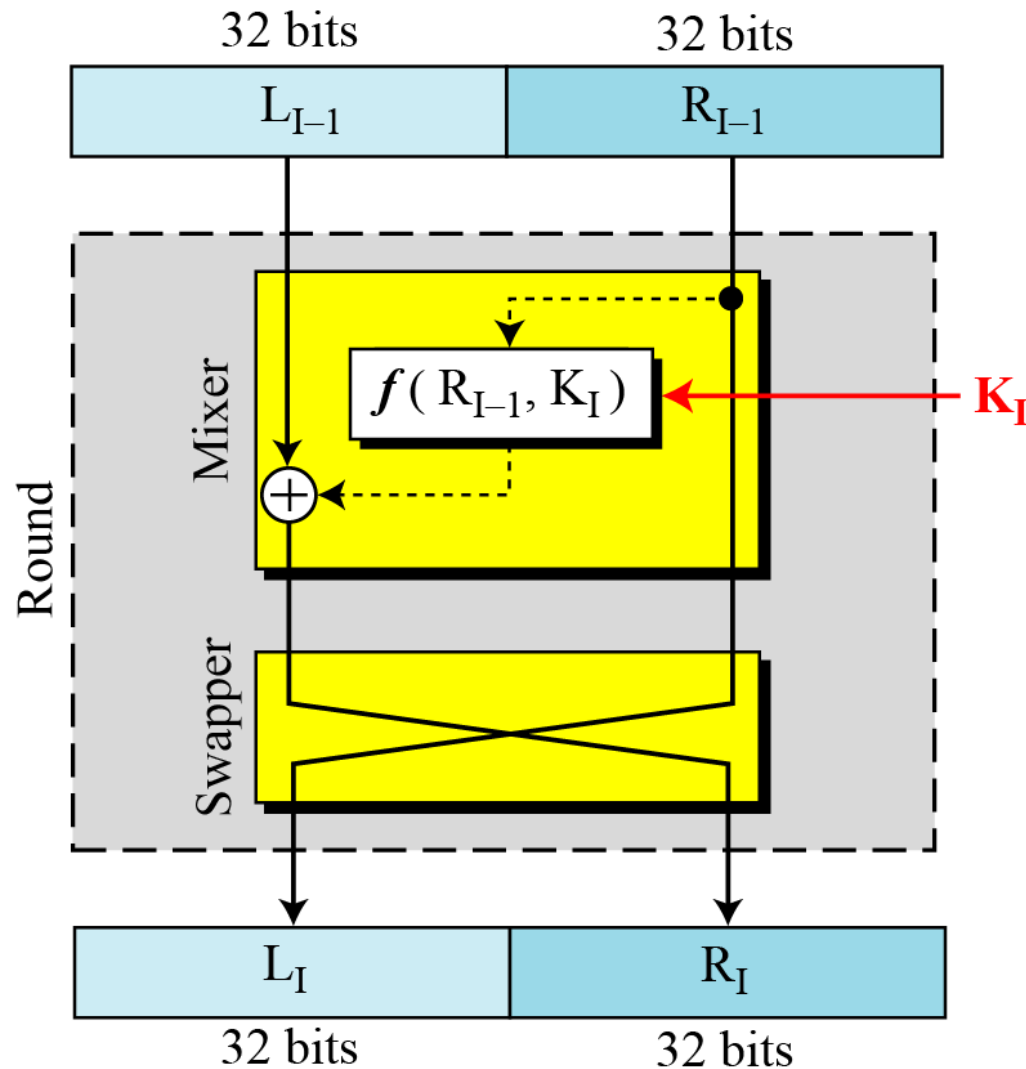


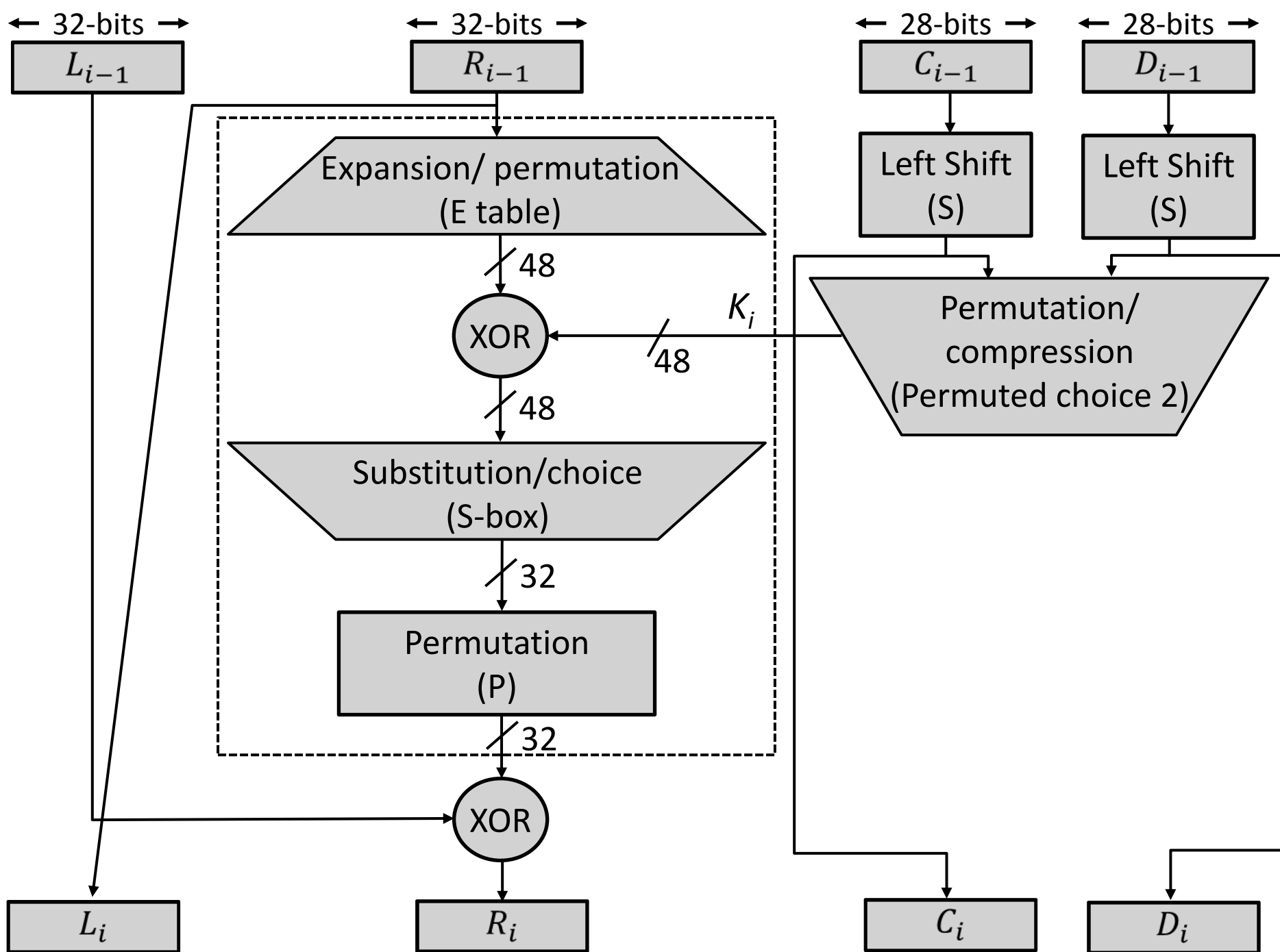
# DES Encryption Algorithm (Cont...)

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- First, the 64-bit plaintext passes through an **initial permutation** (IP) that rearranges the bits to produce the permuted input.
- This is followed by a phase consisting of sixteen rounds of the same function, which involves both **permutation** and **substitution** functions.
- Finally, the preoutput is passed through a permutation that is the **inverse of the initial permutation** function, to produce the 64-bit ciphertext.
- The 56-bit key is passed through a **permutation function**.
- For each of the sixteen rounds, a subkey ( $K_i$ ) is produced by the combination of a **left circular shift** and a **permutation**.

# DES Single Round





**Single Round of DES in Detail**

# DES Single Round (Cont...)

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## 1. Key Transformation

- Permutation of selection of sub-key from original key

## 2. Expansion Permutation (E-table)

- Right half is expanded from 32-bits to 48-bits

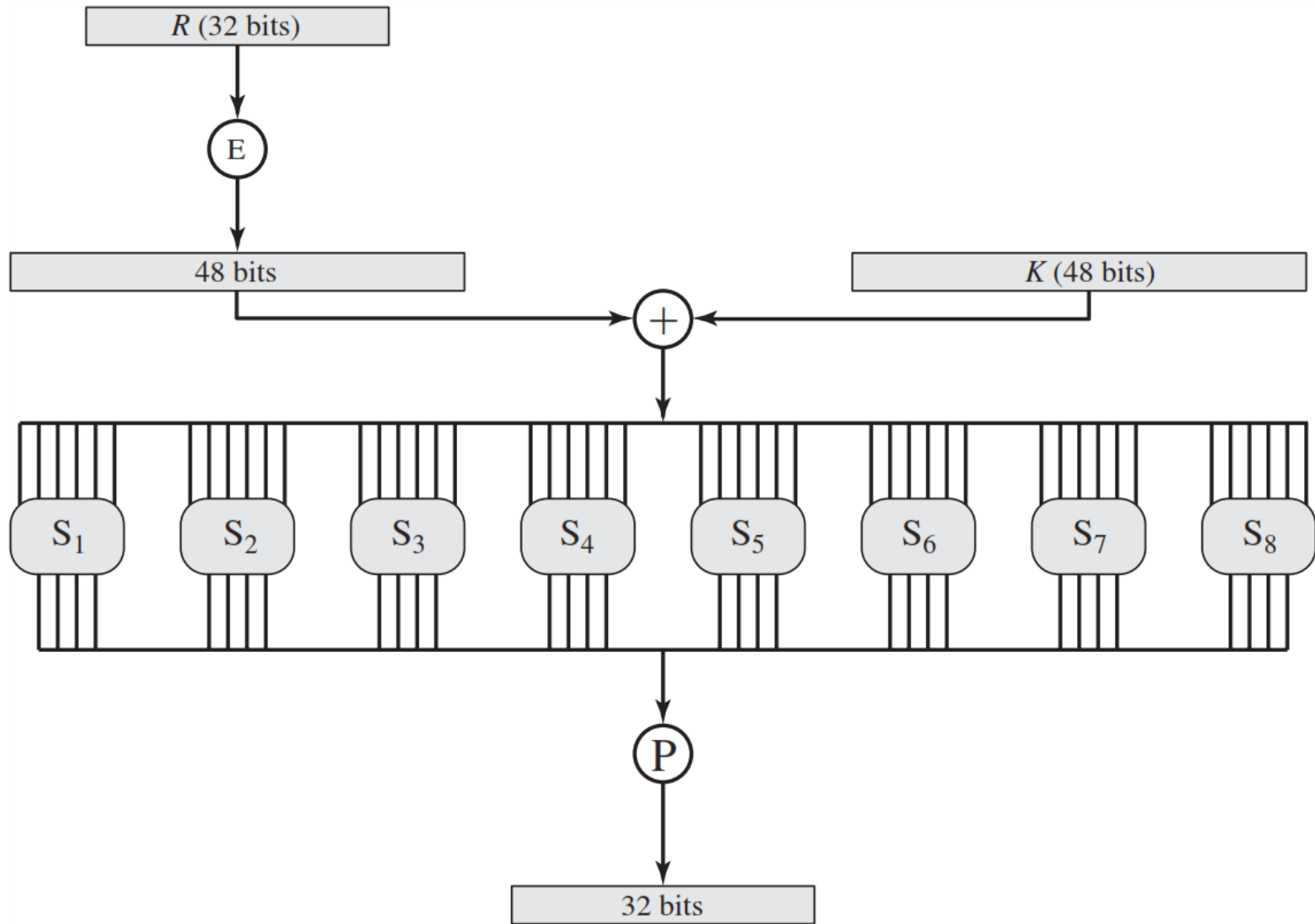
## 3. S-box Substitution

- Accepts 48-bits from XOR operation and produce 32-bits using 8 substitution boxes (each S-boxes has a 6-bit i/p and 4-bit o/p).

## 4. P-Box Permutation

## 5. XOR and Swap

# Role of S-boxes in the function F





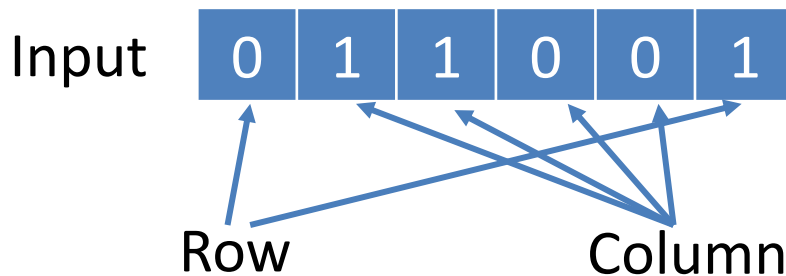
# Role of S-box (Cont...)

- The outer two bits of each group select one row of an S-box.
- Inner four bits selects one column of an S-box.

	0	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15
0	14	04	13	01	02	15	11	08	03	10	06	12	05	09	00	07
1	00	15	07	04	14	02	13	10	03	06	12	11	09	05	03	08
2	04	01	14	08	13	06	02	11	15	12	09	07	03	10	05	00
3	15	12	08	02	04	09	01	07	05	11	03	14	10	00	06	13

**S-box 1**

- Example:



Output **1 0 0 1**

# Avalanche Effect

- Desirable property of any encryption algorithm is that a change in one bit of the plaintext or of the key should produce a change in many bits of cipher text.
- DES performs strong **avalanche effect**.

Plaintext: 0000000000000000

Key: 22234512987ABB23

Ciphertext: 4789FD476E82A5F1

Plaintext: 00000000000000001

Key: 22234512987ABB23

Ciphertext: 0A4ED5C15A63FEA3

- Although the two plaintext blocks differ only in the rightmost bit, the ciphertext blocks differ in 29 bits.
- This means that changing approximately 1.5 % of the plaintext creates a change of approximately 45 % in the ciphertext.

# AES (Advanced Encryption Standard)

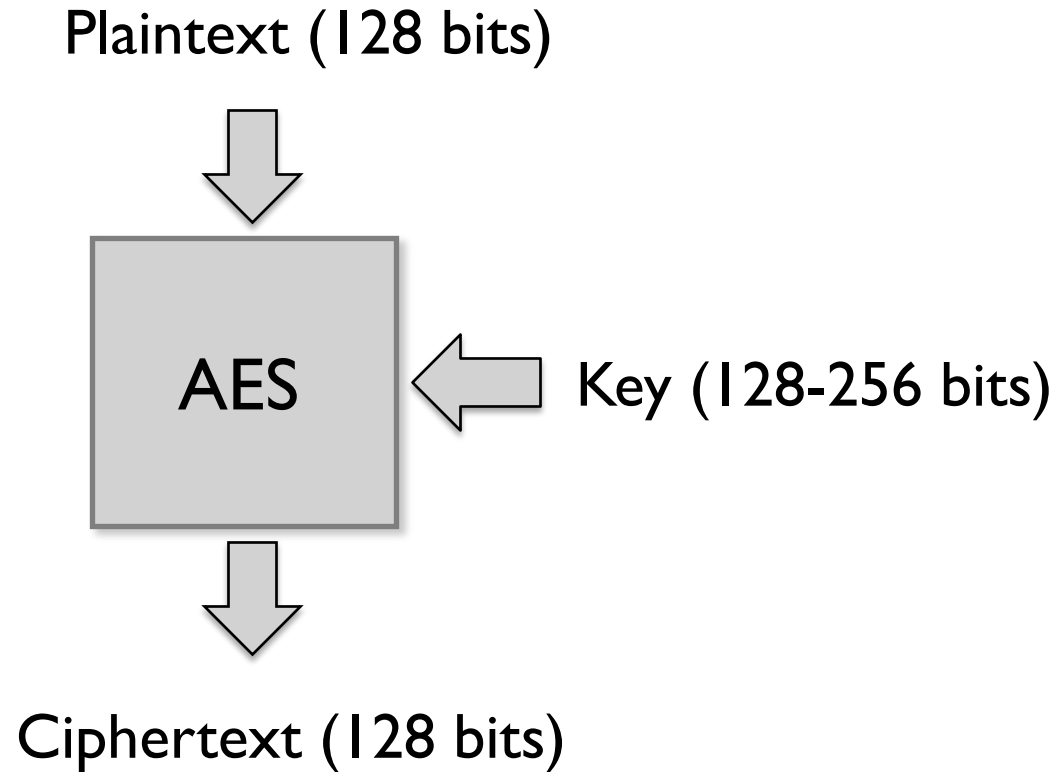
- The Rijndael proposal for AES defined a cipher in which the block length and the key length can be independently specified to be 128, 192, or 256 bits.

<b>Key size (words/ bytes/ bits)</b>	<b>4/16/128</b>	<b>6/24/192</b>	<b>8/32/256</b>
<b>Block size (words/ bytes/ bits)</b>	<b>4/16/128</b>	<b>4/16/128</b>	<b>4/16/128</b>
<b>Round key size (words/ bytes/ bits)</b>	<b>4/16/128</b>	<b>4/16/128</b>	<b>4/16/128</b>
<b>Number of Rounds</b>	<b>10</b>	<b>12</b>	<b>14</b>
<b>Expanded Key Size (words)</b>	<b>44</b>	<b>52</b>	<b>60</b>

- AES designed to have characteristics
  1. Resistance against all known attacks
  2. Speed and code compactness on a wide range of platforms
  3. Design simplicity

# AES (Advanced Encryption Standard)

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# AES Overview

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- Simple Repeating structure
- Cipher begins and ends with Add Round Key
  - Forms a Vernam Cipher or “One Time Pad”
    - Any other stage applied at the beginning or end is reversible without the key
- Other three stages provide confusion, diffusion and nonlinearity
- *n* standard rounds, *n* is 10,12 or 14
- The first *n*-1 rounds are similar consisting of
  - ByteSub
  - ShiftRow
  - MixColumn
  - AddRoundKey
- The last round only perform the transformations
  - ByteSub
  - ShiftRow
  - AddRoundKey

## Initialization

1. Expand 16-byte key to get the actual **key block** to be used.
2. Initialize 16-byte plaintext block called as **state**.
3. XOR the **state** with the **key block**.

# AES Structure

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- The first  $n-1$  rounds consist of four distinct transformation functions.

## SubBytes

- The 16 input bytes are substituted using an **S-box**

## ShiftRows

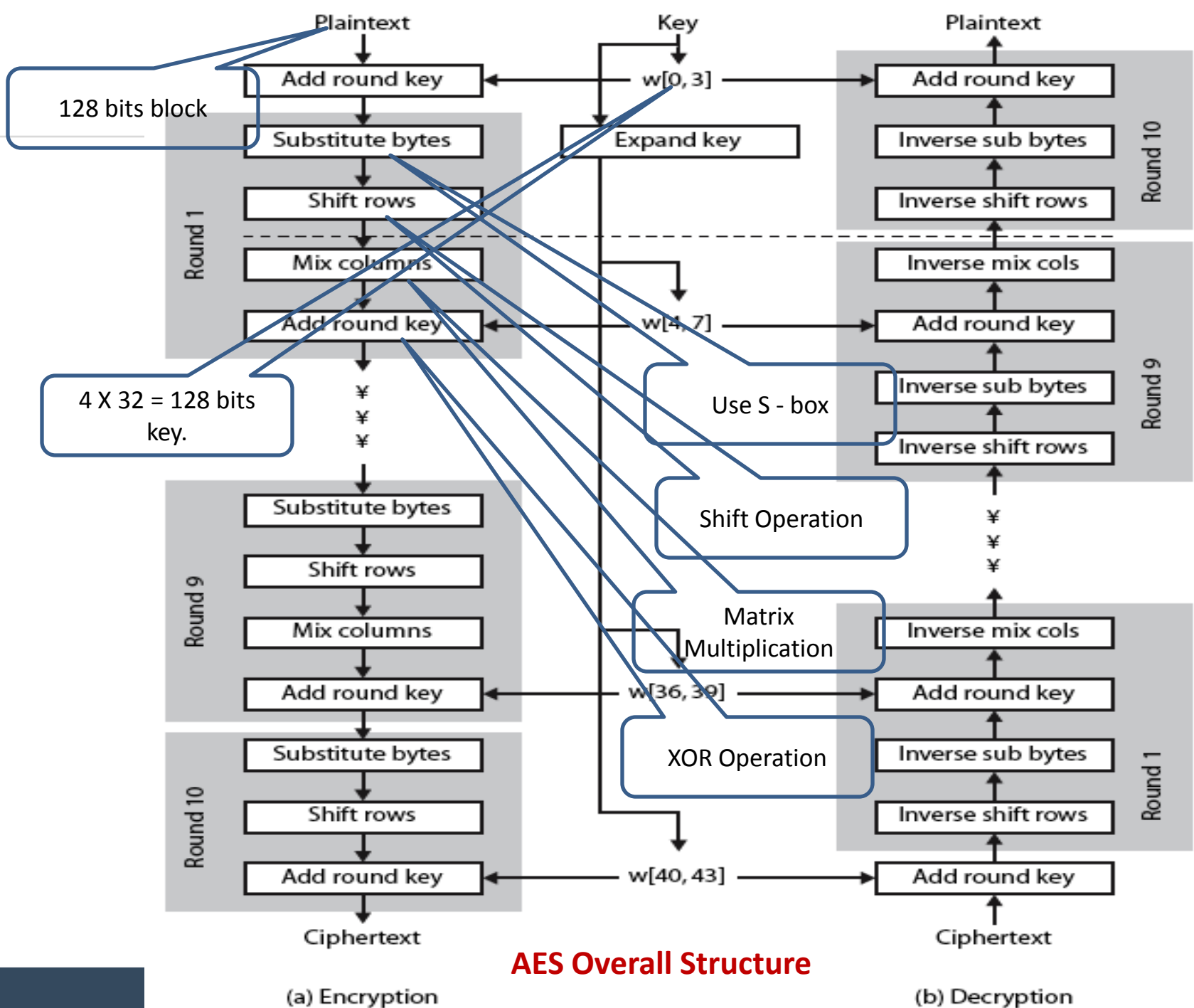
- Each of the four rows of the matrix is shifted to the left

## MixColumns

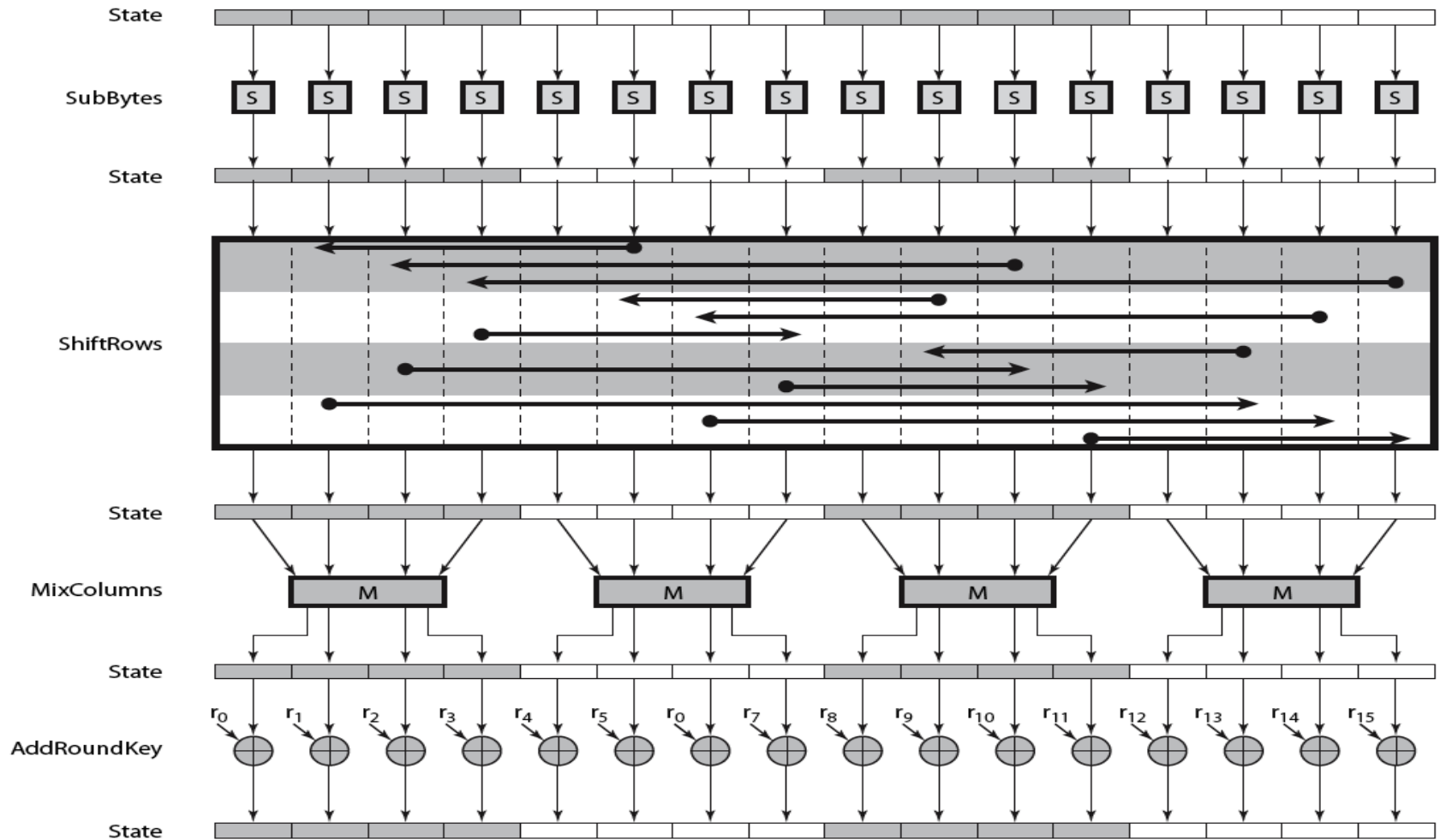
- Each column of four bytes is now transformed using a special mathematical function.

## AddRoundKey

- The 16 bytes of the matrix are now considered as 128 bits and are XORed to the 128 bits of the round key.



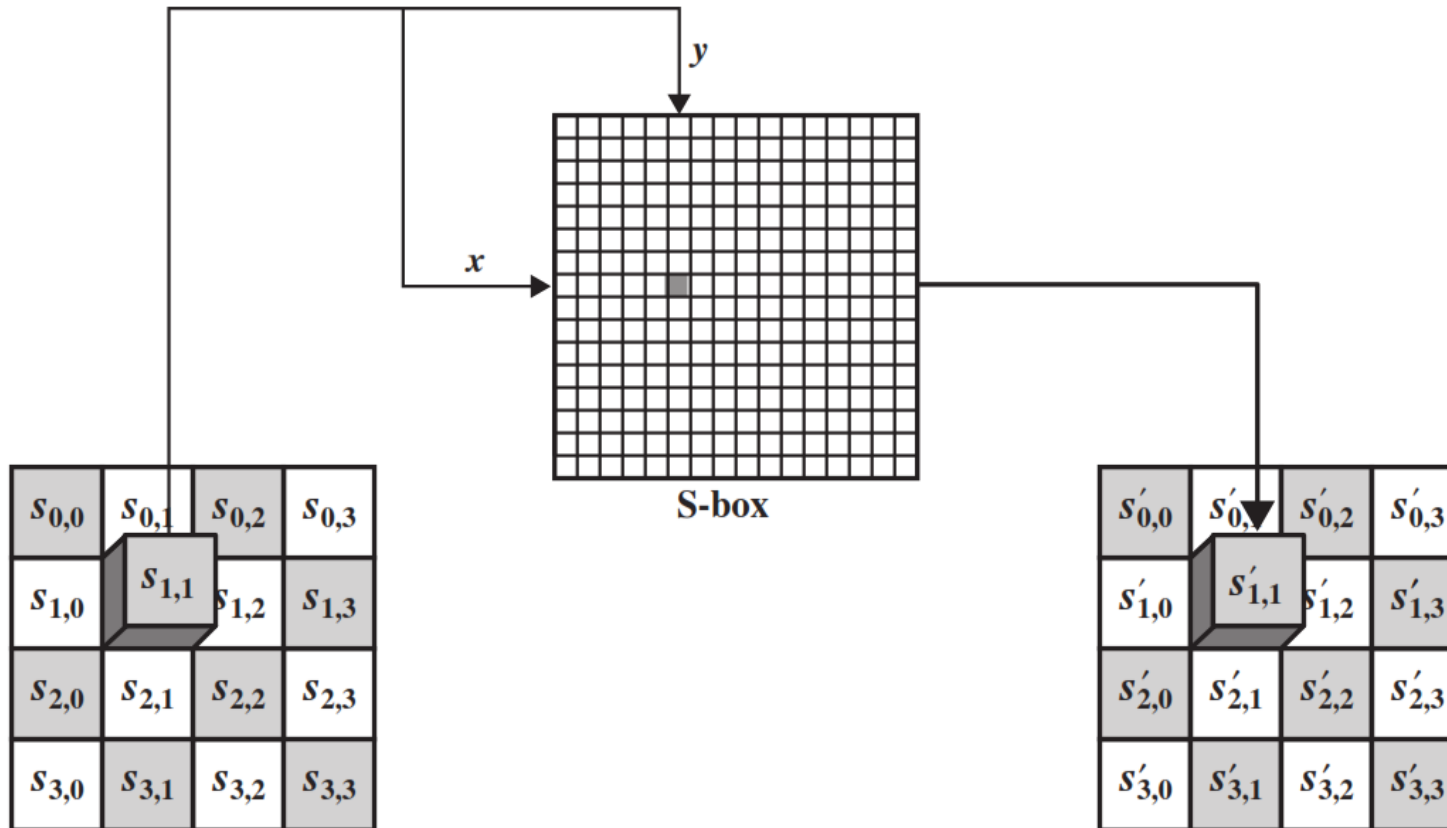
# AES Round





# SubByte Transformation

- The forward substitute byte transformation, called **SubBytes**, is a simple table lookup



# ShiftRows

- The first row of **State is not altered**.
- For the second row, a 1-byte circular left shift is performed.
- For the third row, a 2-byte circular left shift is performed.
- For the fourth row, a 3-byte circular left shift is performed.

87	F2	4D	97
EC	6E	4C	90
4A	C3	46	E7
8C	D8	95	A6

→

87	F2	4D	97
6E	4C	90	EC
46	E7	4A	C3
A6	8C	D8	95

# MixColumns

- Each byte of a column is mapped into a new value that is a function of all four bytes in that column.
- Mix Columns performs matrix multiplication according to [Galois field arithmetic](#)

87	F2	4D	97
6E	4C	90	EC
46	E7	4A	C3
A6	8C	D8	95

→

47	40	A3	4C
37	D4	70	9F
94	E4	3A	42
ED	A5	A6	BC

# GF(2<sup>8</sup>)

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- Finite Field/ Galois fields : A field with finite number of elements
- Finite fields play a key role in cryptography
- The finite field with  $p^n$  elements is denoted  $GF(p^n)$  and is also called the **Galois field** of order  $p^n$
- [Rijndael](#) (standardised as AES) uses the characteristic 2 finite field with 256 elements, which can also be called the Galois field  $GF(2^8)$
- Byte b7b6b5b4b3b2b1b0 will have the representation as
$$b(x) = b_7x^7 + b_6x^6 + b_5x^5 + b_4x^4 + b_3x^3 + b_2x^2 + b_1x + b_0$$
- Therefore, 01010111 would have the representation as
$$x^6 + x^4 + x^2 + x + 1$$

# Addition on Bytes

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- The sum of two elements is the polynomial with coefficients that are given by the sum modulo 2 (i.e.,  $1+1=0$ ) of the coefficients of the two terms.
- Example:  $57+83=?$ 
  - $57 = x^6 + x^4 + x^2 + x + 1$
  - $83 = x^7 + x + 1$
  - $(x^6 + x^4 + x^2 + x + 1) + (x^7 + x + 1) = x^7 + x^6 + x^4 + x^2$
  - $x^7 + x^6 + x^4 + x^2 = D4$

# Multiplication

- Multiplication is performed using a special polynomial called the irreducible polynomial.
- The modulus used for these operations is typically a specific irreducible polynomial of degree 8, which ensures that the resulting values remain within the field.
- Example:  $57 \bullet 83 = ?$ 
  - $57 = x^6 + x^4 + x^2 + x + 1$
  - $83 = x^7 + x + 1$
  - $(x^6 + x^4 + x^2 + x + 1) \bullet (x^7 + x + 1) = x^{13} + x^{11} + x^9 + x^8 + \textcolor{red}{x^7} + \textcolor{red}{x^7} + x^5 + x^3 + \textcolor{red}{x^2} + \textcolor{red}{x} + x^6 + x^4 + \textcolor{red}{x^2} + \textcolor{red}{x} + 1$ 

AES uses arithmetic in the finite field  $GF(2^8)$  with irreducible (prime) polynomial which is  $x^8 + x^4 + x^3 + x + 1$  (11B)
  - $x^{13} + x^{11} + x^9 + x^8 + x^6 + x^5 + x^4 + x^3 + 1$  modulo  $x^8 + x^4 + x^3 + x + 1$
  - ....
  - $x^7 + x^6 + 1 = C1$

# AddRoundKey

- In the forward add round key transformation, the 128 bits of State are bitwise XORed with the 128 bits of the round key.

47	40	A3	4C
37	D4	70	9F
94	E4	3A	42
ED	A5	A6	BC

State

 $\oplus$ 

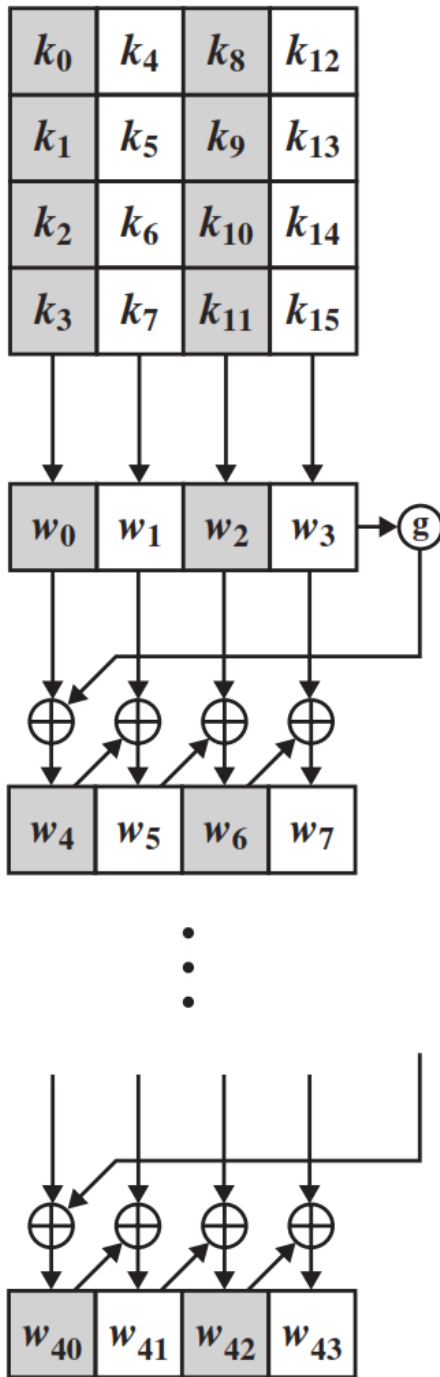
AC	19	28	57
77	FA	D1	5C
66	DC	29	00
F3	21	41	6A

Round Key

 $=$ 

EB	59	8B	1B
40	2E	A1	C3
F2	38	13	42
1E	84	E7	D6

# AES Key Expansion



- The AES key expansion algorithm takes as input a four-word (16-byte) key and produces a linear array of **44 words** (176 bytes).
- Each added word  **$w[i]$**  depends on the immediately preceding word,  $w[i - 1]$ .
- In three out of four cases, a simple XOR is used.

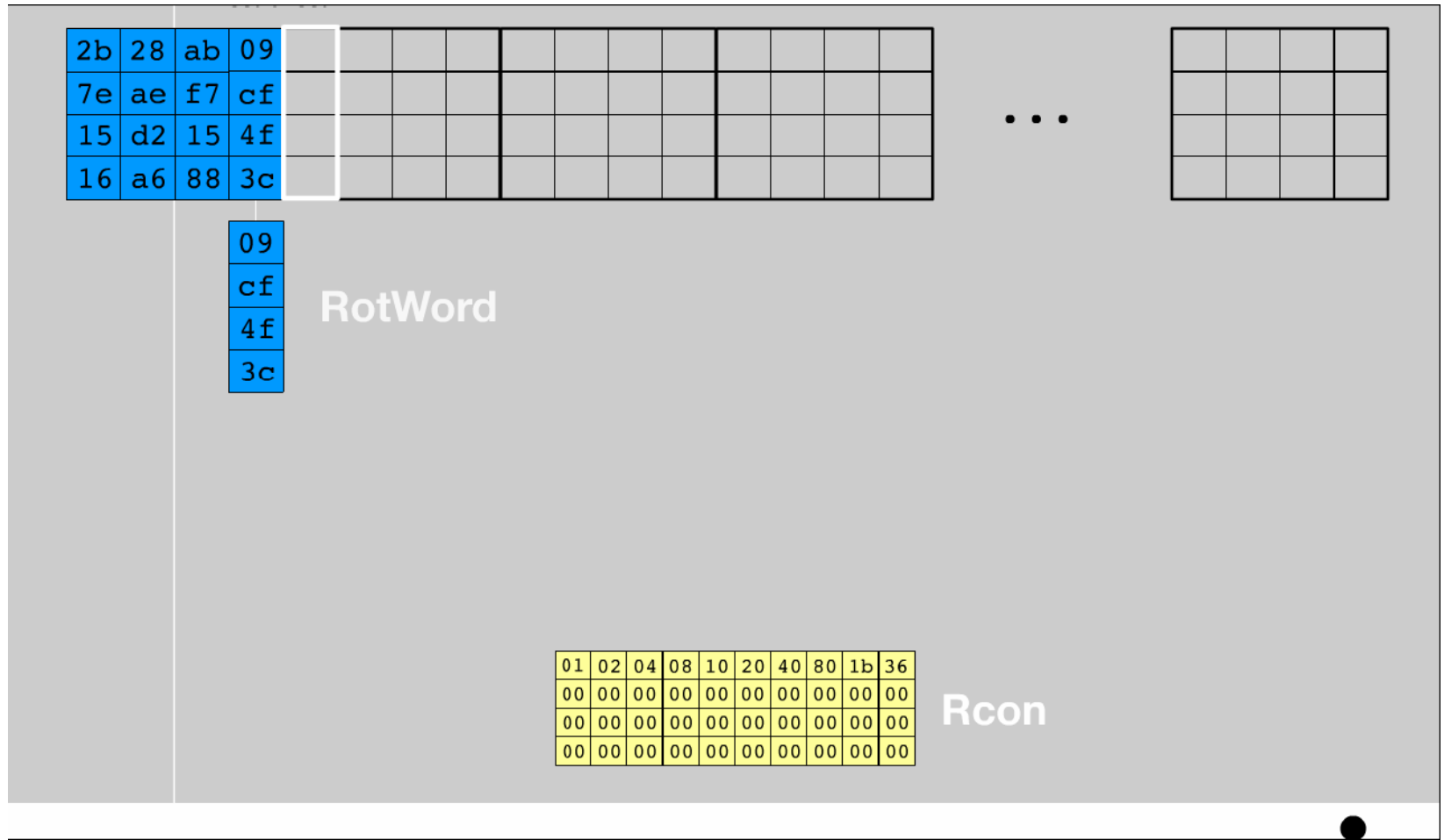


# Key Expansion Example

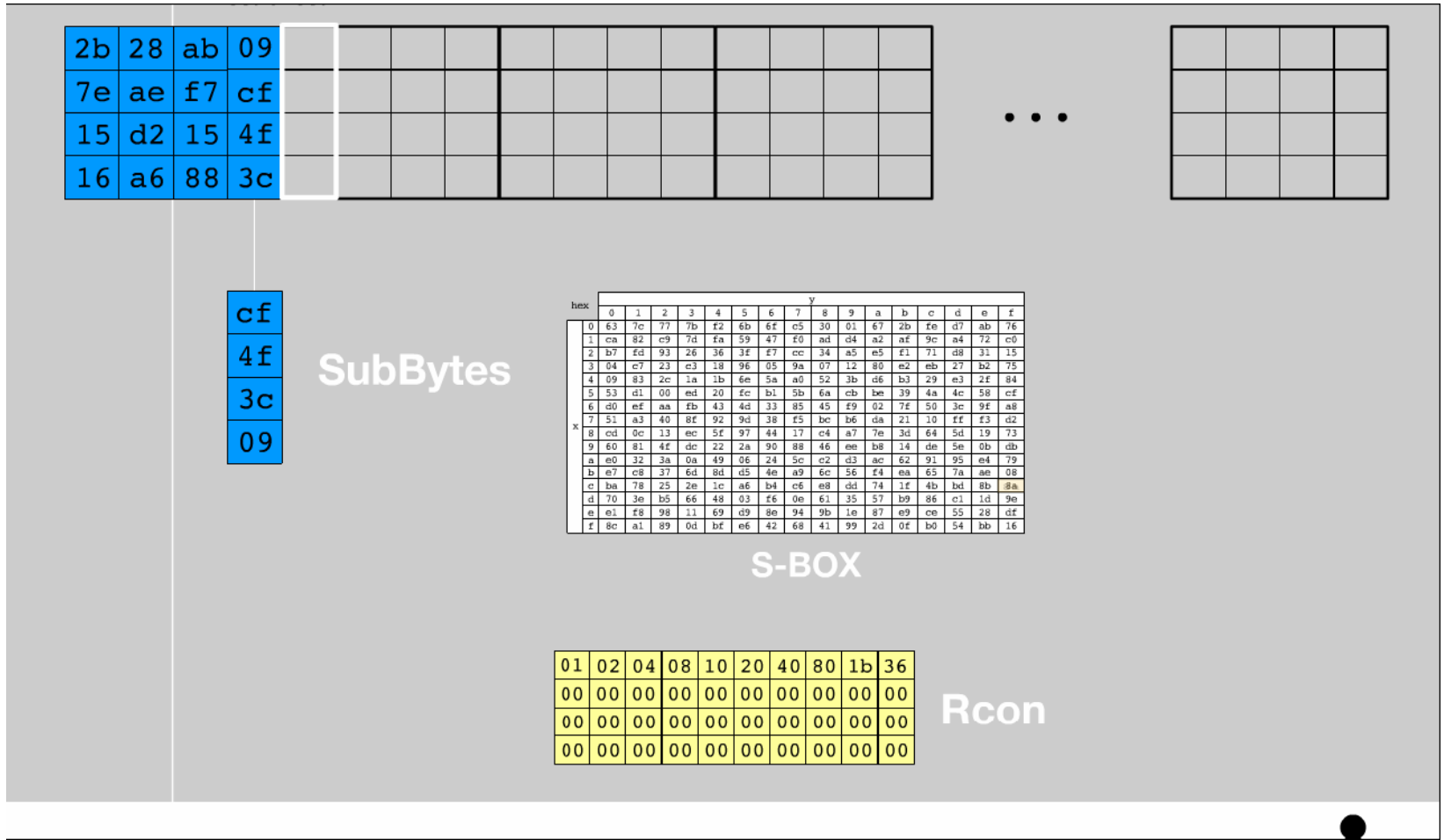
Plaintext:	0123456789abcdef fedcba9876543210
Key:	0f1571c947d9e8590cb7add6af7f6798
Ciphertext:	ff0b844a0853bf7c6934ab4364148fb9

Key Words	Auxiliary Function
$w_0 = 0f\ 15\ 71\ c9$ $w_1 = 47\ d9\ e8\ 59$ $w_2 = 0c\ b7\ ad\ d6$ $w_3 = af\ 7f\ 67\ 98$	$RotWord(w_3) = 7f\ 67\ 98\ af = x_1$ $SubWord(x_1) = d2\ 85\ 46\ 79 = y_1$ $Rcon(1) = 01\ 00\ 00\ 00$ $y_1 \oplus Rcon(1) = d3\ 85\ 46\ 79 = z_1$
$w_4 = w_0 \oplus z_1 = dc\ 90\ 37\ b0$ $w_5 = w_4 \oplus w_1 = 9b\ 49\ df\ e9$ $w_6 = w_5 \oplus w_2 = 97\ fe\ 72\ 3f$ $w_7 = w_6 \oplus w_3 = 38\ 81\ 15\ a7$	$RotWord(w_7) = 81\ 15\ a7\ 38 = x_2$ $SubWord(x_2) = 0c\ 59\ 5c\ 07 = y_2$ $Rcon(2) = 02\ 00\ 00\ 00$ $y_2 \oplus Rcon(2) = 0e\ 59\ 5c\ 07 = z_2$
$w_8 = w_4 \oplus z_2 = d2\ c9\ 6b\ b7$ $w_9 = w_8 \oplus w_5 = 49\ 80\ b4\ 5e$ $w_{10} = w_9 \oplus w_6 = de\ 7e\ c6\ 61$ $w_{11} = w_{10} \oplus w_7 = e6\ ff\ d3\ c6$	$RotWord(w_{11}) = ff\ d3\ c6\ e6 = x_3$ $SubWord(x_3) = 16\ 66\ b4\ 83 = y_3$ $Rcon(3) = 04\ 00\ 00\ 00$ $y_3 \oplus Rcon(3) = 12\ 66\ b4\ 8e = z_3$
$w_{12} = w_8 \oplus z_3 = c0\ af\ df\ 39$ $w_{13} = w_{12} \oplus w_9 = 89\ 2f\ 6b\ 67$ $w_{14} = w_{13} \oplus w_{10} = 57\ 51\ ad\ 06$ $w_{15} = w_{14} \oplus w_{11} = b1\ ae\ 7e\ c0$	$RotWord(w_{15}) = ae\ 7e\ c0\ b1 = x_4$ $SubWord(x_4) = e4\ f3\ ba\ c8 = y_4$ $Rcon(4) = 08\ 00\ 00\ 00$ $y_4 \oplus Rcon(4) = ec\ f3\ ba\ c8 = z_4$

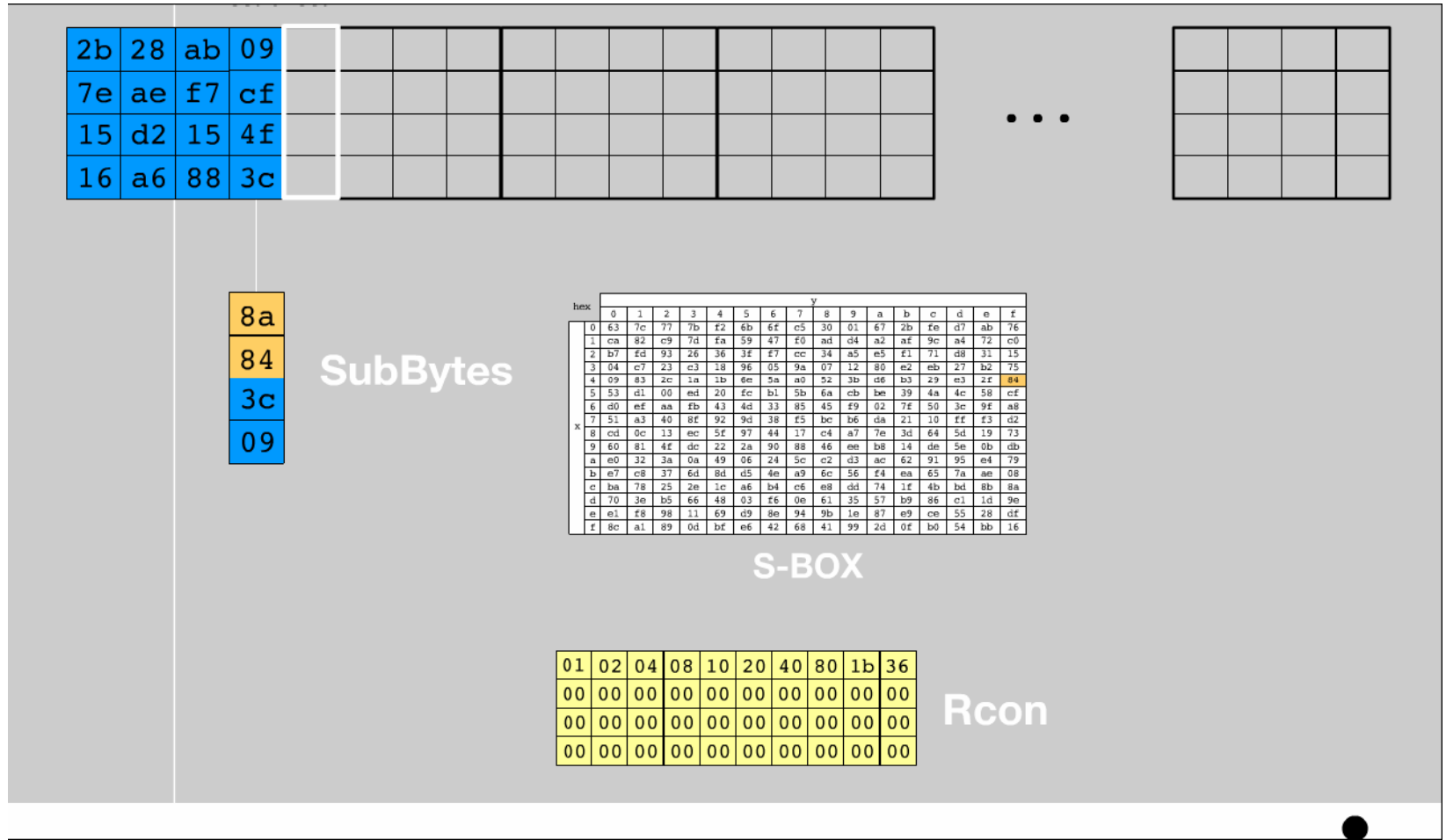
# Key Schedule Generation (For reference)



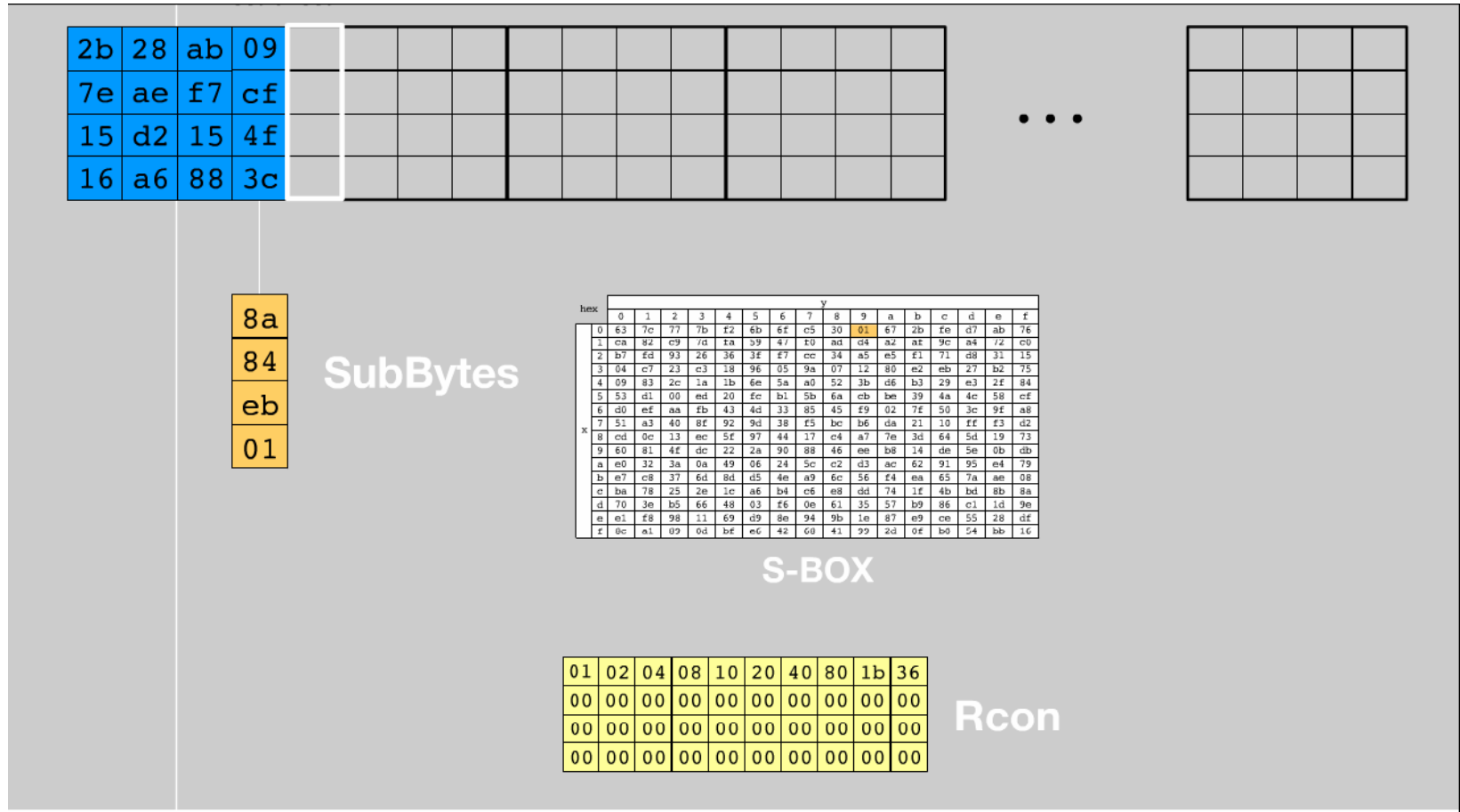
# Key Schedule Generation (Cont.)



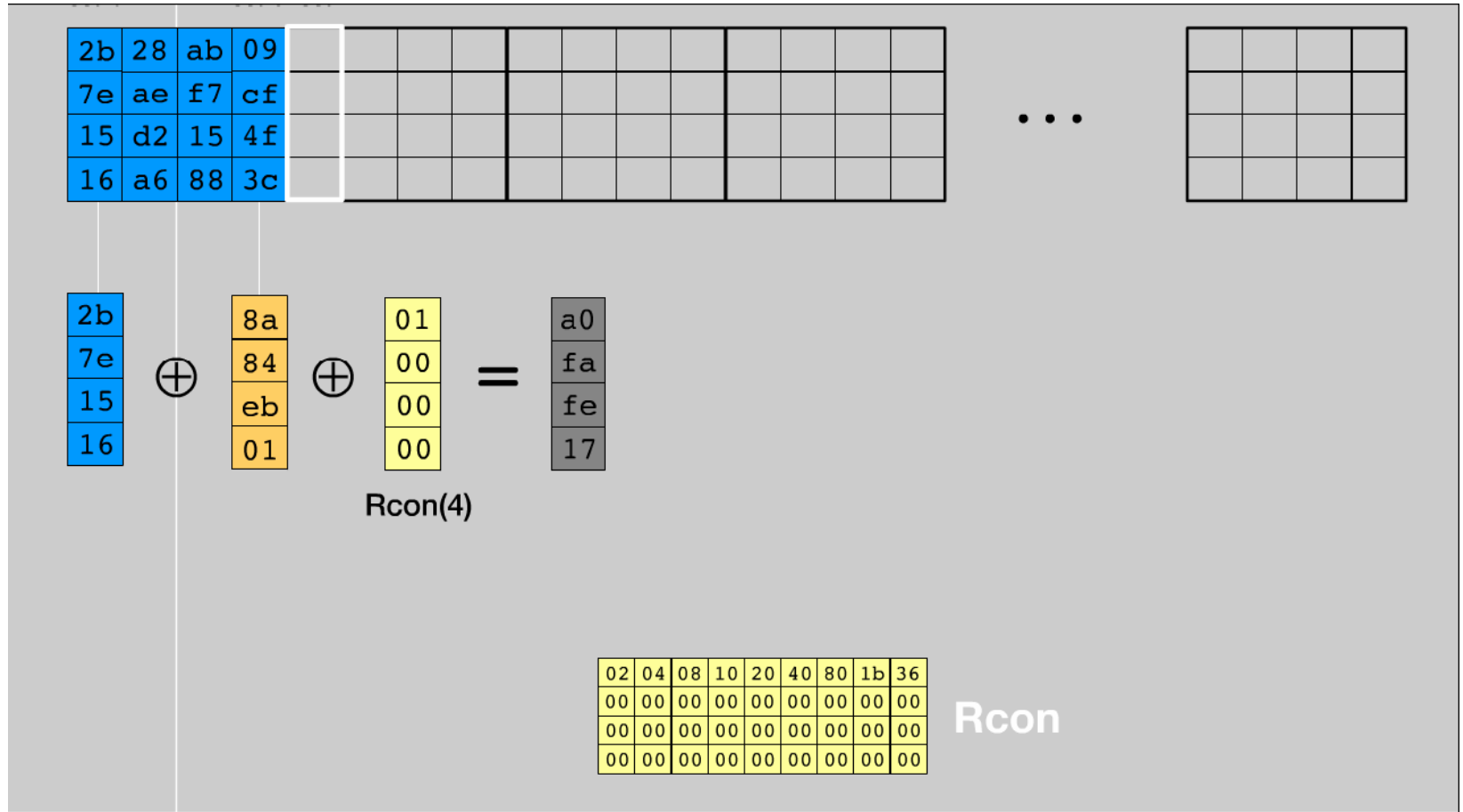
# Key Schedule Generation (Cont.)



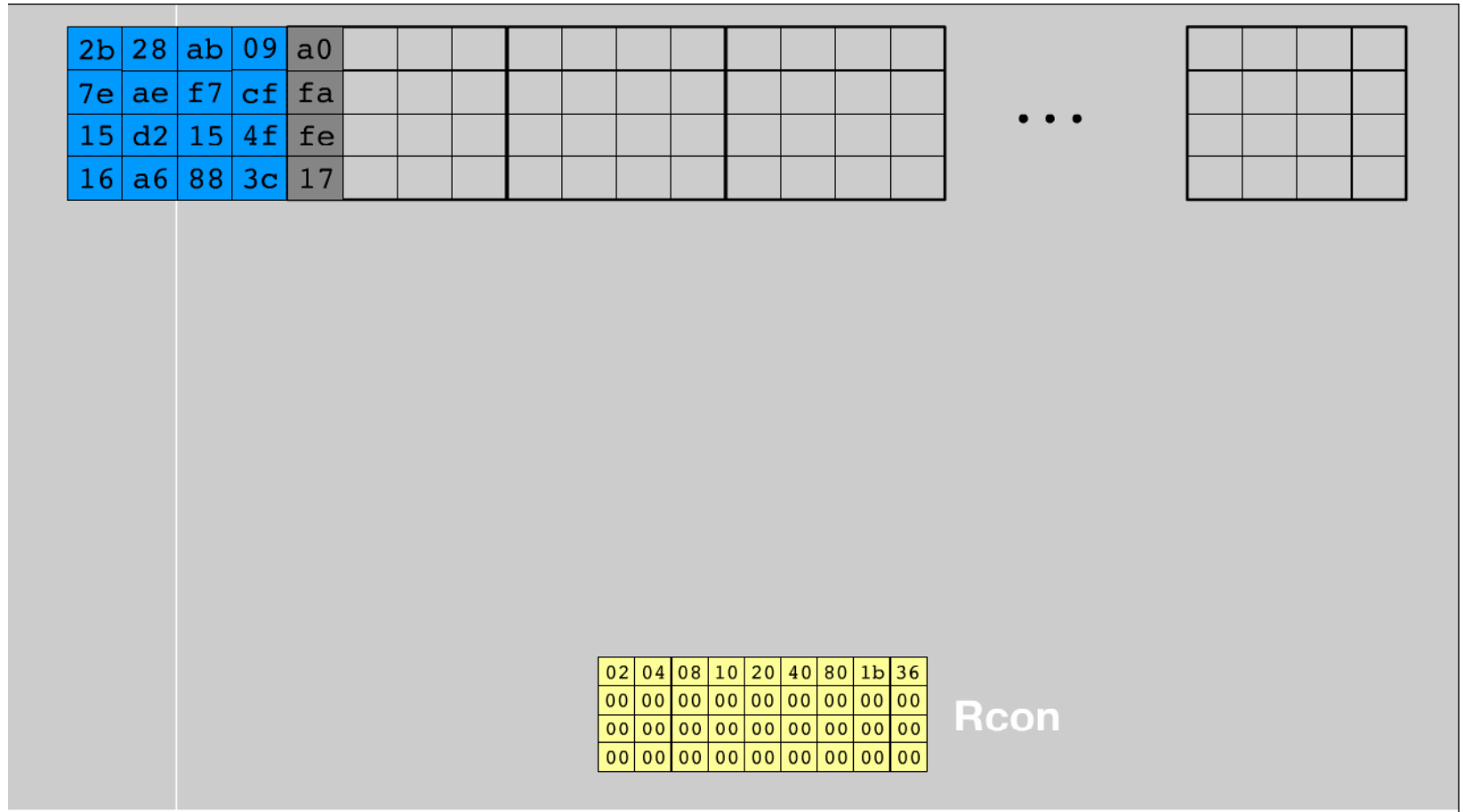
# Key Schedule Generation (Cont.)



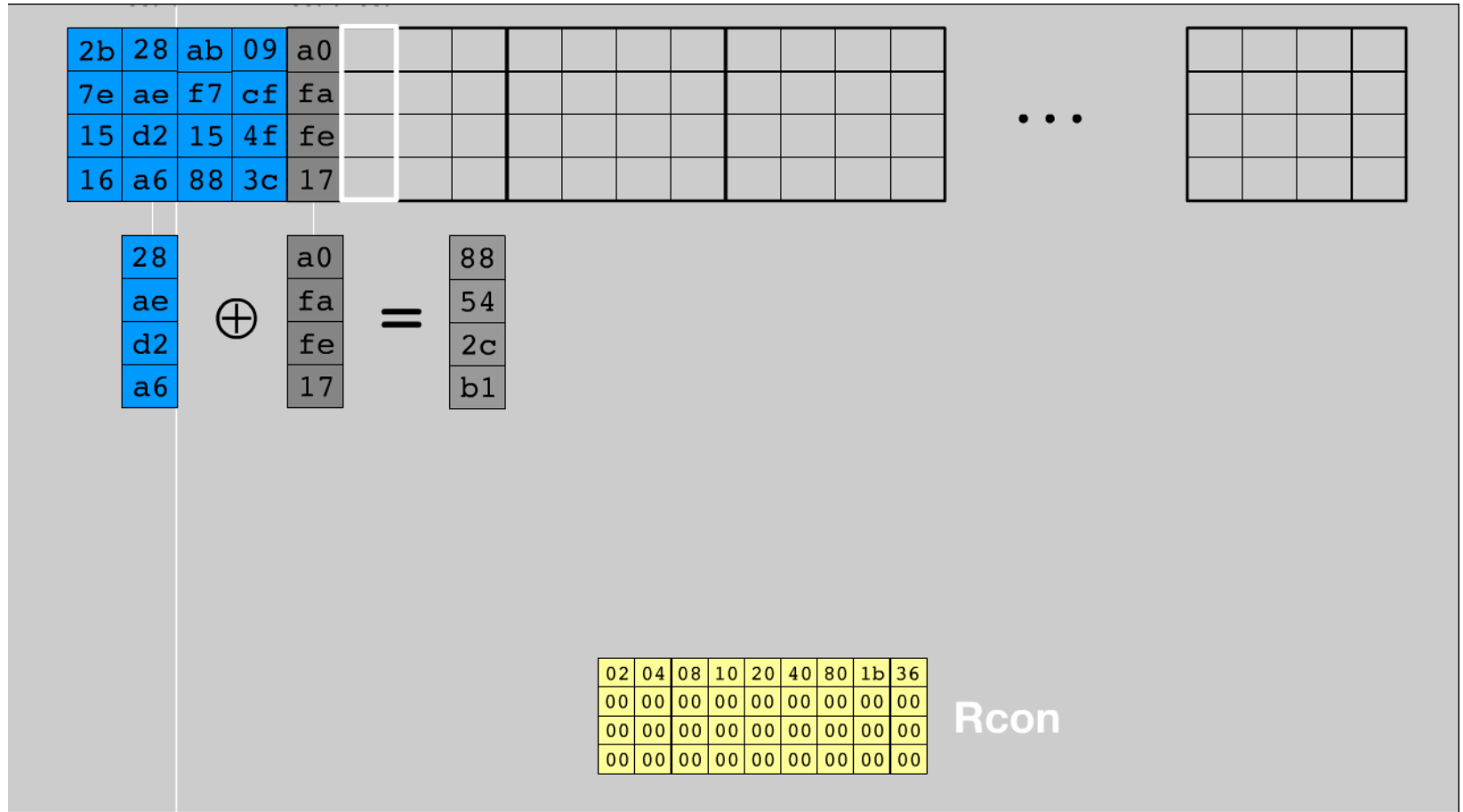
# Key Schedule Generation (Cont.)



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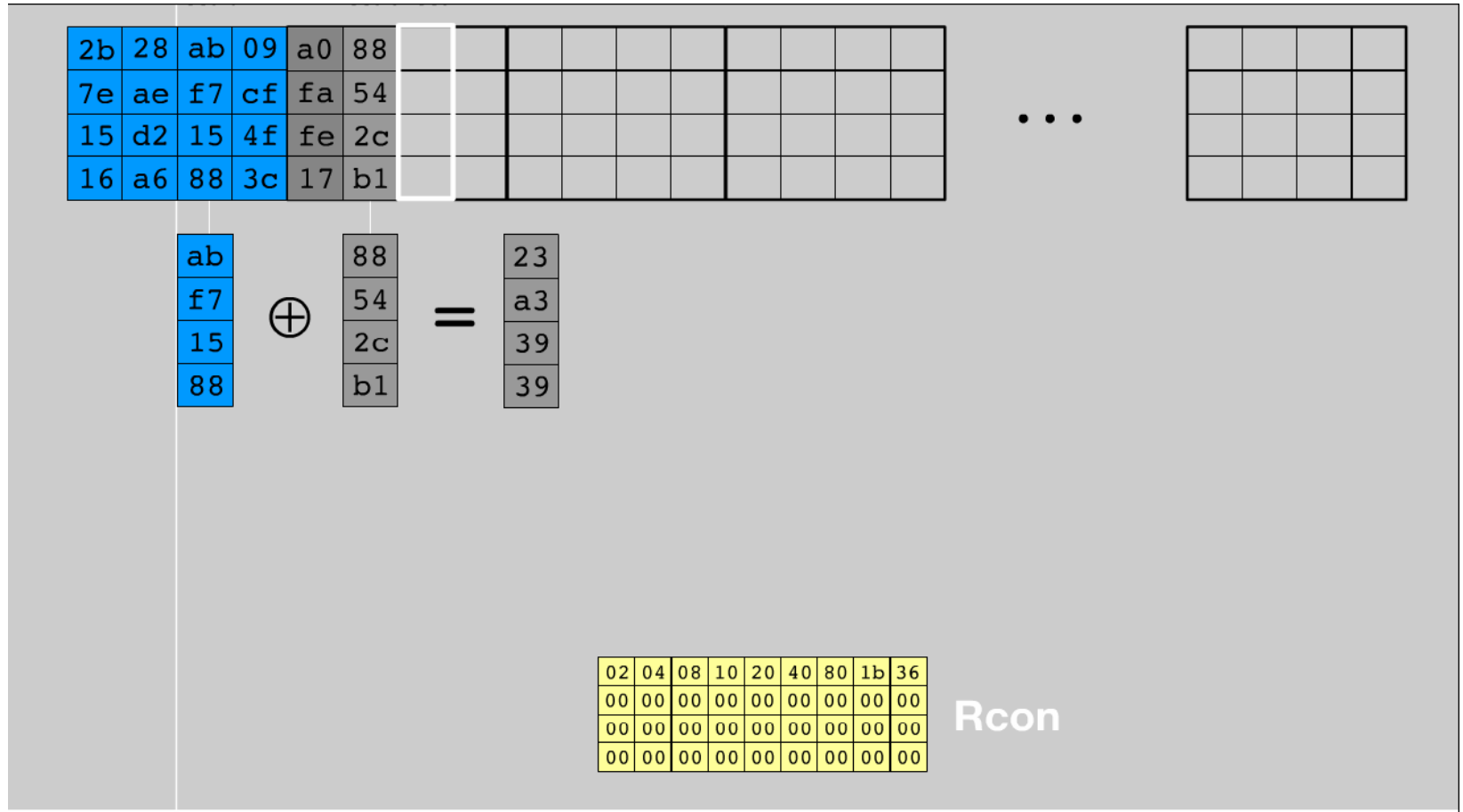


# Key Schedule Generation (Cont.)

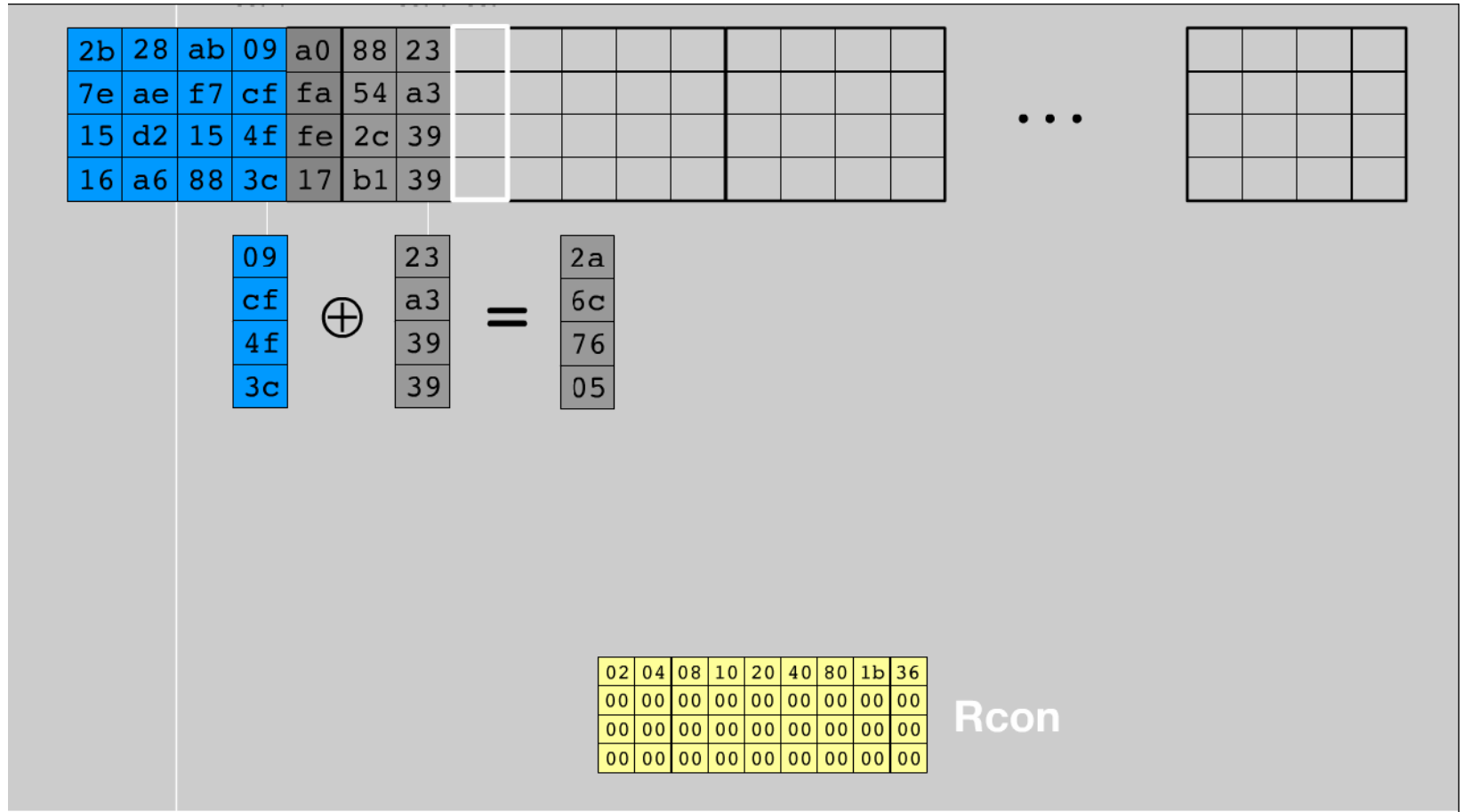




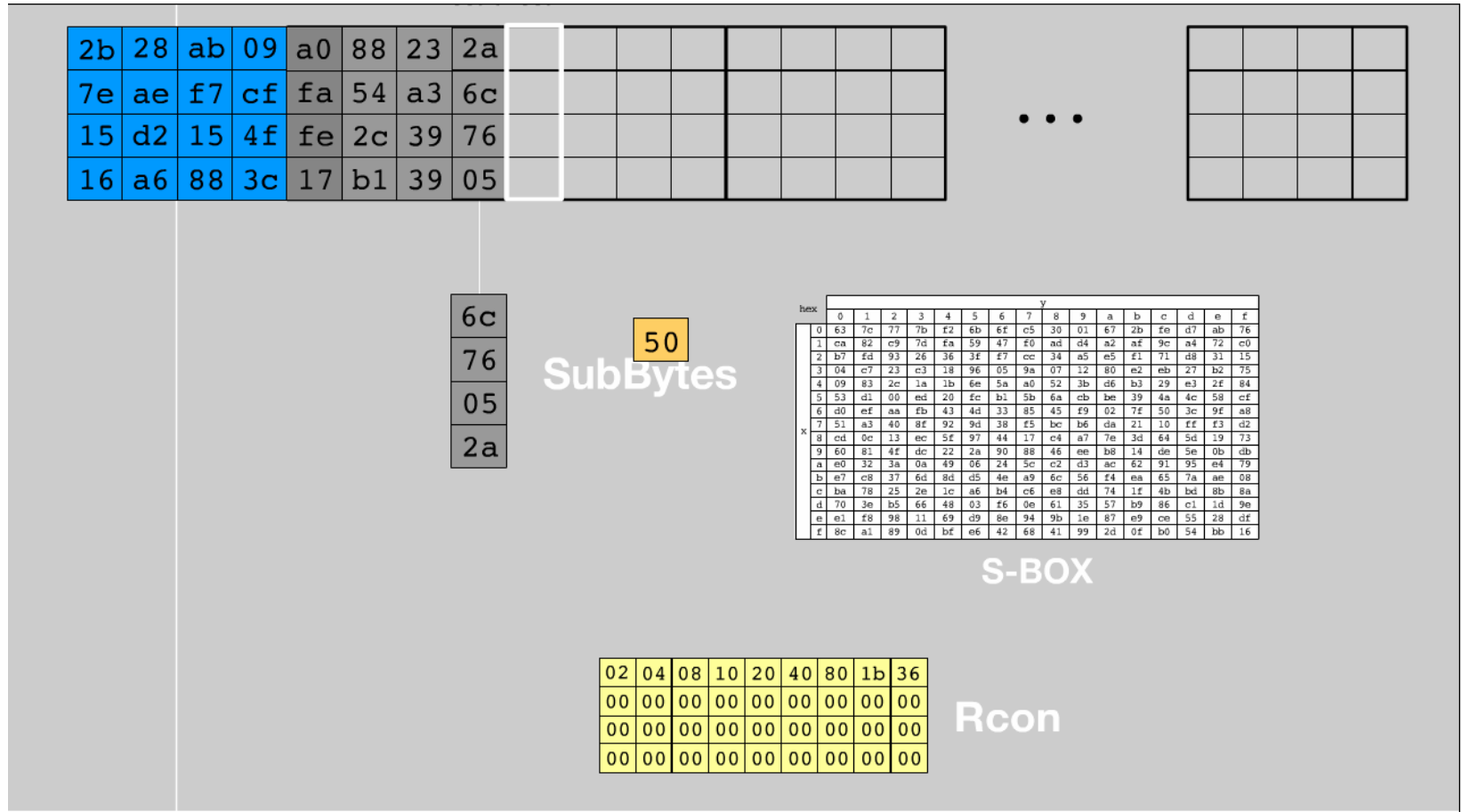
# Key Schedule Generation (Cont.)



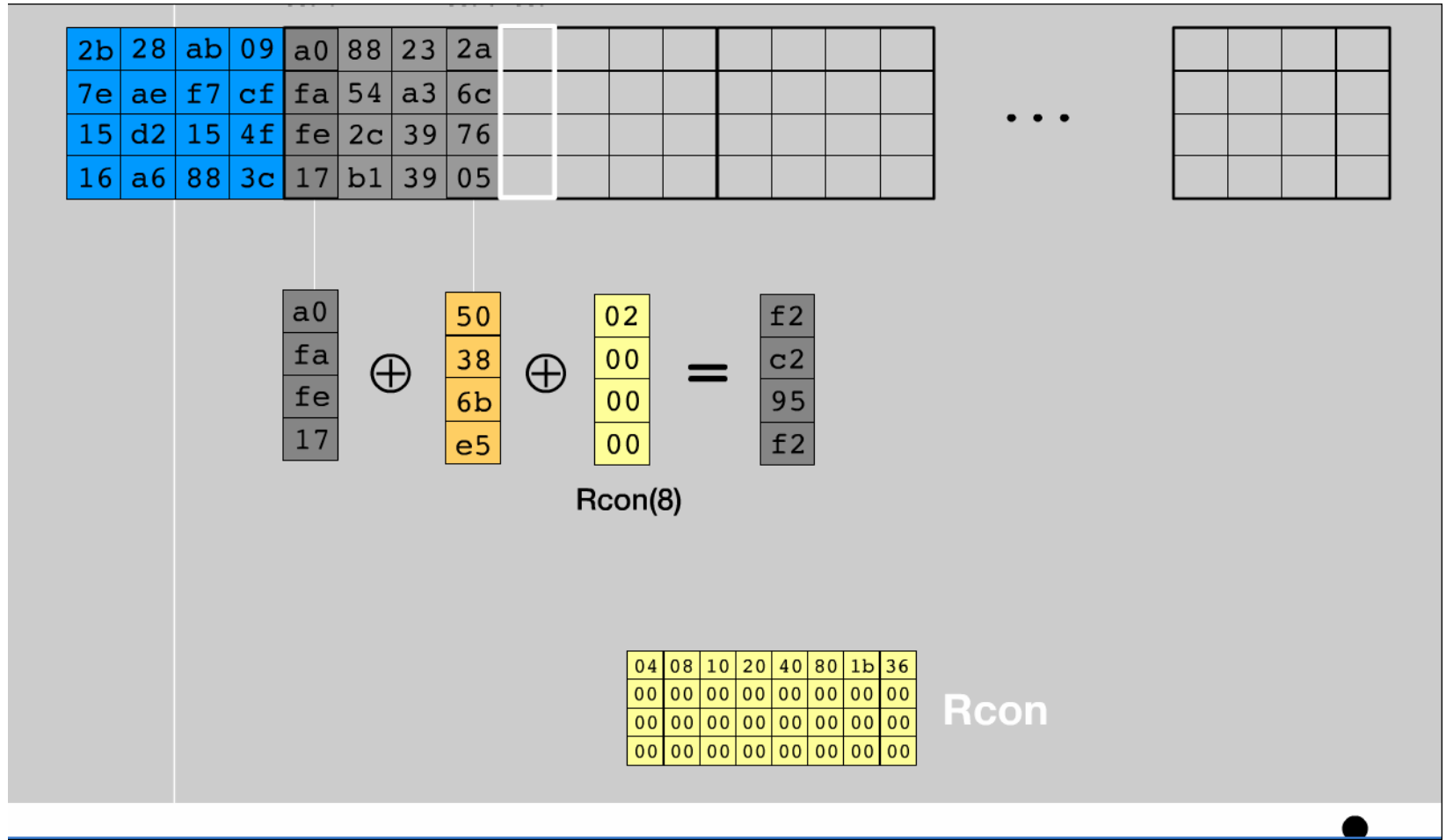
# Key Schedule Generation (Cont.)



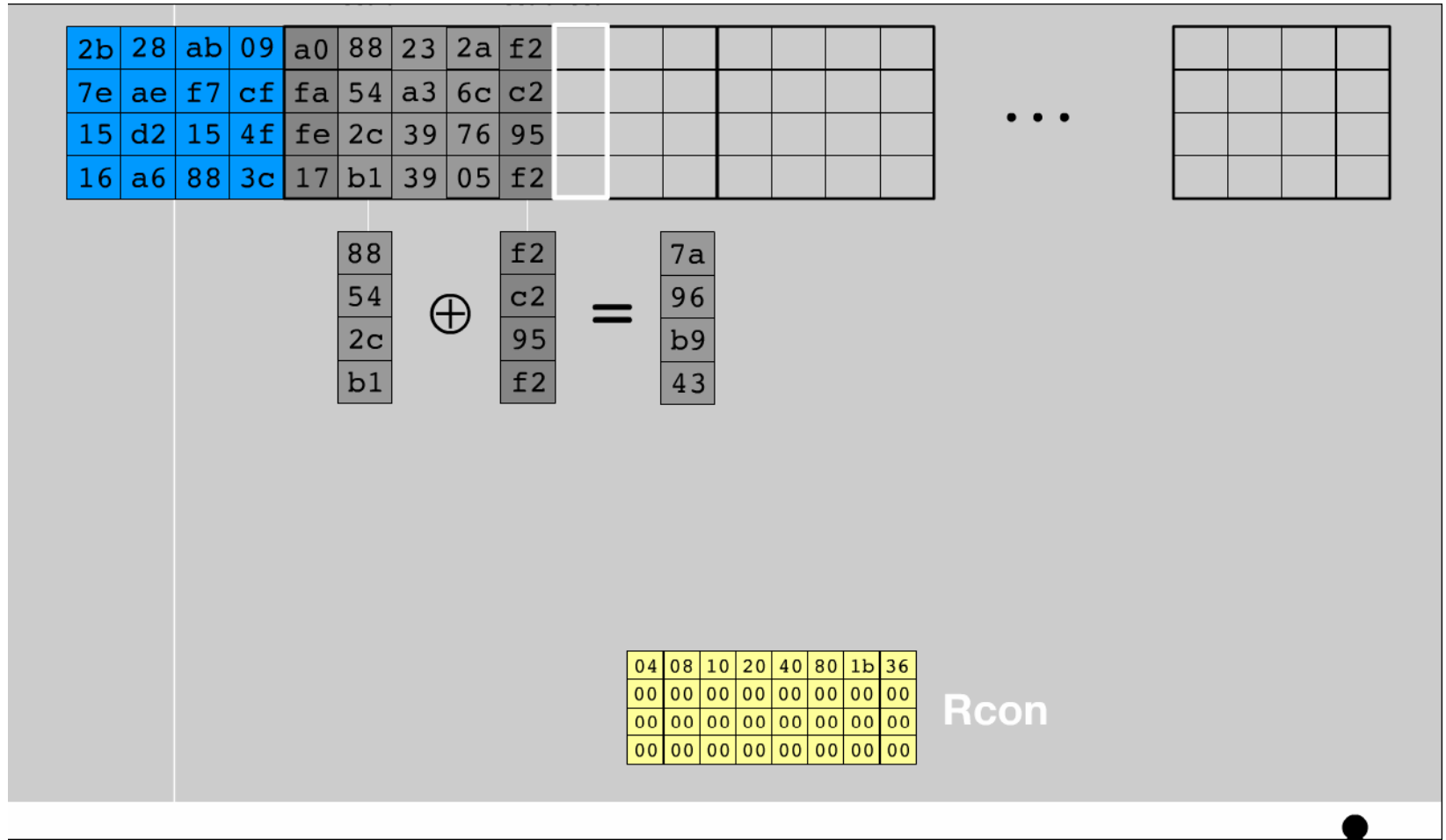
# Key Schedule Generation (Cont.)



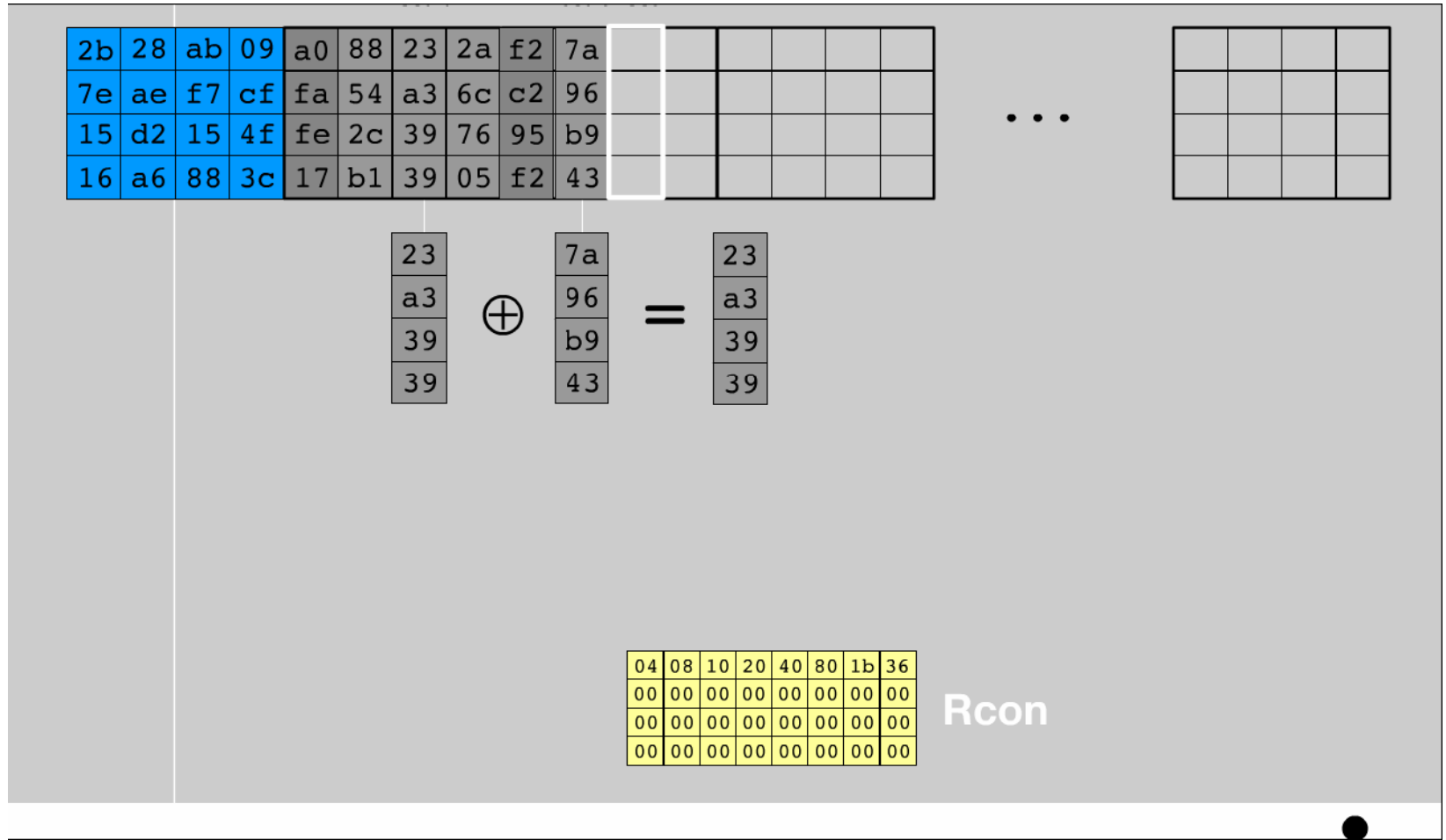
# Key Schedule Generation (Cont.)



# Key Schedule Generation (Cont.)



# Key Schedule Generation (Cont.)



# Key Schedule Generation (Cont.)

2b	28	ab	09	a0	88	23	2a	f2	7a	23	73	3d	47	1e	6d					d0	c9	e1	b6
7e	ae	f7	cf	fa	54	a3	6c	c2	96	a3	59	80	16	23	7a					14	ee	3f	63
15	d2	15	4f	fe	2c	39	76	95	b9	39	f6	47	fe	7e	88					f9	25	0c	0c
16	a6	88	3c	17	b1	39	05	f2	43	39	7f	7d	3e	44	3b					a8	89	c8	a6
Cipher Key				Round key 1				Round key 2				Round key 3								Round key 10			