EE 421 / CS 425 Digital System Design Lab 01
Fall 2024

Electrical Engineering Department
LUMS

Instructor: Dr. Shahid Masud
TA: Ibraheem

Objectives

1. Nuances of Complex Hardware Design and Implementation (see presentation)
2. Role of Hardware Description Languages and EDA Tool Flow (see presentation)
3. Setting up the simulation and synthesis tools for the upcoming labs (this manual)

Table of Contents

REFER TO APPENDIX A FOR INSTALLATION GUIDE OF SOFTWARE

The deadline to complete these tasks in Monday, 9 September, 2024, 5:00 pm PKT.