

TOP > Learn Know-how > Simulation > Foundations of Electronic Circuit Simulation Introduction
> SPICE Device Models: Diode Example-Part 1

Simulation

Download Technical
Documents

SPICE Device Models: Diode Example-Part 1

2019.05.09

Points of this article

- There are two types of SPICE models: “device models” and “subcircuit models”.
- In SPICE device model, each parameter value is set.

In the [previous article](#) it was explained that models used in SPICE simulations can be device models or subcircuit models. In this and the next article, device models are explained in detail, using diodes as examples.

SPICE Device Models: Principle of Operation of Diodes

Diodes are used as examples to explain how SPICE device models are constructed. To this end, we first review the principle of operation of a diode. Here semiconductor theory is involved, but what is important is the parameters used in the device model, and so there is no real need for an understanding of the theory itself.

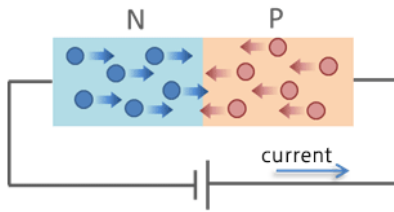
The diagram below illustrates the basic principle of operation of a silicon diode. When a bias voltage is applied to the diode, the electric field in the depletion layer weakens, carrier diffusion occurs, and a current flows. The forward current I_d and the depletion layer capacitance C_j of the diode can be

Downloadable materials, including lecture materials from ROHM-sponsored seminars and a selection guide for DC-DC converters, are now available.

[See list of Documents](#)

Simulation

- [Optimization of Inverter Circuits — Introduction —](#)
- [Foundations of Electronic Circuit Simulation Introduction](#)
- [About the ROHM Solution Simulator](#)
- [Optimization of PFC Circuits](#)



Forward current

$$I_d = I_s \cdot \left\{ \exp\left(\frac{q}{KT} V\right) - 1 \right\}$$

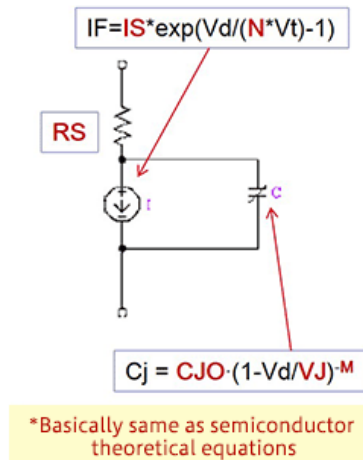
Depletion layer capacitance

$$C_j = C_{js} / \left(1 - \frac{V}{V_{bi}}\right)^M$$

Diode Parameters

The diagram below is the equivalent circuit of a diode. The forward current I_F (represented by I_d in the equation above), depletion layer capacitance C_j , and parasitic resistance R_S are shown. Upon substituting the diode parameters (see the table) into the above equations, we obtain the following model equations.

Parameter	Description	Unit	Default Value
AF	Flicker noise exponent	-	1
BV	Reverse breakdown knee voltage	volt	Infinite
CJO	Zero-bias p-n junction capacitance	farad	0
EG	Bandgap voltage (barrier height)	eV	1.11
FC	Forward-bias depletion layer capacitance coefficient	-	0.5
IBVL	Low-level reverse breakdown knee current	amp	0
IBV	Reverse breakdown knee current	amp	1.00E-10
IKF	High-injection "knee" current	amp	Infinite
IS	Saturation current	amp	1.00E-14
ISR	Recombination current parameter	amp	0
KF	Flicker noise coefficient	-	0
M	p-n junction grading coefficient	-	0.5
N	Emission coefficient	-	1
NBV	Reverse breakdown ideality factor	-	1
NBVL	Low-level reverse breakdown ideality factor	-	1
NR	isr emission coefficient	-	2
RS	Parasitic resistance	ohm	0
TBV1	bv temp. coefficient (1st)	°C ⁻¹	0
TBV2	bv temp. coefficient (2nd)	°C ⁻²	0
TIKF	ikf temp. coefficient (1st)	°C ⁻¹	0
TRS1	rs temp. coefficient (1st)	°C ⁻¹	0
TRS2	rs temp. coefficient (2nd)	°C ⁻²	0
TT	Transition time	sec	0
T_ABS	Absolute temperature	°C	
T_MEASURED	Measured temperature	°C	
T_REL_GLOBAL	Relative to current temperature	°C	
T_REL_LOCAL	Relative to AKO model temperature	°C	
VJ	p-n junction potential	volt	1
XTI	IS temp. coefficient	-	3



Model equations are essentially the same as semiconductor theoretical equations. In a device model, these parameter values are set.

SPICE Device Models: Case Study of Diode Device Model

A device model of an actual diode is presented. This model is provided by

Comments (model description, etc.)

```

* DRR264M-400 D model
* Model Generated by ROHM
* All Rights Reserved
* Commercial Use or
* Resale Restricted
* Date: 2008/10/06
.MODEL DRR264M-400 D
+ IS=599.20E-12
+ N=1.6555
+ RS=62.086E-3
+ IKF=5.5206
+ CJO=106.14E-12
+ M=.43343
+ VJ=.47628
+ ISR=1.4138E-9
+ NR=3
+ BV=400
+ TT=6.9000E-6

```

SPICE Model Description Rules

- When a line begins with an asterisk (*), the line is commented out (when there is a semicolon (;) midway in a line, everything following the semicolon is commented out).
- Parentheses () enclosing parameters can be omitted.
- When a line is continued, a plus (+) is added to the beginning of the continued line.
- In a device model, the default value is used for any parameters not specified.

Although mentioned in the list of description rules, lines beginning with an asterisk (*) are commented out. Simple explanations of the model and the like can be provided as desired. In this example, the actual description begins with “MODEL”. Lines beginning with pluses (+) set parameter values. Please compare the device model description and the parameter table. When a default value can be used as the parameter setting, a value need not be stated.

(Continued to Part 2)

Information on Downloading Technical Documents

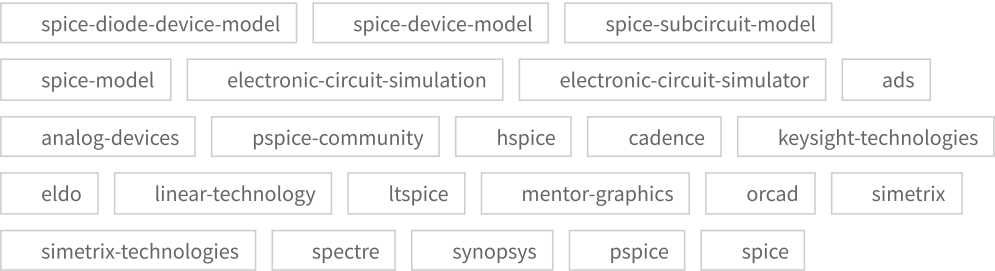
Downloadable materials, including lecture materials from ROHM-sponsored seminars and a selection guide for DC-DC converters, are now available.

See list of Documents

List of articles related to the「SPICE Device Models: Diode Example–Part 1」

- [Foundations of Electronic Circuit Simulation Introduction](#)
- [About SPICE](#)
- [SPICE Simulators and SPICE Models](#)

- [Types of SPICE Model](#)
- [SPICE Device Models: Diode Example–Part 2](#)
- [SPICE Subcircuit Models: MOSFET Example—Part 1](#)
- [SPICE Subcircuit Models: MOSFET Example—Part 2](#)
- [About Thermal Models](#)
- [About Thermal Dynamic Model](#)
- [SPICE Subcircuit Models: Models Using Mathematical Expressions](#)
- [Summary](#)



TOP > Learn Know-how > Simulation > Foundations of Electronic Circuit Simulation Introduction
> SPICE Device Models: Diode Example–Part 1

- Learn Devices
 - AC-DC
 - LED
 - SiC Power Device
 - Motor Driver
 - Stepping Mortor

- DC-DC
 - Si Power Device
 - IGBT
 - Brushed Motor
 - Brushless Mortor
- Learn Know-how
 - Thermal design
 - Simulation
 - Switching Noise
 - Transfer Function

- Technology Trends
 - Engineer Column
 - Product Key Points
 - Ask Direct to Engineers
- Download Technical Documents
 - ROHM Corporate
 - Privacy Policy
 - Terms of Service
 - SNS Terms of Use

Powered by



- Japanese - 简体中文 - 繁體中文 - 한국어

© Copyright 2024. TechWeb Powered by ROHM.