

# AMIT C RAIKAR

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## PROFESSIONAL SUMMARY:

- Good knowledge of Core java concepts.
- Good knowledge on OOPS concepts.
- Good knowledge in Libraries, Collections, Exceptional handling.
- Basic knowledge of data and RDBMS.
- Good in SQL Statements such as DML,DDL,TCL and DCL.
- SQL functions, SQL joins, Normalizations.
- Worked on web-technologies like HTML, CSS.
- Good Communication Skills.

## TECHINICAL SKILLS:

**Languages :** Java, SQL

**Web Design & Programming :** HTML,CSS

**IDE :** Eclipse, Editplus, SQLplus.

**Operating System :** Windows 7/8,Red Hat.

**Tool :** Cadence Virtuoso.

## WORK EXPERIENCE:

Worked as a Technical Desktop Support Engineer in CARE IT Solution, Bangalore.

## EDUCATIONAL QUALIFICATIONS:

- **BE (Electronics and communication), Batch-2015** from **KLE College of Engineering and Technology**, Chikodi.  
**Percentage: 60%**
- **Pre-University College (Science)** from **SDM PUC College**, Ujire.  
**Percentage: 65%**
- **SSLC** from **Sneha Sagar School**, Yellapur.  
**Percentage: 70%**

## PROJECT:

**Title:** Design of High Performance Double Edge Triggered D Flip-Flop based Shift Registers using MOSFET.

### **Description:**

The low power VLSI circuits applications the power consumption is a crucial. We proposed a new Double Edge Triggered D-Flip Flop (DETFF) which is suitable for low power applications. The proposed DETFF having minimum number of clocked transistors than existing designs. In the proposed design the transmission gates are replaced by NMOS to reduce the power. Simulation using SPICE and a 180 nm CMOS technology shows that this DETFF has ideal logic functionality, a simpler structure, lower lowest average power and least delay than existing designs. Further, the average power and the Delay are improved when compared with existing design respectively, which showing that proposed design is appropriate for low power and high performance applications.

**Tools Used:** Cadence Virtuoso( 180nm Technology ).

### **Deliverable/Challenges Faced:**

- DETFF has minimum number of Clocked transistors.
- The Power is reduced as compared to existing design.

## COURSES:

JAVA, SQL, HTML,CSS in **Jspiders**, Bangalore.

## WORKSHOP:

Attended Workshop on ” **BigData and Hadoop**” conducted by CEGONSOFT Pvt Ltd, Bangalore.

## Personal Profile:

<b>Name:</b>	AMIT C RAIKAR
<b>Date of Birth:</b>	03/SEP/1993
<b>Address:</b>	S/O CHANDRAKANTH R S KURDEKAR,M G ROAD HAVERI KARNATAKA PIN CODE: 581110
<b>Father Name:</b>	CHANDRAKANTH
<b>Nationality:</b>	INDIAN
<b>Sex:</b>	MALE
<b>Languages known:</b>	ENGLISH, KANNADA, HINDI, KONKANI

I hereby declare that the above information and details provided by me are correct to the best of my knowledge.

Date:

Place: Bangalore

(AMIT C RAIKAR)