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## POOJA VERMA

**OBJECTIVE** To continue my Career and utilize my prowess in the industry to advance the PROSPECTIVE COMPANY.

- SKILLS**
- PCB testing and debugging
  - Analyses of PCB schematics and fault finding.
  - Operation of CRO, function generators, power supplies, multimeter, autotransformer and other testing equipment.
  - Testing and debugging of modules for aircraft cockpit.
  - Test report preparation and documentation.
  - Knowledge of organizational workflow.

**EXPERIENCE** **7 YEARS EXPERIENCE IN ELECTRONICS HARDWARE TESTING IN HINDUSTAN AERONAUTICS LIMITED,**  
**(AVIONICS DIVISION, KORWA (AMETHI) U.P).**  
**Project 1: Optical location System OLS (36Sh-01)**  
From July, 2007 to Oct, 2008  
**Project Description:** This system does search, locking and automatic tracking of air targets as per their heat emission (Infra-Red). This system is mounted in cockpit of Sukhoi-30 MKI fighter aircraft.

**Role in Project:**

- Test bench setup for PCB testing and debugging.
- System level testing of the unit as per production test procedure document.
- Debug and resolve issues related to system performance by evaluating defective products.
- Responsible for problem report/change request tracking and report preparing to address the defect.

**Project 2: Data Conversion & Coupling device (BSPI-6-03)**

**From Nov 2008 to June 2010**

**Project Description:** This system accomplishes conversion of information (analog to digital & digital to analog) for ensuring mutual action of internal systems and units of complex.

Role in Project:

- Installation/commissioning of the outsourced test equipment for facilitating to the testing of modules for avionics applications.
- Testing and debugging of Analog and Digital PCBs.
- Preparing of necessary documentation (Job Cards, Test Sheets) for the testing and validation as required by Quality Assurance Department.
- Support in endurance test, thermal shock test, hot-cold test and vibration test at system as well as PCB level.

**Project 3: Complex Information System of Signaling (Kiss)**

**From Aug 2011 till now**

**Project Description:** This system delivers the information about failures of main system to crew in flight.

**Role in Project:**

- Electrical drawing(Schematic) analysis and fault finding of PCBs
- Led the team of three for system level testing and impart training to new engineers in the use of various equipment required for system and PCB level testing.

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EDUCATION

- AMIE in Electronics & Communication engg. from THE INSTITUTE OF ENGINEERS (INDIA), Kolkata, passed in 2011 with GPA 7.78.
- Diploma in Electronics & Communication engg. from U.P. Technical Board, Lucknow, passed out in 2005 with 83.4%.
- Class 12<sup>th</sup> from UP BOARD, passed out in 2002 with 64.3%.
- Class 10<sup>th</sup> from UP BOARD passed out in 2000 with 66%.

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COMPUTER SKILL

- MS Office, Power Point.
- Operating system: Windows XP, Windows 2007, Windows 8.

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ACADAMIC PROJECTS

- In Diploma- Digital MOSFET Inverter.
- In AMIE- Study the functioning of Optical-Electronic Aiming & Navigational Complex OEPrNK-30MK-03 in SUKHOI-30MKI

PROFFESIONAL  
TRAINING

One year apprentice training in Testing Department in RADAR Division in Bharat Electronics limited, Ghaziabad.

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PERSONAL  
INFORMATION

- Date of Birth: 1<sup>st</sup> January 1986
- Marital Status: Married
- Language Known: Hindi & English
- Nationality: Indian

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DECLARATION

I hereby declare that the details are true to the best of my knowledge.

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**POOJA VERMA**