**NAVEEN KUMAR G.R.**
#167 , 7th Main, 1 stage,
West of chord road,
Maha Ganapathi nagar,
Rajajinagar, Bengaluru-10.
Mobile   : +91-9611102721
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## Career Objective:

Seeking a position to utilize my skills and abilities in any environment of a Industry that offers Professional growth while being resourceful, innovative and flexible. To prove myself to be an asset to the organization. To continuously grow and learn new and innovative things.

## SYNOPSIS:

➢ An Engineering graduate with 1 years of experience as **Embedded systems R&D Engineer Trainee.**
➢ Specialization in Electrical & Electronics from **Visvesvaraya Technological University, Belagavi.**
➢ Professional diploma in Embedded System Design from **CEITR(Centre for Electronics & Information Technology Research).**
➢ Experience in developing working models based on Embedded systems.
➢ Areas of interest include Digital Signal Processing, Embedded Systems, Consumer Electronics and IoT.
➢ Adept at decision making through analysis of quantitative and qualitative data.
➢ In-depth knowledge of Operating Systems and Embedded Systems Concepts.

## TECHNICAL EXPERTISE:

| | |
|---|---|
| Programming Languages | C, Embedded C, Assembly Programming, Core Java, J2EE |
| Processors | ARM7TDMI-S |
| Controllers | AT89C51, PIC16F877A, LPC2148 |
| Protocols | RS232, SPI, I2C |
| RTOS | VxWorks 6.7 |
| Tools Used | Keil uV4 for 8051, AT89C51, ARM, MPLAB & Hi-tech C compiler for PIC16F877A, PROTEUS8 Simulator, Wind River VxWorks 6.7 for RTOS Programming, Linux (Ubuntu), GCC Compiler Vi editor. |
| Boards | 8051 Development Board, PIC16F877A Development Board, , ARM LPC2148 Development Board |

## CAREER CONTOUR( 1 YEAR + 7 MONTHS):

➢ Company Name        : **CEITR(Centre for Electronics & Information Technology Research)**
 Designation         : Embedded systems R&D Engineer Trainee
 Duration            : 1 Year(16 MARCH 2015 to 31 MARCH 2016)
 Responsibilities     : Testing of Microcontroller based control unit equipment's. Involved in coding of the C/Embedded C. Worked on communication protocols like RS232, I2C, SPI, UART, and USART. Involved in interfacing the controller with hardware and other software modules for system check and in programming the system. Prototype equipment testing and demonstrating. Flashing the source code into the Microcontroller using ISP.

| Project Name | Electronic code lock with user defined Password |
|---|---|
| Institution | CEITR(Centre for Electronics & Information Technology Research) |
| Parts used | PIC16F877A microcontroller, Keypad, switch ,L293D & DC Motor |
| Technologies/Tools | MPLAB IDE ,Proteus |

An electronic lock or digital lock is a device which has an electronic control assembly attached to it. They are provided with an access control system. This system allows the user to unlock the device with a password. The password is entered by making use of a keypad. While unlocking, if the entered password from keypad matches with the stored password, then the lock opens and a message is displayed on LCD & the door is opened. If the security code is wrong, 'Wrong Password' is sent to be displayed on LCD and given again a chance to enter password.

➢ Company Name        : **Hofincons InfoTech and Industrial services**
 Designation         : Electrical Technician
 Duration            : 7 Months (18 AUGUST 2014 to 14 MARCH 2015)
 Responsibility       : Maintenance of Panel Boards, Generator sets, DBs, UPS etc., Electrical Equipment Periodical Readings, Vendor co-ordination, Basic electrical wiring activities, Facility related works, Assistance in Security and Housekeeping management, Local Purchases and Inventory Management, Building Maintenance Activities, Office Administrative Activities.

## CERTIFICATIONS:

| Level | Institution | Duration |
|---|---|---|
| P.G. Diploma in Embedded System | CEITR (Centre for Electronics & IT Research) Bangalore | 6 months |
| Core java/J2EE | Arcus InfoTech Bengaluru | 4 Months |

## ACADEMIC:

| Level | Board/University | Branch | Result | Year of Passing |
|---|---|---|---|---|
| B.E. | SDM Institute of Technology, Ujire | Electrical and Electronics | 63% | 2014 |
| PUC | DVS Composite PU College, Shivamogga | PCMB | 55% | 2010 |
| SSLC | St. Mary's High School, Bhadravathi | - | 83.84% | 2007 |

| Project Name | Water level detection using GSM module |
|---|---|
| Institution | Engineering College |
| Role | Academic |
| Department | E&E |
| Contribution | Hardware design, assembling and Analysis |
| Technologies/Tools | PIC16F877A microcontroller, Hi-Tech C compiler, Proteus 8.1 |

In this project we have discussed about design and implementation of water level control system which is wireless, automatic, cost effective and reliable. It uses two Radio Frequency transceivers along with a controller each installed at the tank. Radio Frequency transceivers are used for wireless communication. It is completely automated with the help of a PIC-microcontroller. Installation cost is reduced since the system is wireless. It is reliable because it has no problems arising after installation such as breakage of wire.

## INTERNSHIP DETAILS:

Company Name      : Visvesvaraya Iron and Steel Plant
Designation       : In plant Trainee
Duration          : 1 Months
Responsibility    : In plant trainee on Plant's Electrical system

**ACADEMIC SEMINAR :** Electrical Breakdown in High-voltage Winding Insulation of Different Manufacturing Qualities.

## ACTIVITIES & ACHIEVEMENT:

➢ Attended training program on PIC-16F series Microcontroller conducted by "**Swadeshee**" Proprietor **'Vishnuprasada V Bhat'** at our college campus.
➢ Participated in three days' workshop on "**Entrepreneurship Awareness Camp**" by **RUDSETI** organized by our College Cultural Committee.
➢ Involved in various cultural activities held at School and College level.
➢ Took part in cricket and basketball tournaments held at school and college level.

## ORGANISATIONAL & PERSONAL SKILLS :

➢ Ability to work and cooperate in a team and to deal with people diplomatically.
➢ Have ability & interest to learn and apply technical skills.
➢ Person with PMA- Positive Mental Attitude.
➢ Good at adapting myself to new challenges.
➢ Determined and willing to work hard to achieve my goal.
➢ Good in communication & Negotiation skills, organizing and managing things.
➢ Emphasis on team working to achieve the end objective as cultivated.
➢ Constant learning process to acquire knowledge on the latest developments in the working field was demonstrated.

## PERSONAL DETAILS :

| | | | |
|---|---|---|---|
| Mother's Name | : Nalinakshamma | Father's Name | : Late Rudrappa P |
| Gender | : Male | Date of Birth | : 15/12/1990 |
| Nationality | : Indian | Languages Known | : Kannada, English, Hindi, Telugu |

Permanent Address : Old seegebagi, Bhadra colony post,
                    Bhadravathi, Shivamogga - 577301

I hereby declare that the above given statements are true and correct to the best of my knowledge and belief.

Place : Bengaluru                                                          yours truly,

                                                                    **(NAVEEN KUMAR G.R.)**