
H R VANISHREE, M.TECH

Banashankari, Bangalore

Cell: +91 9480014902

hr19vanishri@yahoo.com

SUMMARY

To become a successful professional in a highly competitive technological world where performance is rewarded with new challenging responsibilities and to execute versatile, high-energy technocrat projects of large magnitude within strict time schedule.

ACADEMIC CREDENTIALS

Post Graduation: MTECH from SJBIT College Bangalore.

Year of passing : 2016

Aggregate : 77.83%

Graduation: B.E from KLE College of Engineering, Belagavi

Year of passing : 2012

Aggregate : 74.69%

12th Standard: KLE College Nipani, Belagavi

Year of passing : 2008

Aggregate : 70.33%

10th Standard: KLE School Nipani, Belagavi

Year of passing : 2006

Aggregate : 82.72%

ACADEMIC PROJECT

Graduation Project Name : Advanced office automation system

Technology : Java, Embedded-C.

Hardware : RFID, Microcontroller, GSM modem.

Features:

This aims in taking attendance of the employee using rfid technology. The employee id number is verified with the number saved in the database, whenever employee swaps his card, his number is verified against the database, if both Numbers are same then a welcome sms is sent to

the employee, his pc will be turned on automatically. Whenever employee leaves the office his pc will be turned off automatically, whenever employee is late by 30 or more minutes, sms will be sent to the boss. Boss can stop and start receiving sms.

Post Graduation Project Name : An efficient and low-cost methodology for mitigation of multiple errors in TMR system

Technology : Verilog.

Features:

In this project, an error recovery roll-forward technique called scan-chain scheme is presented, which eliminates the drawback of ScTMR. The concept behind this technique is the reusability of the present scan-chains which are assigned for testing purposes and these are now used to examine the inner status of TMR system. Here in this project, in Recovery mode the extra circuitry is eliminated and the recovery signals are directly connected to the modules. This saves the execution time and the cost of the implementation will also be reduced.

ACCOMPLISHMENTS

- Won National level **GROUP DISCUSSION** (conducted in our engineering college).
- Won various prizes in athletics at school level.
- Won second prize in Chess competition.

PAPERS PRESENTED

- Paper presented on Artificial intelligence in KLE engineering college.
- Paper presented on DRAM in KLE engineering college.
- Published a paper on “Error detection and reduction using TMR systems” in IJIRCCE website.

PERSONAL DETAILS

Name	H R Vanishree
Father Name	H Ramappa
Date of Birth	19 th July 1990
Nationality	Indian
Languages Known	English,Kannada,Marathi and Hindi

DECLARATION

I do hereby declare that the statements made above are true and complete to the best of my knowledge and belief.