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Anand Jose

Career Objective

Seeking a challenging career in your esteemed organization where I can learn and grow dynamically as a person and as a professional along with the development of the firm.

Professional Experience (1.9 years)

Systems Engineer at *Alpha Design Technologies (ADTL), Bangalore* (Sept 2012 to present)

Embedded Engineer Trainee at *Cranes Varsity, Bangalore* (Nov 2011 to April 2012)

Technical Proficiency

Programming Languages	C, Embedded C
Operating Systems	Linux, VxWorks, FreeRTOS, Windows
Microprocessors	x86, PXA303
Microcontrollers	PIC24HJ256GP210, PIC16F877A, 8051
Protocols	TCP/IP, MIL-1553, ARINC 429

Projects

Identify Friend or Foe (IFF)

Duration : 2013 Aug – Present	Created different software modules in desktop like Transponder, Interrogator, CDU and Altimeter which are the sub-systems of IFF and made them communicating each other by IPC mechanism.
Team size : 6	
Processor used : x86	
IDE used : Eclipse	
Language, OS : C, Linux	
Compiler : GCC Compiler	

Alpha Radar IQ Modulator

Duration : 2013 Jun – 2013 Jul	Programmed PIC24HJ256GP210 and PIC16F877A microcontrollers to store and output I & Q values at a rate of 2MHz and 500KHz by monitoring trigger and pretrigger.
Team size : 2	
Controller used : PIC24H, PIC16F	
IDE used : MP Lab X	
Language : Embedded C	
Compiler : C30 Compiler	

Porting FreeRTOS on Interface Card (IFC)

Duration	: 2013 Mar – 2013 May	IFC card used for porting contains Marvell PXA303 processor, using ARM V5TE instruction set. The project was to create a FreeRTOS port for the IFC card. I have worked on changing timer registers, configuring interrupt controller registers, setting up scheduler for periodic task switching.
Team size	: 2	
Processor used	: PXA303	
IDE used	: Eclipse	
Language, OS	: C, FreeRTOS	
Compiler	: ARM-Linux-GCC Compiler	

Communication Interface Unit (CIU)

Duration	: 2012 Sept – 2013 Feb	CIU is an Interface unit to facilitate communication between two ends through 5 different mediums, namely VHF, HF, WD line, VSAT and FSK. I have particularly worked on establishing communication between two interface units through VSAT medium.
Team size	: 9	
Processor used	: x86 & PXA303	
IDE used	: Eclipse	
Language, OS	: C, Linux	
Compiler	: GCC compiler & ARM-Linux-GCC Compiler	

PC-based Wireless Pick& Place Robot

Duration	: 2010 Sept – 2010 Dec	Designed a pick and place robot which can be controlled by using a computer. The signal is sent to the robot using an ASK transmitter, which is connected to the serial port of the computer system. ASK receiver receives the signal and give to the microcontroller and then control the motors.
Team size	: 4	
Controller used	: 8051	
IDE used	: Keil µvision	
Language	: Embedded C	

Education

Qualification	Institute	Year	%
B.Tech (Electronics & Communication)	Vimal Jyothi Engineering College, Kannur University	2011	63.6
XII	Nirmala HSS, Chemperi, Kerala State Board	2007	86.0
X	St.Joseph's HS, Pulikurumba, Kerala State Board	2005	87.4

Personal Information

Date of Birth	: 05 August 1989
Gender	: Male
Languages Known	: English, Hindi, Malayalam
Place	: Banaglore