## JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY KAKINADA

KAKINADA - 533 003, ANDHRA PRADESH, INDIA

## CONSOLIDATED MARKS MEMO / CREDIT / GRADE SHEET

00539969

Bachelor of Technology in ELECTRONICS & COMMUNICATION ENGINEERING

Name: MOHAMMAD SHAHISTA ADIBA



POTTI SRIRAMULU CHALAVADI MALLIKARJUNA Name of the College: RAO COLLEGE OF ENGG & TECH Name & Year of Final Exam: B. Tech July 2021

	COURSE TITLE	GRADE	GRADE POINT		COURSE TITLE	GRADE	GRADE POINT	
			ı	YE	AR			
1 1	MATHEMATICS-I	Α	8	3	1 ENGLISH-II	В	7	
	ENGINEERING DRAWING	Α	8	3	2 MATHEMATICS - III	В	7	
	ENGLISH-I	Α	8	3	3 APPLIED CHEMISTRY	В	7	
	MATHEMATICS-II (NUM. METH.&COMPLEX VAR.)	С	6	3	4 ENVIRONMENTAL STUDIES	В	7	
	APPLIED PHYSICS	В	7	3	5 DATA STRUCTURES	В	7	1
	COMPUTER PROGRAMMING	Α	8	3	6 ELECTRICAL&MECHANICAL TECH.	В	7	
	ENGLISH-COMMUNICATION SKILLS LAB-I	0	10	2	7 ENGLISH - COMM. SKILLS LAB - II	S	9	
	ENGINEERING WORKSHOP & IT WORKSHOP	0	10	2	8 COMPUTER PROGRAMMING LAB	S	9	
9.1	APPLIED/ENGINEERING PHYSICS LAB	0	10	2	9 APPLIED/ENGINEERING CHEMISTRY LAB	0	10	
				YE	AR			1
1	ELECTRONIC DEVICES AND CIRCUITS	Α	8	3	1 ELECTROMAG. WAVES& TRANSMISSION LINES	В	7	
2	SWITCHING THEORY AND LOGIC DESIGN	В	7	3	2 PULSE AND DIGITAL CIRCUITS	S	9	
3	SIGNALS AND SYSTEMS	Α	8	3	3 MANAGEMENT SCIENCE	В	7	
4	NETWORK ANALYSIS	В	7	3	4 ELECTRONIC CIRCUIT ANALYSIS	0	10	
	RANDOM VARIABLES AND STOCHASTIC PROCES	S S	9	3	5 CONTROL SYSTEMS	Α	8	
5			10000	3	C ANALOG COMMUNICATIONS	В.	7	1
5 6	MANAGERIAL ECO. & FIN. ANALYSIS	C	6	- 3	6 ANALOG COMMUNICATIONS	В	1	- 1
	Annual Control	C A	6 8	2	7 ANALOG COMMUNICATIONS  7 ANALOG COMMUNICATIONS LAB	A	8	
6	MANAGERIAL ECO. & FIN. ANALYSIS		and the second			4000	T-100 WOOT 10	
6 7	MANAGERIAL ECO. & FIN. ANALYSIS ELECTRONIC DEVICES AND CIRCUITS LAB	Α	8	2	7 ANALOG COMMUNICATIONS LAB 8 ELECTRONIC CIRCUIT ANALYSIS LAB	Α	8	
6 7	MANAGERIAL ECO. & FIN. ANALYSIS ELECTRONIC DEVICES AND CIRCUITS LAB NETWORKS & ELECTRICAL TECHNOLOGY LAB	Α	8	2 2 4 K	7 ANALOG COMMUNICATIONS LAB 8 ELECTRONIC CIRCUIT ANALYSIS LAB	Α	8	
6 7 8	MANAGERIAL ECO. & FIN. ANALYSIS ELECTRONIC DEVICES AND CIRCUITS LAB NETWORKS & ELECTRICAL TECHNOLOGY LAB	A	8 8	2 2 1 YE	7 ANALOG COMMUNICATIONS LAB 8 ELECTRONIC CIRCUIT ANALYSIS LAB EAR	A O	8 10	
6 7 8	MANAGERIAL ECO. & FIN. ANALYSIS ELECTRONIC DEVICES AND CIRCUITS LAB NETWORKS & ELECTRICAL TECHNOLOGY LAB  LINEAR I C APPLICATIONS	A A	10 9 7	2 2 2 1 YE	7 ANALOG COMMUNICATIONS LAB 8 ELECTRONIC CIRCUIT ANALYSIS LAB  EAR  1 MICRO PROCESSORS & MICRO CONTROLLERS 2 DIGITAL SIGNAL PROCESSING 3 VLSI DESIGN	A O	8 10	
6 7 8 1 2	MANAGERIAL ECO. & FIN. ANALYSIS ELECTRONIC DEVICES AND CIRCUITS LAB NETWORKS & ELECTRICAL TECHNOLOGY LAB  LINEAR I C APPLICATIONS COMPUTER ARCHITECTURE&ORG.	A A O S B B	10 9 7 7	2 2 2 3 3 3 3 3	7 ANALOG COMMUNICATIONS LAB 8 ELECTRONIC CIRCUIT ANALYSIS LAB  LACK MICRO PROCESSORS & MICRO CONTROLLERS 2 DIGITAL SIGNAL PROCESSING 3 VLSI DESIGN 4 MICRO WAVE ENGINEERING	A O C C B C	8 10 6 6 7 6	
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6 7 8 1 2 3 4 5 6	MANAGERIAL ECO. & FIN. ANALYSIS ELECTRONIC DEVICES AND CIRCUITS LAB NETWORKS & ELECTRICAL TECHNOLOGY LAB  LINEAR I C APPLICATIONS COMPUTER ARCHITECTURE&ORG. DIGITAL I C APPLICATIONS DIGITAL COMMUNICATIONS ANTENNA AND WAVE PROPAGATION DIGITAL I C APPLICATIONS LAB	A A O S B B C O	10 9 7 7 6 10	2 2 3 3 3 3 3 3 2	7 ANALOG COMMUNICATIONS LAB 8 ELECTRONIC CIRCUIT ANALYSIS LAB  1 MICRO PROCESSORS & MICRO CONTROLLERS 2 DIGITAL SIGNAL PROCESSING 3 VLSI DESIGN 4 MICRO WAVE ENGINEERING 5 OOPS THROUGH JAVA 6 MICROPROCESSORS&MICROCONTR. LAB	C C B C C O	8 10 6 6 6 7 6 6 6	
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6 7 8 1 2 3 4 5 6 7 8 9	MANAGERIAL ECO. & FIN. ANALYSIS ELECTRONIC DEVICES AND CIRCUITS LAB NETWORKS & ELECTRICAL TECHNOLOGY LAB  LINEAR I C APPLICATIONS COMPUTER ARCHITECTURE&ORG. DIGITAL I C APPLICATIONS DIGITAL COMMUNICATIONS ANTENNA AND WAVE PROPAGATION DIGITAL I C APPLICATIONS LAB PULSE AND DIGITAL CIRCUITS LAB LINEAR I C APPLICATIONS LAB PROF. ETHICS&HUMAN VALUES  ELECTRONIC SWITCHING SYSTEMS EMBEDDED SYSTEMS RADAR SYSTEMS OPTICAL COMMUNICATIONS DIGITAL IMAGE PROCESSING	A A A O S B B C O O C C P A B B C C	8 8 8 10 9 7 7 6 10 10 0	2 2 2 3 3 3 3 3 2 2 2 2 0	7 ANALOG COMMUNICATIONS LAB 8 ELECTRONIC CIRCUIT ANALYSIS LAB  EAR  1 MICRO PROCESSORS & MICRO CONTROLLERS 2 DIGITAL SIGNAL PROCESSING 3 VLSI DESIGN 4 MICRO WAVE ENGINEERING 5 OOPS THROUGH JAVA 6 MICROPROCESSORS&MICROCONTR. LAB 7 DIGITAL COMMUNICATIONS LAB 8 VLSI LAB 9 IPR & PATENTS  EAR  1 WIRELESS SENSORS & NETWORKS 2 ELECTR. MEASUREMENTS & INSTRUM.	C C B C C O O C C P /	8 10 6 6 7 6 6 10 10 10 0	
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Number of Credits registered for : CGPA Secured:

180

7.93

Date of Declaration of Result: (See overleaf for Instructions) August 2021

\* CP^ - Completed

5/9/2021

CONTROLLER OF EXAMINATIONS

\* Madium of Instruction and Evaminations in English