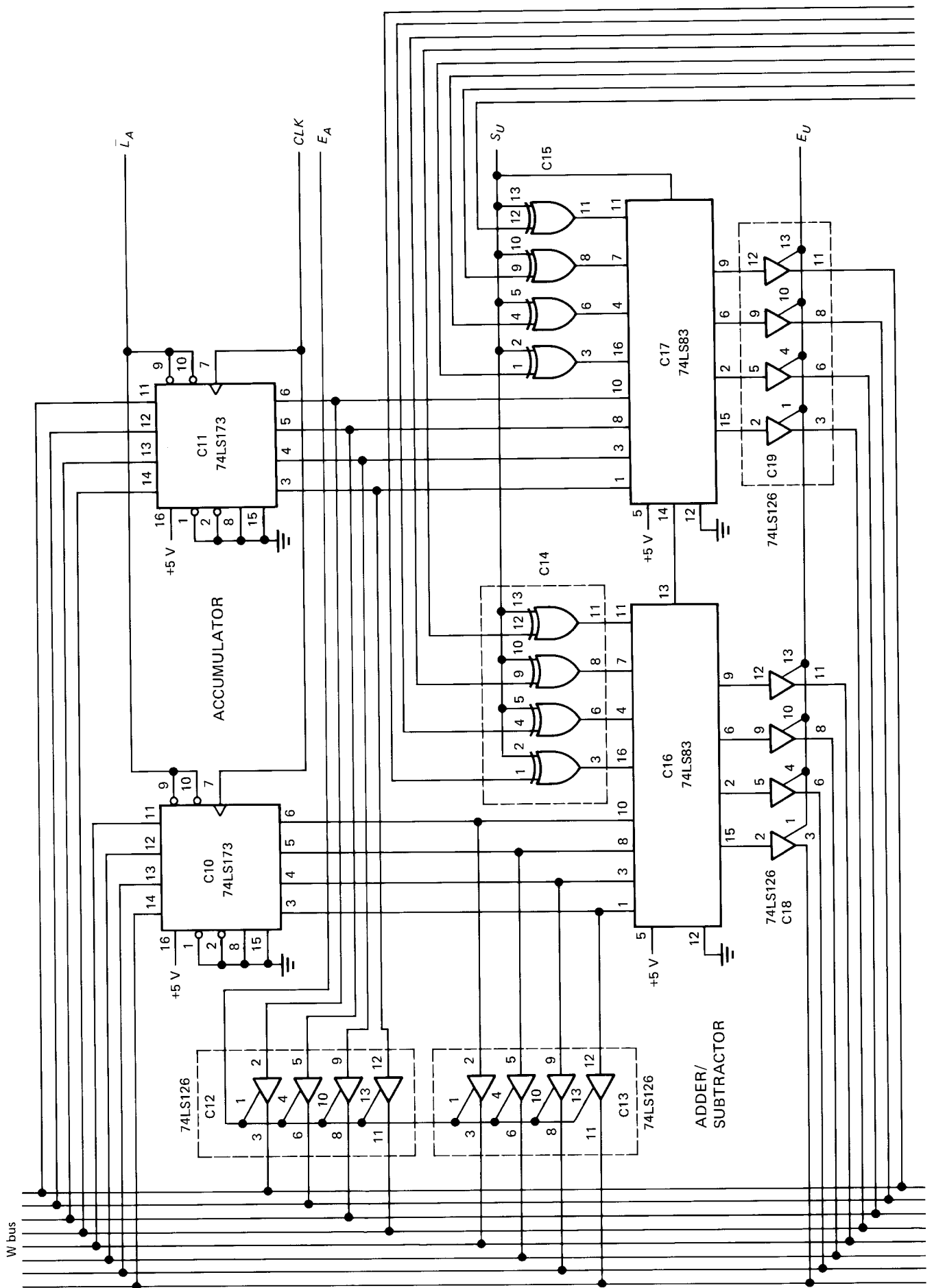


Fig. 10-12 SAP-1 program counter, memory, and instruction register.



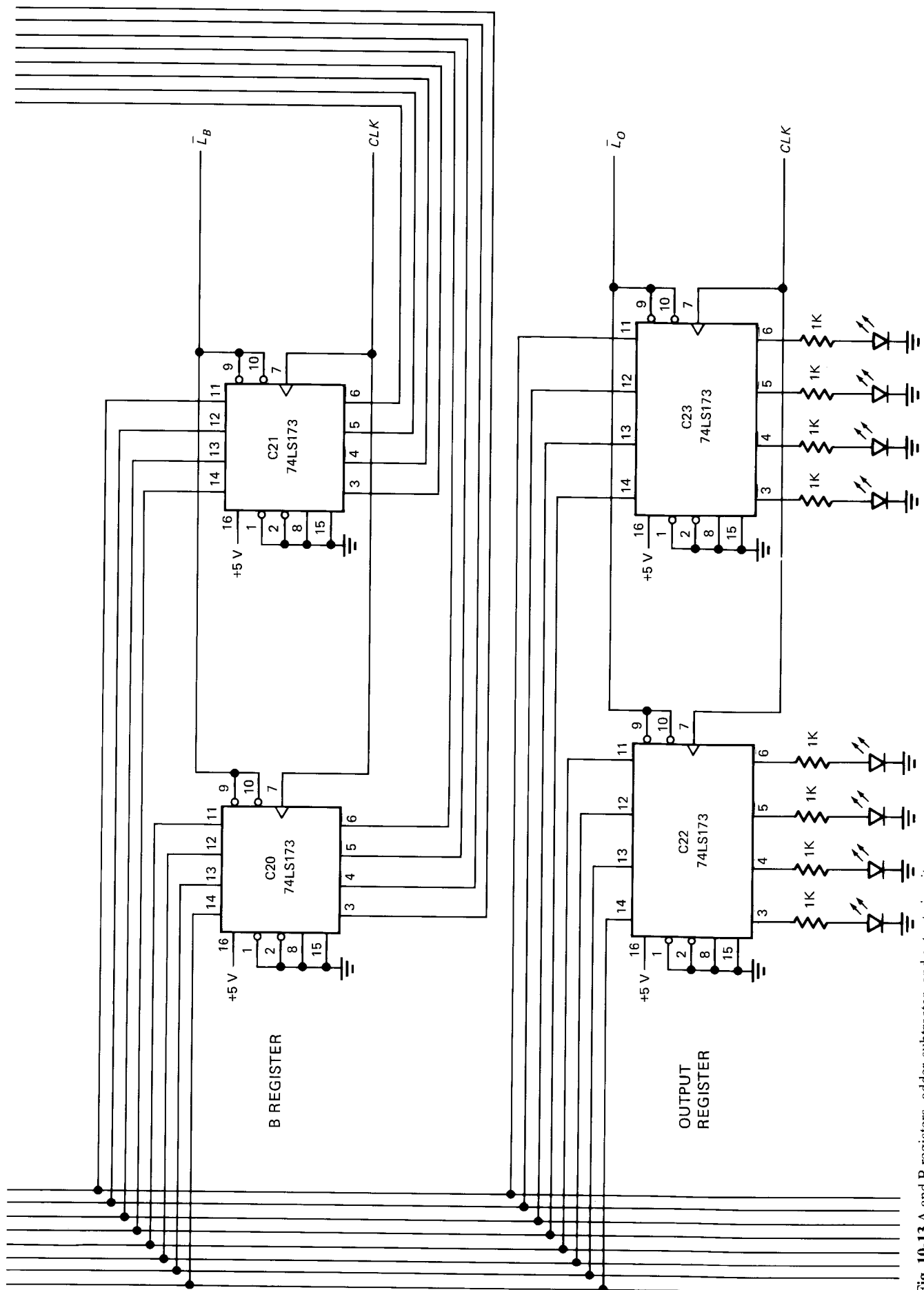


Fig. 10-13 A and B registers, adder-subtractor, and output circuits.