## LAB REPORT

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CSE209; ELECTRICAL CIRCUIT

Experiment no: 05
Group no: 01

## **Experiment name:**

Verification of Superposition Theorem

#### Name of students & Id:

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## **Course Instructor information:**

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## **Date of Report Submitted:**

3 December ,2022

#### Abstract:

Superposition theorem works for linear circuits. The superposition theorem states that if a linear circuit contains more than one source, the voltage across or the current through any element may be determined by algebraically adding the contribution of each source acting alone with other sources remaining inactive. A voltage source is made inactive by setting its voltage value to zero. In this experiment we verify the superposition theorem using PSpice simulation. For this, we use four circuit and some fixed resistors and voltage sources.

### **OBJECTIVE:**

To obtain knowledge and verify the superposition theorem theoretically and using PSpice simulation.

# Circuit diagrams:

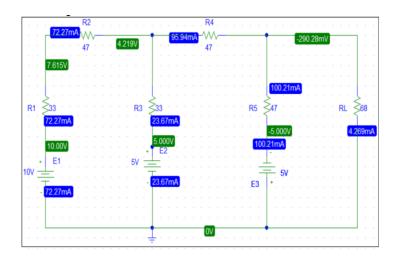


Figure 1: Circuit with all sources active

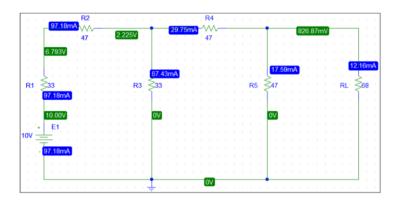


Figure 2: Circuit with 10V source active.

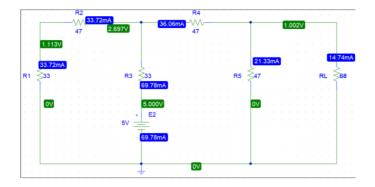


Figure 3: Circuit with 5V source active

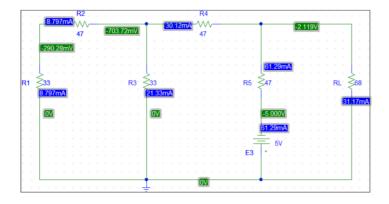


Figure 4: Circuit with 5V source active.

# **Experimental Datasheet:**

Measuced	Measured	Measewad	Hearwied	Hearwood	Meanweed	Meanweed	Меалито
valu of Ei (v)	value of $E_{L}(v)$	value of E3(v)	with all now	value of IL, with only E, active (mA)	value of IL2 with only E2 active	value of Is with	value of resistory (0 hm)
10	5	5	-4.2	112_	14.5	-3075	R1 = 3: R1 = 47 R3 = 47 R4 = 47 R5 = 47 R1 = 68
ID : Name : Name : Name :	BM 3h 2021-1 Avdara 2021- Sidra 2021-3	3-60-0 Sarker 3-60-	016 U 056 oowtaher	***************************************	39412		

Now,
$$I_{L} = I_{L_{1}} + I_{L_{2}} + I_{L_{3}}$$

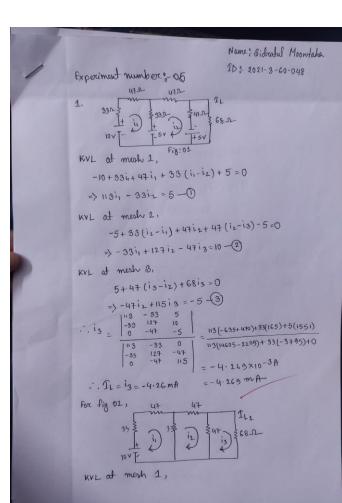
$$\Rightarrow -4.27 \Rightarrow (12 + 14.5 - 30.75) \text{ mA}$$

$$\Rightarrow -4.27 = -4.25 \text{ mA}$$

$$\Rightarrow -4.27 \approx -4.25 \text{ mA}$$

$$\text{mA}$$

Pre-lab report:



-10+33i,+47i,+33(i,-iz)=0 => 113i1 - 33i2 = 10 -1 KVL at mesh 2, 33(12-11)+4712+47(12-13)=0 =>-33i, +127i2 - 47i3 = 0-2 KVL at mesh 3, 47 (13-12)+6813=0 =>-4712+11513=0-3 113 -33 10 -33 124 0 0 -47 0 113 -33 0 -33 124 -47 0 -47 115 10 (1551) =0.012 A 1275513 = 12.16 mA . IL1 = 12.16 mA For fig 3, KVL at mesh 1, 11311 - 3312 = -5 -0 KVL at mest 2,

-3311+12712-4713=5-2

-47i2+115i3=0-3

KYL of mesh 3,

$$i_{3} = \frac{\begin{vmatrix} 113 & -93 & -5 \\ 0 & -44 & 6 \end{vmatrix}}{\begin{vmatrix} 113 & -93 & 0 \\ -93 & 124 & -44 \end{vmatrix}} = \frac{113(235) - 5(1551)}{124 5513}$$

$$= \frac{18800}{1245513} = 0.0147A$$

$$\therefore T_{L_{2}} = |4 \cdot 74 \text{ mA}$$

$$= 14 \cdot 74 \text{ mA}$$
Fore fig 04,
$$\frac{47}{1245513} = 0.0147A$$

$$\text{kvL at mesky 1 s}$$

$$1|3i_{1} - 33i_{2} = 0.0$$

$$\text{kvL at mesky 2,} -33i_{1} + 127i_{2} - 47i_{3} = 5-(2)$$

$$\text{kvL at mesky 3,}$$

$$-47i_{2} + 115i_{3} = -5 - (3)$$

$$\frac{113}{33} - \frac{33}{33} = 0$$

$$\frac{113}{-33} - \frac{33}{127} - \frac{1}{5}$$

$$\frac{113}{0} - \frac{33}{127} = 0.0311A$$

$$= -31.17 \text{ mA}$$

$$\therefore T_{L_{1}} + T_{L_{2}} + T_{L_{3}}$$

$$= 12 \cdot 1C + 14 \cdot 74 + (-31 \cdot 17) = -4 \cdot 27 \text{ mA}$$

$$\therefore L_{L} = T_{L_{1}} + T_{L_{2}} + T_{L_{3}}$$
So, suppexposition theorem has theoretically proved.

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ID: 2021-3-60-056

Giraup no: 01 Experiment No: 05

1. Theonitically calculate the values of IL, It., IL2 and IL3 of the cinemits of Figures 1 through 4. From the calculated values, Show that the super-position theorem holds that is IL=IL1+

IL2+ IL3.

yn2 yn2

yn2 yn2

Fig: 1

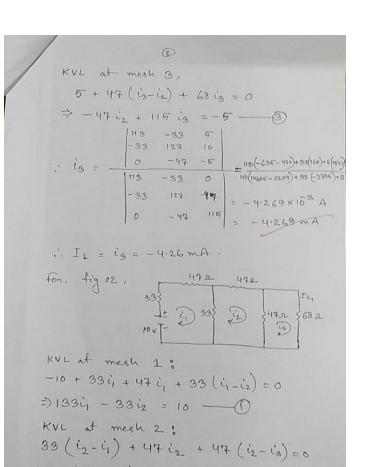
KVL at mesh 1:

-10+33i<sub>1</sub>+47i<sub>1</sub>+33(i<sub>1</sub>-i<sub>2</sub>)+5=0

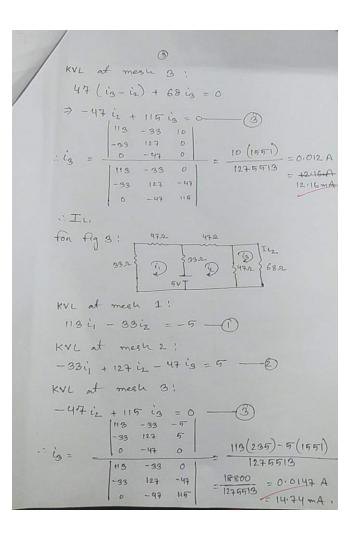
KVL at mesh 2,

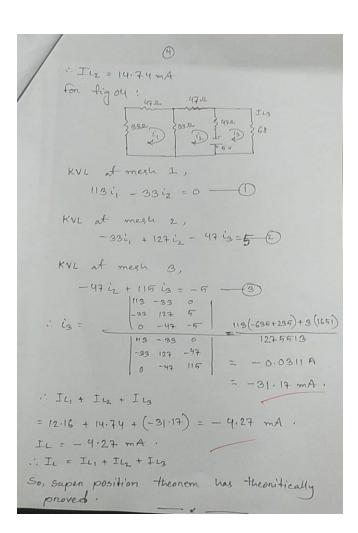
-5+33(i<sub>2</sub>-i<sub>1</sub>)+47i<sub>L</sub>+47(i<sub>2</sub>-i<sub>3</sub>)-5=0

→ - 334 + 127 i2 - 47 i3 = 10 - (2)



> 33 i<sub>1</sub> + 127 i<sub>2</sub> - 47 i<sub>3</sub> = 0 - €





Name: B M Shahria Alam ID: 2021-3-60-016 Course: 209(2) Lab: 3 Experiment number : 05

# Lab- 3 Prze lab rzepord

Q. Theoretically calculate the values of IL, IL, and ILs Of the circcuits of Figures 1 through 4. From the calculated values, show that the supersposition theorem holds, that is  $I_L = I_{L_1} + I_{L_2} + I_{L_3}$ 

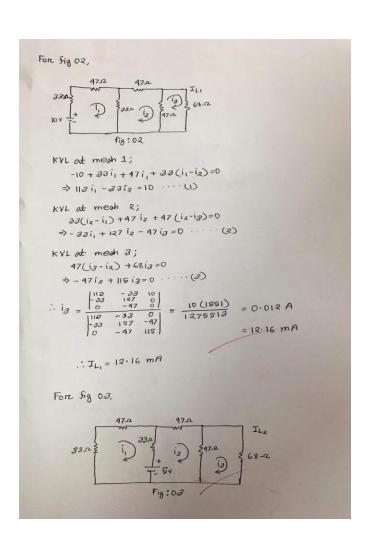
KVL at meah 1, -10+33i1+47i1+33(i1-i2)+5=0 ⇒ 118i, - 33i2 =5 ·····(i)

KYL at mesh ?, - 5 + 33 (i2-i1) + 47 i2 + 47 (i2-i3)-5=0 ⇒ - 33i, + 127 i2 - 47 i3=10 . . . . (ii)

KVL at mesh 3, 5 + 47 (i3 - i2) + 68 i3 = 0 ⇒ -47 i2 + 115 i3 = -5 ····· (iii)

= 118(-635+470)+ 33×165+5 (1551) = -4.269 × 10-3 A = -4.269 mA

: I1 = 13 = -4.26 mA



$$I_{L_1} + I_{L_2} + I_{L_3}$$

$$= 12 \cdot 16 + 14 \cdot 74 + (-3 \cdot 17)$$

$$= -4 \cdot 27 m \beta$$

$$I_L = -4 \cdot 269 m\beta$$

$$I_L = I_{L_1} + I_{L_2} + I_{L_3}$$

$$So, suppersposition theorem has theoretically proved.$$

### **Equipments and Components Needed:**

- 1. DC power supply
- 2. Trainer board
- 3. DC ammeter
- 4. Multimeter
- 5. Resistors  $33\Omega$  (two),  $47\Omega$  (three),  $68\Omega$  (one)
- 6. Breadboard
- 7. Connecting wires

#### **Lab Procedure:**

- 1. Measure the resistance values of the given resistors and record them in Table 1.
- 2. Construct the circuit with all voltage sources active as shown in Figure. 1. For the E1 = 10V source, use DC power supply. For the E2 = 5V and E3 = 5V sources, use the fixed voltage sources of the trainer board (be careful of the polarity of the voltage sources). Measure the values of the voltage sources and record them in Table 1. Measure IL and record it in Table 1.
- 3. Construct the circuit with only voltage source E1 active as shown in Figure 2. This may be done by removing the voltage sources E2 and E3 from the circuit and replacing them with short circuits. Caution: Do not try to replace any voltage source with a short circuit by directly connecting a wire across it. This will burn the trainer board. Measure the value of IL1 and record it in Table 1. This is the current through the  $RL = 68\Omega$  resistor when only the E1 = 10V source is active.
- 4. Construct the circuit with only voltage source E2 active as shown in Figure 3. Measure the current IL2 and record it in Table 1. This is the current through the  $RL = 68\Omega$  resistor when only the E2 = 5V source is active.
- 5. Construct the circuit with only voltage source E3 active as shown in Figure 4. Measure the current IL3 and record it in Table 1. This is the current through the  $RL = 68\Omega$  resistor when only the E3 = 5V source is active (be careful of the polarity of this source).
- 6. From the experimental data, show that the superposition theorem holds, that is, IL = IL1 + IL2 + IL2.
- 7. Have the datasheet signed by your instructor.

### Result & Discussion:

By doing this experiment, we slightly widen our knowledge that we gained from the previous experiment. Here, we are able to simulate our circuits via PSpice and test the results. Previously we had tested our circuits practically. As a result, this is more efficient.

# Conclusion:

While doing the experiments, the readings were taken very carefully. Though there is some difference between calculated value and PSpice value, at the end of the experiment we finally gained practical knowledge about Superposition theorem.

# Reference:

[1] Lab manual. [2] Academia.