



EAST WEST UNIVERSITY
Department of Computer Science and Engineering
Fall 2021 Semester
CSE345 Digital Logic Design, Section-2, Class Test-2
Marks:10
Time: 25 minutes

1. Design an AND-OR circuit for the Boolean function (5)

$$F(A, B, C, D) = \prod (5, 6, 10, 15) \cdot \prod_{d.c.} (2, 7, 13, 14)$$

2. Analyze the following circuit by constructing the truth table of the output. (5)

