



**EAST WEST UNIVERSITY**  
**Department of Computer Science and Engineering**  
**Summer 2021 Semester**  
**CSE345 Digital Logic Design, Section-3, Class Test-2**  
**Marks:10**  
**Time: 25 minutes**

1. Design an OR-AND circuit for the Boolean function (5)

$$F(A, B, C, D) = \sum (0, 6, 10, 15) + \sum_{d.c.} (2, 7, 8, 11, 14)$$

2. Analyze the following circuit by constructing the truth table of the output. (5)

