

HamidReza Zare

Curriculum Vitae

PERSONAL DETAILS

Birth April 23, 1997
Address 91 East Teymoori St., Habibolah St.
Tehran, THR 1455618181
Iran
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Website ce.sharif.edu/~hzare

EDUCATION

B.Sc. Computer Engineering, Computer Architecture

2015-Present

Sharif University of Technology, Tehran, Iran

CGPA 18.93/20.00, 3.98/4.00

GPA of Last 67 credits(Last two years) 19.48/20.00, 4.00/4.00

Diploma in Physics and Mathematics

2011-2015

NODET (National Organization for Exceptional Talents), Dastgheib 1 High School

GPA 19.44/20.00, 4.00/4.00

RESEARCH INTERESTS

Computer Architecture
Neural Network Accelerators
Cache Design
Low-Power
Embedded Systems Design

HONORS

Ranked 365th Among the 223000 participants in the National Universities Entrance Exam for B.Sc. degree in Mathematics and Physics

Admitted to NODET (National Organization for Exceptional Talents) by passing an exam with below 2% acceptance rate

RESEARCH PROJECTS

A new architecture for the die-stacked DRAM, considering both dynamic and static behavior of the modern big-data application

B.Sc. Project, Advisor: Prof. Hamid Sarbazi-Azad, at HPCAN lab

According to this work, we wrote an article which is available in arxiv.org/abs/1809.08828.

VOLUNTEER TA AND RA EXPERIENCES

Research Assistant

Fall 2017-Present

High-Performance Computer Architectures and Networks Laboratory (HPCAN), Sharif University of Technology

Supervisor: Prof. Hamid Sarbazi-Azad

Teaching Assistant

Fall 2018

Computer Language and Structure, Sharif University of Technology

Supervisor: Dr. Hossein Asadi

Teaching Assistant

Fall 2017

Computer Language and Structure, Sharif University of Technology

Supervisor: Dr. Hossein Asadi

Teaching Assistant

Spring 2017

Logical Circuit, Sharif University of Technology

Supervisor: Dr. Siavash Bayat-Sarmadi

Teaching Assistant

Fall 2016

Fundamentals of Programming, Sharif University of Technology

Supervisor: Dr. Omid Gheibi

WORK EXPERIENCE

Linux Programmer

Summer 2018

Scientific Green Incorporation, Full-time

I did my internship there. I was working on an IoT infrastructure's server-side which was based on LoRaWAN. The configuration of server and gateway station that were based on Linux was my responsibility.

Software Developer

Summer 2017

Dr. BayatSarmadi's association, Part-time

I have worked in a team for designing an HSM tool (IPSec based on Xilinx FPGA) for 4 month.

SKILLS

Languages

Persian (Native)

English (Fluent)

Programming

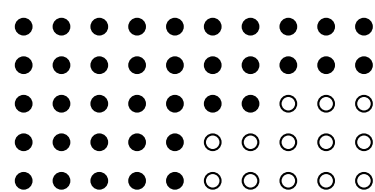
C

C++

PYTHON

X86 ASSEMBLY

IBM 360 ASSEMBLY



HDL Programming

VERILOG



	QUARTUS	● ● ● ● ● ● ● ○ ○ ○
Script	BASH	● ● ● ● ● ● ● ○ ○
Simulators	ZSIM	● ● ● ● ● ● ● ● ○
	MODELSIM	● ● ● ● ● ● ● ○ ○
Documentation	L ^A T _E X, MS OFFICE	● ● ● ● ● ● ● ○ ○

RELEVANT COURSE GRADES

Microprocessors (<i>Dr. Nezam Rohbani</i>)	20.0/20.0 Class Rank:1/24
Computer Networks (<i>Dr. Mehdi Kharrazi</i>)	19.4/20.0 Class Rank:3/43
Operating Systems (<i>Dr. Rasool Jalili</i>)	18.8/20.0 Class Rank:2/36
Computer Architecture Laboratory (<i>Dr. Hossein Asadi</i>)	19.5/20.0 Class Rank:1/11
Compiler Design (<i>Dr. Ali Sharifi-Zarchi</i>)	20.0/20.0 Class Rank:1/52
Digital Electronics (<i>Dr. Siavash Bayat</i>)	18.7/20.0 Class Rank:9/32
Digital Systems Design Laboratory (<i>Dr. Maziar Goudarzi</i>)	19.8/20.0 Class Rank:4/18
Electrical Circuits Laboratory (<i>Dr. A. Hemmat-Yar</i>)	20.0/20.0 Class Rank:1/17
Computer Architecture (<i>Dr. Hossein Asadi</i>)	20.0/20.0 Class Rank:1/59
Digital Systems Design (<i>Dr. Maziar Goudarzi</i>)	20.0/20.0 Class Rank:1/41
Numerical Analysis (<i>Dr. Mohammed Gharib</i>)	20.0/20.0 Class Rank:1/29
Electrical Circuits (<i>Dr. A. Hemmat-Yar</i>)	20.0/20.0 Class Rank:1/29
Data Structure and Algorithms (<i>Dr. Masoud Sedighein</i>)	18.2/20.0 Class Rank:9/26
Advanced Programming (<i>Dr. Omid Jafarinezhad</i>)	19.5/20.0 Class Rank:5/30
Logical Circuit Laboratory (<i>Dr. Siavash Bayat-Sarmadi</i>)	19.6/20.0 Class Rank:1/19
Comp. Lang. and Structure (<i>Prof. H. Sarbazi-Azad</i>)	19.6/20.0 Class Rank:7/54
Logical Circuit (<i>Dr. Siavash Bayat-Sarmadi</i>)	19.8/20.0 Class Rank:2/53

REFERENCES

Prof. Hamid Sarbazi-Azad

Professor of Computer Engineering, Sharif Univesity of Technology and Head of School of Computer Science, Institute for Research in Fundamental Sciences (IPM)

Website: sharif.edu/~azad

Email: azad@sharif.edu, azad@ipm.ir

Dr. Pejman Lotfi-Kamran

Research associate professor of Computer Science and the manager of Iran's National Compute-Grid Project at Institute for Research in Fundamental Sciences (IPM)

Website: cs.ipm.ac.ir/~plotfi

Email: plotfi@ipm.ir

Dr. Alireza Ejlali

Associate Professor of Computer Engineering and the director of Embedded Systems Research Laboratory (ESR-LAB) at the department of computer engineering, Sharif University of Technology

Website: sharif.edu/~ejlali

Email: ejlali@sharif.edu