

FPGA Mapping Report

Group: k-furthest-neighbours

Cyclone IV FPGA Mapping of Vanilla Version	
Fmax	122.2 MHz
Switching Activity	20%
PVT	ss-1.2V-100C
Thermal Dynamic Power	18.76 mW
Total Logic Elements	6886
Total Registers	4132
Total Operations	$8 \times 8 \times 2 = 128$
GOPs/s	16
TOPs/W	0.834