

Name	Version	Description
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+	Analog	
+	Bus	
+	Bus Bridge	
+	Clock Control	
+	Communication High-Speed	
+	Communication Low-Speed	
+	Debug	
+	DMA	
+	FPGA Reconfiguration	
+	General Purpose IO	
+	Interrupt Control	
+	Memory Block	
+	Memory Controller	
+	PCI	
+	Peripheral Controller	
+	Processor	
-	Project Repository	
	adder_opbw	
+	Reset Control	
+	Shared EDK Repository	
+	Timer	
+	Utility	

