Base System Builder - System Created

Below is a summary of the system you have created. Please review the information below. If it is correct, hit <Generate> to enter the information into the XPS data base and generate the system files. Otherwise return to the previous page to make corrections.

```
Processor: PPC 405
Processor clock frequency: 100.000000 MHz
Bus clock frequency: 50.000000 MHz
Debug interface: FPGA JTAG
On Chip Memory : 192 KB
```
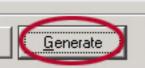
The address maps below have been automatically assigned. You can modify them using the editing features of XPS.

Processor OCM:

Core Name	Instance Name	Base Addr	High Addr
isbram_if_cntlr	iocm_cntlr	0xFFFE0000	0xFFFFFFFF

Processor OCM:

Core Name	Instance Name	Base Addr	High Addr
dsbram_if_cntlr	docm_cntlr	0x20800000	0x2080FFFF

PLB Bus : PLB_V34 Inst. name: plb   Attached Components:

Core Name	Instance Name	Base Addr	High Addr
plb2opb_bridge	plb2opb_C_RNG0_BA!	0x40000000	0x7FFFFFFF

OPB Bus : OPB_V20 Inst. name: opb   Attached Components:

Core Name	Instance Name	Base Addr	High Addr
opb_gpio	LED_7SEGMENT	0x40000000	0x4000FFFF
opb_gpio	LED_7SEGMENT_1	0x40020000	0x4002FFFF
opb_gpio	LEDs_4Bit	0x40040000	0x4004FFFF

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