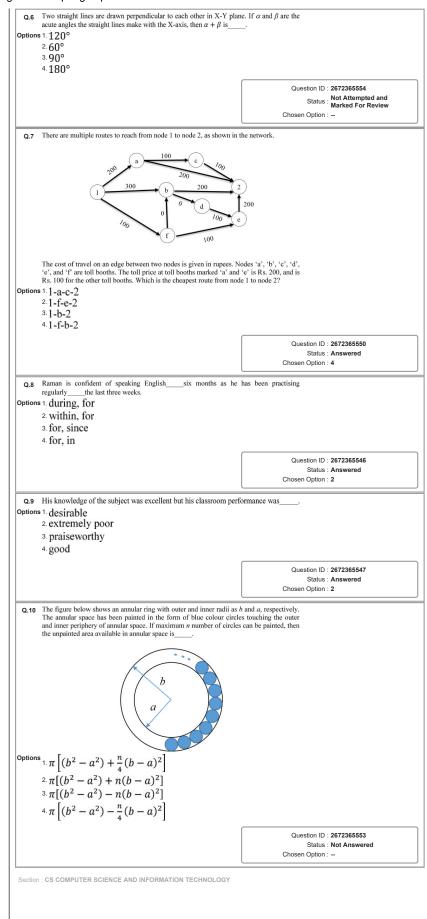
G	Graduate Aptitude Test in Engineering	2020 08th Feb S2
Participant ID	CS20S62038499	
Participant Name	SHAILESH RAVI BHIMANPELLI	
Test Center Name	iON Digital Zone IDZ Powai	
Test Date	08/02/2020	
Test Time	2:30 PM - 5:30 PM	
Subject	CS COMPUTER SCIENCE AND INFORMATION TECHNOLOGY	
_ ,	TECHNOLOGY	

Q.1 The dawn of the 21st century witnessed the melting glaciers oscillating between giving too much and too little to billions of people who depend on them for fresh water. The UN climate report estimates that without deep cuts to man-made emissions, at least 30% of the northern hemisphere's surface permafrost could melt by the end of the century. Given this situation of imminent global exodus of billions of people displaced by rising seas, nation-states need to rethink their carbon footprint for political concerns, if not for environmental ones. Which one of the following statements can be inferred from the given passage? Options 1 Billions of people are affected by melting glaciers. Nation-states are responsible for providing fresh water to billions of people. 3 Nation-states do not have environmental concerns. Billions of people are responsible for man-made emissions. Question ID : 2672365549 Chosen Option: 4 The total revenue of a company during 2014-2018 is shown in the bar graph. If the total expenditure of the company in each year is 500 million rupees, then the aggregate profit or loss (in percentage) on the total expenditure of the company during 2014-2018 is _____. 800 800 700 600 500 400 400 E 300 200 100 2014 2015 2016 2017 2018 Options 1. 20 % loss 2. 20 % profit 3.16.67 % profit 4.16.67 % loss Question ID : 2672365555 Chosen Option: 2 Goods and Services Tax (GST) is an indirect tax introduced in India in 2017 that is imposed on the supply of goods and services, and it subsumes all indirect taxes except few. It is a destination-based tax imposed on goods and services used, and it is not imposed at the point of origin from where goods come. GST also has a few components specific to state governments, central government and Union Territories (UTs). Which one of the following statements can be inferred from the given passage? GST is imposed at the point of usage of goods and services. 2 GST is imposed on the production of goods and services. 3 GST includes all indirect taxes. 4 GST does not have a component specific to UT. Question ID : 2672365551 Status : Answ Chosen Option : 1 9.4 Select the word that fits the analogy: Cook: Cook:: Fly:_ Options 1. Flew 2. Flyer 3. Flighter 4. Flying Question ID: 2672365548 Chosen Option : 1 0.5 If P = 3, R = 27, T = 243, then Q + S =Options 1. 110 2.40 3.80 4.90 Question ID : 2672365552 Status : Ansv Chosen Option : 4



Q.1 Consider the following statements.	
I. If $L_1 \cup L_2$ is regular, then both L_1 and L_2	must be regular
II. The class of regular languages is closed L_1	
2. The chass of regular languages is closed	
Which of the above statements is/are TRUE? Options 1. II only	
2. I only	
3 Neither I nor II 4 Both I and II	
- Both I and II	0 11 10 222222
	Question ID : 2672365563 Status : Answered
	Chosen Option : 2
Q.2 If there are m input lines and n output lines for a decoder	55/ 15
address a byte addressable 1 KB RAM, then the minimum Given 10	value of $m + n$ is
Answer:	
	Question ID : 2672365575 Status : Answered
Q.3 Consider a double hashing scheme in which the pri $h_1(k) = k \mod 23$, and the secondary hash function is	
Assume that the table size is 23. Then the address returned	
sequence (assume that the probe sequence begins at probe	0) for key value $k = 90$
is	
Given Answer :	
	Question ID : 2672365578
	Status : Not Answered
Q.4 A multiplexer is placed between a group of 32 registers	
regulate data movement such that at any given point in one register will move to the accumulator. The minimum	
needed for the multiplexer is	
Given 8 Answer:	
	Question ID : 2672365574
	Status : Answered
Q.5 The preorder traversal of a binary search tree is 15, 10,	12, 11, 20, 18, 16, 19.
Which one of the following is the postorder traversal of the	e tree?
Options 1. 19, 16, 18, 20, 11, 12, 10, 15 2. 11, 12, 10, 16, 19, 18, 20, 15	
3 20, 19, 18, 16, 15, 12, 11, 10	
4.10, 11, 12, 15, 16, 18, 19, 20	
	Question ID : 2672365560 Status : Answered
	Chosen Option : 1
Q.6 What is the worst case time complexity of inserting n^2 ele	ements into an AVL-tree
with n elements initially?	
Options 1. $\Theta(n^2 \log n)$ 2. $\Theta(n^3)$	
$3.\Theta(n^4)$	
$4.\Theta(n^2)$	
	Question ID : 2672365561
	Status : Not Answered Chosen Option :
O.7. Consider the following statements about the force!	
Q.7 Consider the following statements about the functionality	or an if based fouter.
I. A router does not modify the IP packets during forw	5/
II. It is not necessary for a router to implement any rou III. A router should reassemble IP fragments if the MTU	7.0
larger than the size of the incoming IP packet.	
Which of the above statements is/are TRUE?	
Options 1. II only	
2. II and III only 3. I only	
4 I and II only	
	Question ID : 2672365570
	Status : Answered Chosen Option : 3
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Q.8 Consider the following grammar.
             S \rightarrow aSB \mid d
             B \rightarrow b
       The number of reduction steps taken by a bottom-up parser while accepting the
       string aaadbbb is
  Given 7
                                                                             Question ID : 2672365579
 Q.9 Consider the following statements
         I. Symbol table is accessed only during lexical analysis and syntax analysis.
        II. Compilers for programming languages that support recursion necessarily
             need heap storage for memory allocation in the run-time environment.
       III. Errors violating the condition 'any variable must be declared before its
             use' are detected during syntax analysis.
      Which of the above statements is/are TRUE?
Options 1. None of I, II, and III
      2. I only
      3. I and III only
      4. II only
                                                                             Question ID : 2672365564
                                                                                  Status: Answered
                                                                           Chosen Option : 3
 Q.10 Consider allocation of memory to a new process. Assume that none of the existing
      holes in the memory will exactly fit the process's memory requirement. Hence, a
       new hole of smaller size will be created if allocation is made in any of the existing
      holes. Which one of the following statements is TRUE?
Options 1.

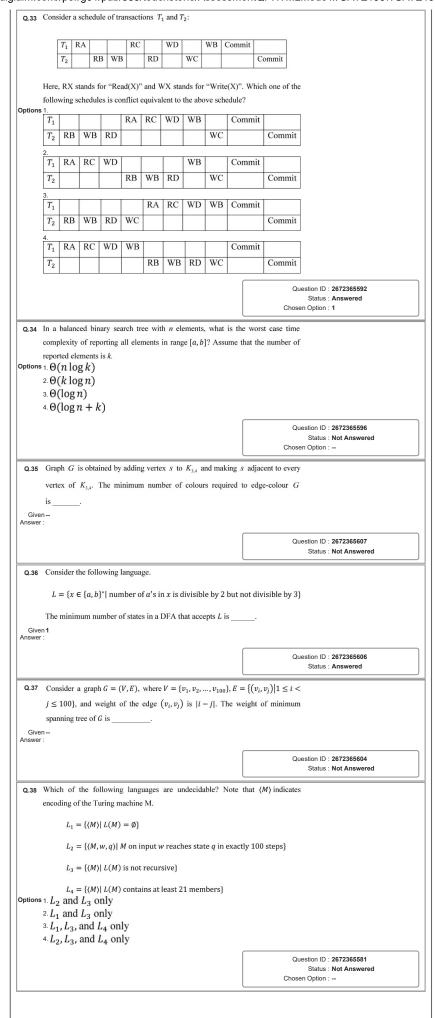
The hole created by worst fit is always larger than the hole created by first fit.
       The hole created by next fit is never larger than the hole created by best fit.
       The hole created by first fit is always larger than the hole created by next fit.
       The hole created by best fit is never larger than the hole created by first fit.
                                                                             Question ID : 2672365566
                                                                                  Status: Not Answered
                                                                           Chosen Option : --
 Q.11 Consider the following C program.
       #include <stdio.h>
       int main() {
              int a[4][5] = \{\{1, 2, 3, 4, 5\},\
                                    {6, 7, 8, 9, 10},
                                    {11, 12, 13, 14, 15},
                                    {16, 17, 18, 19, 20}};
              printf("%d\n", *(*(a+**a+2)+3));
               return(0);
       The output of the program is _
                                                                             Question ID : 2672365577
                                                                                  Status : Answered
 Q.12 Which one of the following regular expressions represents the set of all binary
      strings with an odd number of 1's?
Options 1. (0*10*10*)*10*
      2.(0*10*10*)*0*1
      3.((0+1)^*1(0+1)^*1)^*10^*
      4.10*(0*10*10*)*
                                                                             Question ID : 2672365562
                                                                           Chosen Option : 2
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Q.13 Consider the following statements about process state transitions for a system
        using preemptive scheduling.
          I. A running process can move to ready state.
         II. A ready process can move to running state.
        III. A blocked process can move to running state.
        IV. A blocked process can move to ready state.
Which of the above statements are TRUE?
Options 1. I, II, and III only
       2. II and III only
       3 I, II, III, and IV
        4. I, II, and IV only
                                                                                           Question ID : 2672365567
                                                                                                Status : Answered
                                                                                        Chosen Option : 4
 Q.14 Assume that you have made a request for a web page through your web browser
        to a web server. Initially the browser cache is empty. Further, the browser is
        configured to send HTTP requests in non-persistent mode. The web page contains
        text and five very small images. The minimum number of TCP connections
         required to display the web page completely in your browser is
   Given 5
                                                                                           Question ID : 2672365580
 Q.15 Consider the following data path diagram.
                                            BUS
                                                                                 TEMP1
              MAR
                       MDR
                                  IR
                                           PC
                                                     R1
                                                             TEMP2
                                                                           ALU
                To Memory
        Consider an instruction: R0 ← R1 + R2. The following steps are used to execute
        it over the given data path. Assume that PC is incremented appropriately. The
        subscripts r and w indicate read and write operations, respectively.
               R2<sub>r</sub>, TEMP1<sub>r</sub>, ALU<sub>add</sub>, TEMP2<sub>w</sub>
            2. R1<sub>r</sub>, TEMP1<sub>w</sub>
            3. PC<sub>r</sub>, MAR<sub>w</sub>, MEM<sub>r</sub>
            \begin{array}{ll} \text{4.} & \text{TEMP2}_r,\, R0_w \\ \text{5.} & \text{MDR}_r,\,\, IR_w \end{array}
        Which one of the following is the correct order of execution of the above steps?
Options 1. 3, 5, 1, 2, 4
       2.1, 2, 4, 3, 5
       3.3, 5, 2, 1, 4
       4.2, 1, 4, 5, 3
                                                                                           Question ID : 2672365559
                                                                                                Status: Answered
                                                                                        Chosen Option : 4
 Q.16 Consider the functions
                      I. e^{-x}
                     II. x^2 - \sin x
                    III. \sqrt{x^3+1}
        Which of the above functions is/are increasing everywhere in [0,1]?
Options 1. I and III only
       2. III only
        3. II only
        4 II and III only
                                                                                           Question ID : 2672365556
                                                                                                Status : Not Answered
                                                                                        Chosen Option :
 Q.17 For parameters a and b, both of which are \omega(1), T(n) = T(n^{1/a}) + 1, and T(b) = 1.
        Then T(n) is
Options 1. \Theta(\log_b \log_a n)
       2.\Theta(\log_2\log_2 n)
        з.\Theta(\log_{ab} n)
        4.\Theta(\log_a \log_b n)
                                                                                           Question ID : 2672365557
                                                                                                Status: Not Answered
                                                                                        Chosen Option :
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statements. I. L is deterministic context-free. II. L is context-free but not deterministic context-free. III. L is not LL(k) for any k. Which of the above statements is/are TRUE? Options 1. III only	
II. L is context-free but not deterministic context-free. III. L is not $LL(k)$ for any k . Which of the above statements is/are TRUE? Options 1. III only	
Which of the above statements is/are TRUE? Options 1. III only	
Options 1. III only	
2. II only	
3. I only	
4. I and III only	
Question ID : 2672365565 Status : Not Answered	
Chosen Option :	
Q.19 Let \mathcal{R} be the set of all binary relations on the set $\{1,2,3\}$. Suppose a relation is	
chosen from \mathcal{R} at random. The probability that the chosen relation is reflexive (round off to 3 decimal places) is	
Given Answer :	
Question ID : 2672365572 Status : Not Answered	
Q.20 A direct mapped cache memory of 1 MB has a block size of 256 bytes. The cache	
has an access time of 3 ns and a hit rate of 94%. During a cache miss, it takes	
20 ns to bring the first word of a block from the main memory, while each subsequent word takes 5 ns. The word size is 64 bits. The average memory access	
time in ns (round off to 1 decimal place) is	
Given 5.95 Answer:	
Question ID : 2672365576	
Status : Answered	
Q.21 Consider a relational database containing the following schemas.	
$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	
S1 P1 150 S1 M/s Royal furniture Delhi S1 P2 50 S2 M/s Balaji furniture Bangalore	
S1 P3 100 S3 M/s Premium furniture Chennai	
S2 P5 250 Parts S3 P1 250 pno pname part_spec	
S3 P2 150 P1 Table Wood S3 P5 300 P2 Chair Wood	
S3 P4 250 P3 Table Steel P4 Almirah Steel	
P5 Almirah Wood	
The primary key of each table is indicated by underlining the constituent fields.	
SELECT s.sno, s.sname	
FROM Suppliers s, Catalogue c WHERE s.sno = c.sno AND	
cost > (SELECT AVG (cost)	
FROM Catalogue WHERE pno = 'P4'	
GROUP BY pno);	
The number of rows returned by the above SQL query is Options 1. 4	
2. 0	
3. 2 4. 5	
Question ID : 2672365568	
Status : Answered Chosen Option : 1	
Q.22 What is the worst case time complexity of inserting n elements into an empty	
linked list, if the linked list needs to be maintained in sorted order?	
Options 1. $\Theta(n)$ 2. $\Theta(1)$	
$3 \cdot \Theta(n \log n)$	
$4.\Theta(n^2)$	
Question ID : 2672365571	
Status: Not Answered Chosen Option:	
Q.23 Let G be a group of 35 elements. Then the largest possible size of a subgroup of G	
other than G itself is	
Given Answer :	
Question ID : 2672365573	
Status: Not Answered	

	I. Daisy c	device	raises a v	rectored int	errur		CFI	does	poll	ling to i	1entity			
	the sour			cotorea m	crrup	,		uoes	Pon	ing to it				
	III. In pollii		-	odically ch	ecks	the st	atus t	oits to	kno	ow if any	device			
	needs it IV. During			PII and D	MAG	ontro	iller c	an he	hus	masters	at the			
	same tir		our the v	or o and D	WIA C	ontre	oner c	an oc	ous	masters	, at the			
ptions	Which of the	only	tatement	s is/are TR	UE?									
	2. I and III 3. I and IV													
	4 III only	omy												
											Question	ı ID :	2672365558	
											Sta Chosen Op		Not Answere	d
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Q.25	relationships									7.00	any-one			
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											Sta	atus :	Answered	
											Chosen Op	uon :	. 4	
Q.26	Consider the		-											
	Robin (RR)													
	P_1, P_2, P_3, P_4													
			n .	ec	P_1	P_2	P_3	P_4						
			Process			- 2	- 4	4						
			Process Burst ti	me (in ms)	8	7	2	7						
	If the time		Burst ti	me (in ms)						6 4l J:	CC			
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Answer	between the decimal place on 3.25	on requirements on requirements on the organical transfer of the second	Burst ti for RR e turnaro ires a rai nization P uses C 1.0.0/17.	is 4 ms, tund times inge of IP achas approaches approa	ddress ched erves	ses to an In the state of the s	assign assign	and and and set Ser	RR e to ovice om t	each of i	Question State Sta			
Answer	An organizati computers. T for this task, address space	on required the organics of the IS.	Burst till for RR turnaro	is 4 ms, tund times inge of IP achas approach approach approach approach are IPP voices.	ddress ched erves wants	sees to an III the action of root	assign assign assign assign	and	RR e to covice om t ddre s in	each of ite Provide the avail the ISP	Question States 1500 er (ISP) lable IP er to the s router			
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Q.29 A computer system with a word length of 32 bits has a 16 MB byte-addressable
       main memory and a 64 KB, 4-way set associative cache memory with a block size
       of 256 bytes. Consider the following four physical addresses represented in
       A1 = 0x42C8A4, A2 = 0x546888, A3 = 0x6A289C, A4 = 0x5E4880
       Which one of the following is TRUE?
Options 1 A1 and A4 are mapped to different cache sets.
       2. A3 and A4 are mapped to the same cache set.
       3. A2 and A3 are mapped to the same cache set.
       4 A1 and A3 are mapped to the same cache set.
                                                                             Question ID : 2672365585
                                                                                  Status : Not Answered
                                                                           Chosen Option : --
 Q.30 Consider the following C functions.
        int tob(int b, int* arr){      int pp(int a,int b) {
                                               int arr[20];
            int i;
            for(i=0; b>0; i++){
                                                 int i, tot = 1, ex, len;
              in (i=0; b>0; 1++,\(\)
if (b%2) arr[i]=1;
else arr[i]=0;
                                                ex = a:
                                               len = tob(b,arr);
                                               for(i=0; i<len; i++){
               b = b/2;
                                                   if(arr[i]==1)
            return(i);
                                                        tot = tot * ex;
                                                    ex = ex * ex;
                                                 return(tot);
                                             }
       The value returned by pp (3, 4) is ___
                                                                             Question ID : 2672365603
                                                                                  Status : Answered
 Q.31 Let A and B be two n \times n matrices over real numbers. Let rank(M) and det(M)
      denote the rank and determinant of a matrix M, respectively. Consider the
      following statements.
        I. rank(AB) = rank(A) rank(B)
        II. det(AB) = det(A) det(B)
       III. rank(A + B) \le rank(A) + rank(B)
       IV. det(A + B) \le det(A) + det(B)
Which of the above statements are TRUE?
Options 1. I and IV only
      2. II and III only
       3. III and IV only
       4. I and II only
                                                                             Question ID: 2672365582
                                                                                  Status : Answered
                                                                           Chosen Option : 2
 Q.32 Which one of the following predicate formulae is NOT logically valid?
       Note that W is a predicate formula without any free occurrence of x.
Options 1. \exists x (p(x) \land W) \equiv \exists x \ p(x) \land W
      2. \forall x (p(x) \lor W) \equiv \forall x \ p(x) \lor W
      3. \forall x (p(x) \to W) \equiv \forall x \ p(x) \to W
       4.\exists x(p(x) \to W) \equiv \forall x p(x) \to W
                                                                             Question ID : 2672365594
                                                                                  Status : Not Answered
                                                                           Chosen Option : --
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Q.39 Consider the following C functions.
                                                  int fun2(int n) {
        int fun1(int n) {
           static int i = 0;
                                                     static int i = 0;
           if (n > 0) {
                                                     if (n > 0) {
              ++i;
                                                       i = i + funl(n);
              fun1(n-1);
                                                         fun2(n-1);
                                                     }
           return(i);
                                                     return(i);
       The return value of fun2 (5) is __
  Given 1
                                                                             Question ID : 2672365601
 Q.40 Let G = (V, E) be a weighted undirected graph and let T be a Minimum
       Spanning Tree (MST) of G maintained using adjacency lists. Suppose a new
       weighted edge (u, v) \in V \times V is added to G. The worst case time complexity of
       determining if T is still an MST of the resultant graph is
Options 1. \Theta(|E| + |V|)
      2.\Theta(|V|)
       3.\Theta(|E||V|)
       4.\Theta(|E|\log|V|)
                                                                             Question ID : 2672365586
                                                                                 Status: Not Answered
                                                                           Chosen Option :
 Q.41 For n > 2, let a \in \{0,1\}^n be a non-zero vector. Suppose that x is chosen
       uniformly at random from \{0,1\}^n. Then, the probability that \sum_{i=1}^n a_i x_i is an odd
 Given --
Answer :
                                                                             Question ID : 2672365600
                                                                                 Status: Not Answered
 Q.42 Consider the Boolean function z(a, b, c).
       Which one of the following minterm lists represents the circuit given above?
      3.z = \sum (2,3,5)
                                                                             Question ID : 2672365583
                                                                                 Status : Answ
                                                                           Chosen Option : 2
 Q.43 Consider the following languages.
                L_1 = \{wxyx \mid w, x, y \in (0+1)^+\}
                L_2 = \{xy \mid x,y \in (a+b)^*, |x| = |y|, x \neq y\}
       Which one of the following is TRUE?
Options 1. Neither L_1 nor L_2 is context-free.
       ^{2} L_{1} is context-free but L_{2} is not context-free.
       ^{3}. L_{1} is regular and L_{2} is context-free.
       4 \cdot L_1 is context-free but not regular and L_2 is context-free.
                                                                             Question ID : 2672365587
                                                                           Chosen Option : --
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Q.44 Consider a relational table R that is in 3NF, but not in BCNF. Which one of the
       following statements is TRUE?
Options 1. R has a nontrivial functional dependency X \to A, where X is not a superkey
       and A is a non-prime attribute and X is a proper subset of some key.
       <sup>2</sup> A cell in R holds a set instead of an atomic value.
       R has a nontrivial functional dependency X \to A, where X is not a superkey
       and A is a prime attribute.
       R has a nontrivial functional dependency X \to A, where X is not a superkey
       and A is a non-prime attribute and X is not a proper subset of any key.
                                                                                  Question ID : 2672365591
                                                                                       Status: Not Answered
                                                                                Chosen Option : -
 Q.45 Each of a set of n processes executes the following code using two semaphores
       a and b initialized to 1 and 0, respectively. Assume that count is a shared
       variable initialized to 0 and not used in CODE SECTION P.
               CODE SECTION P
              wait(a); count=count+1;
              if (count==n) signal(b);
              signal(a); wait(b); signal(b);
               CODE SECTION Q
       What does the code achieve?
Options
       It ensures that no process executes CODE SECTION O before every process has
       finished CODE SECTION P.
       It ensures that at most two processes are in CODE SECTION O at any time.
       It ensures that at most n-1 processes are in CODE SECTION P at any time.
       4.
It ensures that all processes execute CODE SECTION P mutually exclusively.
                                                                                  Question ID : 2672365589
                                                                               Chosen Option : -
 Q.46 Consider three registers R1, R2, and R3 that store numbers in IEEE-754 single
       precision floating point format. Assume that R1 and R2 contain the values (in
       hexadecimal notation) 0x42200000 and 0xC1200000, respectively.
      If R3 = \frac{R1}{R2}, what is the value stored in R3?
Options 1. 0x40800000
       2.0xC0800000
       3.0x83400000
       4.0xC8500000
                                                                                  Question ID : 2672365584
                                                                                       Status : Not Answered
                                                                                Chosen Ontion : --
 Q.47 Consider the following five disk access requests of the form (request id, cylinder
       number) that are present in the disk scheduler queue at a given time.
              (P, 155), (O, 85), (R, 110), (S, 30), (T, 115)
       Assume the head is positioned at cylinder 100. The scheduler follows Shortest
       Seek Time First scheduling to service the requests.
       Which one of the following statements is FALSE?
Options 1.

The head reverses its direction of movement between servicing of Q and P.
       <sup>2</sup> T is serviced before P.
       <sup>3</sup> R is serviced before P.<sup>4</sup> Q is serviced after S, but before T.
                                                                                   Question ID : 2672365590
                                                                                       Status: Answered
 Q48 Consider a TCP connection between a client and a server with the following
        specifications: the round trip time is 6 ms, the size of the receiver advertised
        window is 50 KB, slow-start threshold at the client is 32 KB, and the maximum
        segment size is 2 KB. The connection is established at time t = 0. Assume that
        there are no timeouts and errors during transmission. Then the size of the
       congestion window (in KB) at time t + 60 ms after all acknowledgements are
       processed is
  Given -
                                                                                   Question ID : 2672365610
                                                                                       Status: Not Answered
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Q.49 Consider a non-pipelined processor operating at 2.5 GHz. It takes 5 clock cycles
        to complete an instruction. You are going to make a 5-stage pipeline out of this
        processor. Overheads associated with pipelining force you to operate the pipelined
        processor at 2 GHz. In a given program, assume that 30% are memory
        instructions, 60% are ALU instructions and the rest are branch instructions.
        5% of the memory instructions cause stalls of 50 clock cycles each due to cache
        misses and 50% of the branch instructions cause stalls of 2 cycles each. Assume
        that there are no stalls associated with the execution of ALU instructions. For this
        program, the speedup achieved by the pipelined processor over the non-pipelined
        processor (round off to 2 decimal places) is
                                                                                     Question ID : 2672365598
                                                                                         Status : Not Answered
 Q.50 Let G = (V, E) be a directed, weighted graph with weight function w: E \to \mathbb{R}
       For some function f: V \to \mathbb{R}, for each edge (u, v) \in E, define w'(u, v) as
        w(u,v) + f(u) - f(v).
        Which one of the options completes the following sentence so that it is TRUE?
       "The shortest paths in G under w are shortest paths under w' too,
Options 1. f(u) is the distance from s to u in the graph obtained by adding a
        new vertex s to G and edges of zero weight from s to every vertex of G
       2 if and only if \forall u \in V, f(u) is positive
       3 for every f: V \to \mathbb{R}
       4 if and only if \forall u \in V, f(u) is negative
                                                                                    Question ID : 2672365595
                                                                                         Status: Not Answered
 Q.51 Consider the productions A \rightarrow PQ and A \rightarrow XY. Each of the five non-terminals A,
       P, Q, X, and Y has two attributes: s is a synthesized attribute, and i is an inherited
       attribute. Consider the following rules.
             Rule 1: P.i = A.i + 2, Q.i = P.i + A.i, and A.s = P.s + Q.s
             Rule 2: X.i = A.i + Y.s and Y.i = X.s + A.i
        Which one of the following is TRUE?
Options 1. Only Rule 1 is L-attributed.
       <sup>2</sup> Neither Rule 1 nor Rule 2 is L-attributed.

    Only Rule 2 is L-attributed.

       <sup>4</sup> Both Rule 1 and Rule 2 are L-attributed.
                                                                                     Question ID : 2672365588
                                                                                         Status: Not Answered
 Q.52 A processor has 64 registers and uses 16-bit instruction format. It has two types of
        instructions: I-type and R-type. Each I-type instruction contains an opcode, a
        register name, and a 4-bit immediate value. Each R-type instruction contains an
        opcode and two register names. If there are 8 distinct I-type opcodes, then the
        maximum number of distinct R-type opcodes is
  Given 48
                                                                                    Question ID: 2672365599
  Q.53 Consider the array representation of a binary min-heap containing 1023 elements
        The minimum number of comparisons required to find the maximum in the heap
  Given 511
                                                                                    Question ID: 2672365602
 Q.54 The number of permutations of the characters in LILAC so that no character
        appears in its original position, if the two L's are indistinguishable, is
   Given 96
                                                                                    Question ID : 2672365597
 Q.55 Consider a paging system that uses 1-level page table residing in main memory
        and a TLB for address translation. Each main memory access takes 100 ns and
        TLB lookup takes 20 ns. Each page transfer to/from the disk takes 5000 ns.
        Assume that the TLB hit ratio is 95%, page fault rate is 10%. Assume that for
        20% of the total page faults, a dirty page has to be written back to disk before the
        required page is read in from disk. TLB update time is negligible. The average
        memory access time in ns (round off to 1 decimal places) is
  Given --
                                                                                    Question ID : 2672365608
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