Electronic Devices

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Reference book:

Electronic Devices and Circuit Theory (Chapter-2)

Robert L. Boylestad and L. Nashelsky, (11th Edition)



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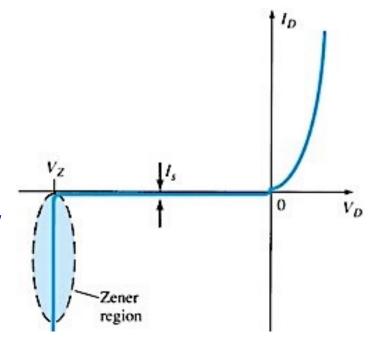


Objectives

• Become familiar with the analysis and the range of applications for Zener diodes.

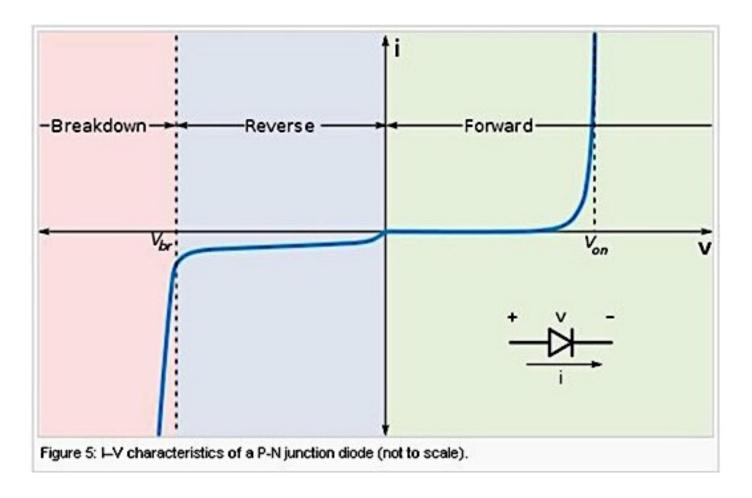
ZENER REGION

- If the *Diode is reverse biased and the voltage is increased*, a point will be reached when *the diode enters reverse breakdown* and current will flow with a very rapid rate.
- The direction of this current will be opposite to that of the positive voltage region.
- The reverse bias potential that results in this dramatic change in characteristics is called the Zener Potential /Voltage and is given by the symbol V_z .
- The reverse or Avalanche breakdown is often termed as Zener Breakdown (at very low levels).
- The region where this sharp change in characteristics occur is known as **Zener or Avalanche breakdown region** and the current is termed as avalanche current.



ZENER REGION

• The maximum reverse-bias potential that can be applied before entering the Zener Region is called the peak inverse voltage (referred to simply as the PIV rating) or the peak reverse voltage (denoted by PRV rating).

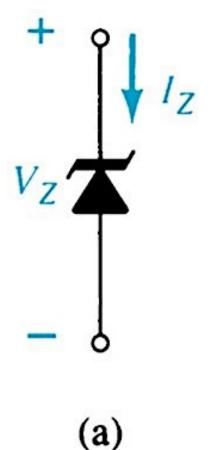


ZENER DIODE

• Diodes that employ the dramatic characteristics of a p-n junction are called Zener Diodes.

• Therefore, a Zener diode operates in reverse bias.

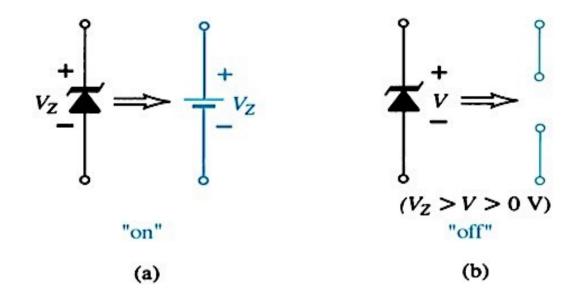
• Common Zener Voltages: 1.8V to 200V.



ZENER DIODE

• The state of the diode must be determined followed by a substitution of the appropriate model and a determination of the unknown quantities of the network.

• The <u>off state</u> is defined by a voltage <u>less than V_z but greater than 0V</u>. The Zener equivalent is the open circuit.



ZENER DIODE

- The Zener diode is a heavily doped diode which, as a result of doping, <u>has a very</u> <u>narrow depletion region</u>. This allows the diode to be operated in the reverse biased region of the characteristic curve without damaging the PN junction.
- "Zener Effect": The area of Zener diode operation (<5V) where the Diode maintains a constant voltage output while operating reverse biased.
- "Avalanche Effect": >5V applied to the diode while reverse biased which tends to cause the diode to eventually breakdown due to heat generation within the lattice structure of the crystal.
- Because of its higher temperature and current capability, <u>silicon is usually</u> <u>preferred in manufacture of Zener Diodes.</u>
- Zener Diodes provide a stable reference voltage for use in power supplies, voltmeter & other instruments, voltage regulators.

ZENER DIODE EXAMPLE

EXAMPLE 2.25 The network of Fig. 2.110 is designed to limit the voltage to 20 V during the positive portion of the applied voltage and to 0 V for a negative excursion of the applied voltage. Check its operation and plot the waveform of the voltage across the system for the applied signal. Assume the system has a very high input resistance so it will not affect the behavior of the network.

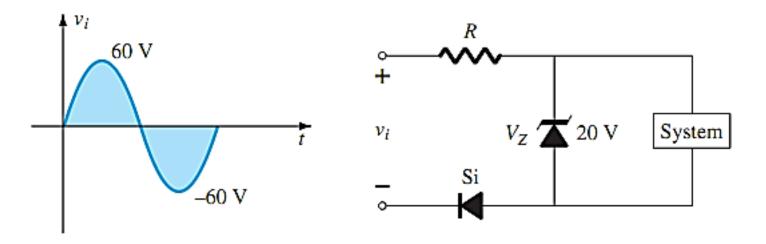
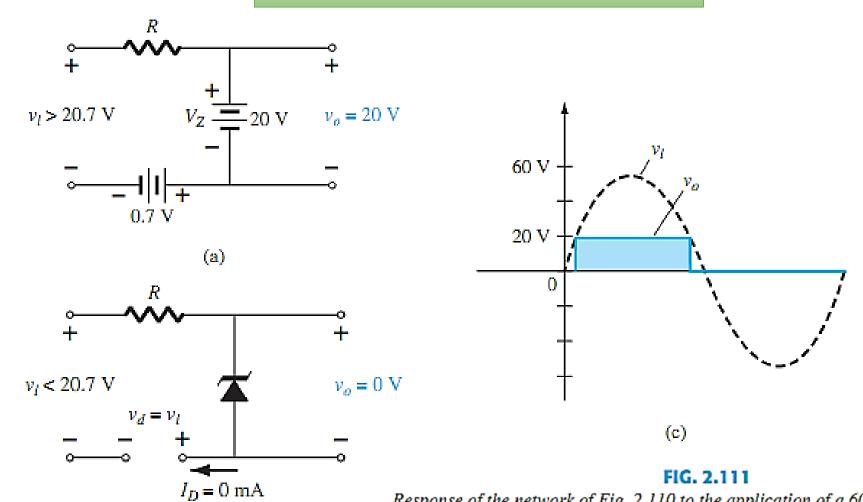


FIG. 2.110

Controlling network for Example 2.25.

ZENER DIODE EXAMPLE



Response of the network of Fig. 2.110 to the application of a 60-V sinusoidal signal.

(b)

- The simplest of Zener diode regulator networks appears in Fig. 2.112. The applied dc voltage is fixed, as is the load resistor. The analysis can fundamentally be broken down into two steps.
- 1. Determine the state of the Zener diode by removing it from the network and calculating the voltage across the resulting open circuit.

$$V = V_L = \frac{R_L V_i}{R + R_L}$$

- If $V \ge V_Z$, the Zener diode is on, and the appropriate equivalent model can be substituted.
- If V < V_Z, the diode is off, and the open-circuit equivalence is substituted.

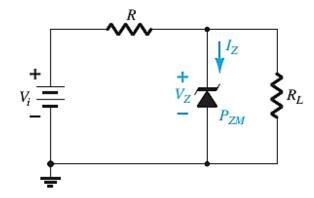


FIG. 2.112
Basic Zener regulator.

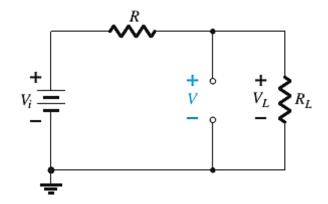


FIG. 2.113

Determining the state of the Zener diode.

2. Substitute the appropriate equivalent circuit and solve for the desired unknowns.

For the network of Fig. 2.112, the "on" state will result in the equivalent network of Fig.2.114. Since voltages across parallel elements must be the same, we find that

$$V_L = V_Z$$
 $I_Z = I_R - I_L$ $I_L = \frac{V_L}{R_L}$ and $I_R = \frac{V_R}{R} = \frac{V_i - V_L}{R}$

• The power dissipated by the Zener diode is determined by $P_Z = V_Z I_Z$

That must be less than the P_{ZM} specified for the device.

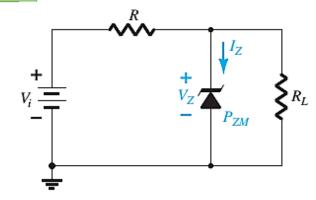


FIG. 2.112

Basic Zener regulator.

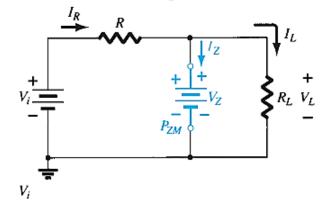


FIG. 2.114

Substituting the Zener equivalent for the "on" situation.

EXAMPLE 2.26

- a. For the Zener diode network of Fig. 2.115, determine V_L , V_R , I_Z , and P_Z .
- b. Repeat part (a) with $R_L = 3 \text{ k}\Omega$.

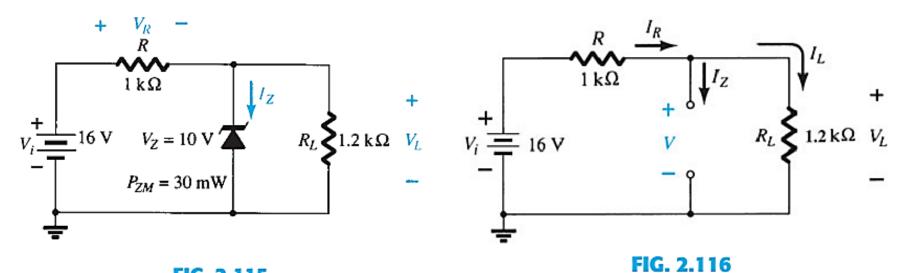


FIG. 2.115

Zener diode regulator for Example 2.26.

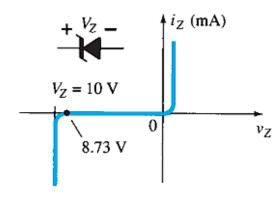
Determining V for the regulator of Fig. 2.115.

$$V = \frac{R_L V_i}{R + R_L} = \frac{1.2 \,\mathrm{k}\Omega (16 \,\mathrm{V})}{1 \,\mathrm{k}\Omega + 1.2 \,\mathrm{k}\Omega} = 8.73 \,\mathrm{V}$$

Since V = 8.73 V is less than $V_Z = 10$ V, the diode is in the "off" state.

$$V_L = V = 8.73 \text{ V}$$

 $V_R = V_i - V_L = 16 \text{ V} - 8.73 \text{ V} = 7.27 \text{ V}$
 $I_Z = 0 \text{ A}$
 $P_Z = V_Z I_Z = V_Z (0 \text{ A}) = 0 \text{ W}$



b. Applying Eq. (2.16) results in

$$V = \frac{R_L V_i}{R + R_L} = \frac{3 \text{ k}\Omega(16 \text{ V})}{1 \text{ k}\Omega + 3 \text{ k}\Omega} = 12 \text{ V}$$

FIG. 2.117

Resulting operating point for the network of Fig. 2.115.

Since V = 12 V is greater than $V_Z = 10$ V, the diode is in the "on" state

$$V_L = V_Z = 10 \text{ V}$$

 $V_R = V_i - V_L = 16 \text{ V} - 10 \text{ V} = 6 \text{ V}$
 $I_L = \frac{V_L}{R_L} = \frac{10 \text{ V}}{3 \text{ k}\Omega} = 3.33 \text{ mA}$
 $I_R = \frac{V_R}{R} = \frac{6 \text{ V}}{1 \text{ k}\Omega} = 6 \text{ mA}$
 $I_Z = I_R - I_L [\text{Eq. (2.18)}]$
 $= 6 \text{ mA} - 3.33 \text{ mA}$
 $= 2.67 \text{ mA}$

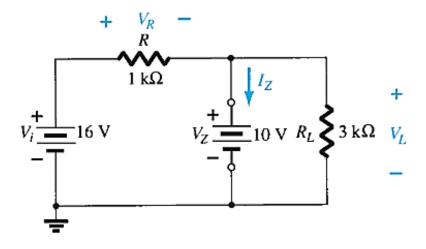


FIG. 2.118
Network of Fig. 2.115 in the "on" state.

The power dissipated is

$$P_Z = V_Z I_Z = (10 \text{ V})(2.67 \text{ mA}) = 26.7 \text{ mW}$$

which is less than the specified $P_{ZM} = 30 \text{ mW}$.

2nd CONDITION: FIXED V_i, VARIABLE R_I

• Due to the offset voltage V_z, there is a specific range of resistor values (and therefore load current) which will ensure that the Zener diode is in the on state.

• Too small $R_L \rightarrow V_L < V_z \rightarrow$ Zener diode will be in the off state.

• To determine the min R_I that will turn the Zener diode on:

$$R_{L_{\min}} = \frac{RV_Z}{V_i - V_Z} \qquad V_L = V_Z = \frac{R_L V_i}{R_L + R}$$

$$V_L = V_Z = \frac{R_L V_i}{R_L + R}$$

• Any load resistance value greater than the R_I min will ensure that the Zener diode is in the on state and the diode can be replaced by its V_z source equivalent.

• The max I₁

$$I_{L_{\text{max}}} = \frac{V_L}{R_L} = \frac{V_Z}{R_{L_{\text{min}}}}$$

2nd CONDITION: FIXED V_i, VARIABLE R_I

• Once the diode is in the on state, the voltage across R remains fixed at:

$$V_R = V_i - V_Z \qquad I_R = \frac{V_R}{R} \qquad I_Z = I_R - I_L$$

$$I_R = \frac{V_R}{R}$$

$$I_Z = I_R - I_L$$

• I_z is limited to I_{ZM} as provided on the data sheet, it does affect the range of R_L and therefore I_L . $I_{L_{min}} = I_R - I_{ZM}$

• The maximum load resistance

$$R_{L_{\text{max}}} = \frac{V_Z}{I_{L_{\text{min}}}}$$

See Example 2.27.

3rd CONDITION: FIXED R_I, VARIABLE V_i

• For fixed values of R_L, the voltage V_i must be sufficiently large to turn the Zener diode on. The min turn-on voltage V_i=V_{i min}:

$$V_{i_{\min}} = \frac{(R_L + R)V_Z}{R_L}$$

• The max value of V_i is limited by the max Zener current I_{ZM} .

$$V_L = V_Z = \frac{R_L V_i}{R_L + R} \qquad I_{R_{\text{max}}} = I_{ZM} + I_L$$

$$I_{R_{\text{max}}} = I_{ZM} + I_{L}$$

• Since I_L is fixed at V_Z/R_L and I_{ZM} is the max value of I_Z , the max V_i is defined by:

$$V_{i_{\text{max}}} = V_{R_{\text{max}}} + V_{Z}$$

$$V_{i_{\text{max}}} = V_{R_{\text{max}}} + V_{Z}$$
 $V_{i_{\text{max}}} = I_{R_{\text{max}}}R + V_{Z}$

3rd CONDITION: FIXED R_L, VARIABLE V_i

EXAMPLE 2.28 Determine the range of values of V_i that will maintain the Zener diode of

Fig. 2.121 in the "on" state.

$$V_{i_{\min}} = \frac{(R_L + R)V_Z}{R_L} = \frac{(1200 \ \Omega + 220 \ \Omega)(200 \ \Omega)}{1200 \ \Omega}$$

$$I_L = \frac{V_L}{R_L} = \frac{V_Z}{R_L} = \frac{20 \ V}{1.2 \ k\Omega} = 16.67 \ mA$$

$$I_{R_{\max}} = I_{ZM} + I_L = 60 \ mA + 16.67 \ mA$$

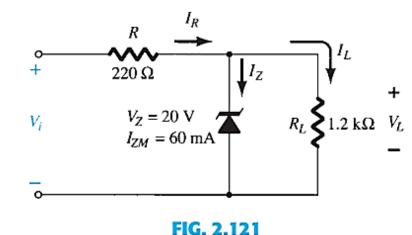
$$= 76.67 \ mA$$

$$V_{i_{\max}} = I_{R_{\max}}R + V_Z$$

$$= (76.67 \ mA)(0.22 \ k\Omega) + 20 \ V$$

$$= 16.87 \ V + 20 \ V$$

$$= 36.87 \ V$$



Regulator for Example 2.28.

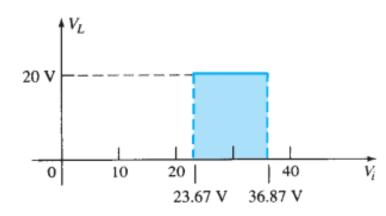


FIG. 2.122

 V_L versus V_i for the regulator of Fig. 2.121.

Thank You