

## Experiment 5

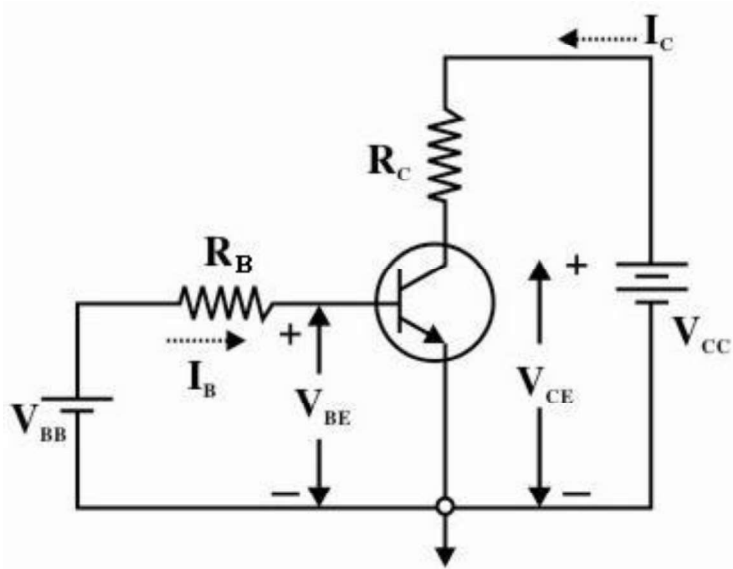
**Aim:** To study the input and output characteristics of a transistor in its various configurations (CE and CB).

**Apparatus:** Transistor kit, connecting wires, DC voltmeter (0-2V), DC voltmeter (0-20V), Ammeter (0-200 $\mu$ A), Ammeter (0-20mA)

**Theory:**

### Common emitter characteristics of NPN transistor:

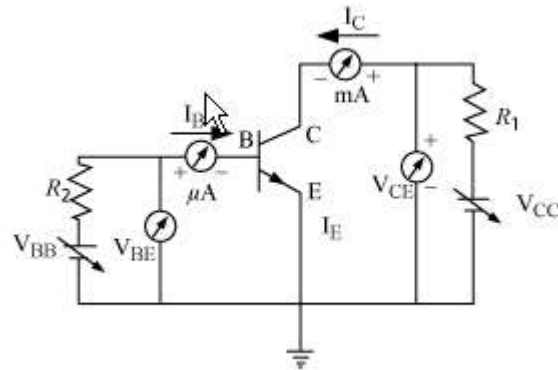
Transistor is three terminal active device having terminals collector, base and emitter. Transistor is widely used in amplifier, oscillator, electronic switch and so many other electronics circuits for variety of applications. To understand operation of the transistor, we use three configurations common emitter, common base and common collector. In this practical, we will understand common emitter configuration. As the name suggest, emitter is common between input and output. Input is applied to base and output is taken from collector.



We will obtain input characteristics and output characteristics of common emitter (CE) configuration. We will connect variable DC power supply at  $V_{BB}$  and  $V_{CC}$  to obtain characteristics. Input voltage in CE configuration is base-emitter voltage  $V_{be}$  and input current is base current  $I_b$ . Output voltage in CE configuration is collector to emitter voltage  $V_{CE}$  and output current is collector current  $I_c$ . We will use multi-meter to measure these voltages and currents for different characteristics. Collector to emitter junction is reverse biased and base to emitter junction is forward biased. The CE configuration is widely used in amplifier circuits because it provides voltage gain as well as current gain. In CB configuration current gain is less than unity. In CC configuration voltage gain is less than unity. Input resistance of CE configuration is less than CC configuration and more than CB

configuration. Output resistance of CE configuration is more than CC configuration and less than CB configuration.

### Circuit setup:



### Experiment Procedure (for input characteristics):

Connect circuit as shown in the circuit diagram for input characteristics

Connect variable power supply 0-15V at base circuit and collector circuit.

Keep  $V_{CC}$  fix at 0V (Or do not connect  $V_{CC}$ )

Increase  $V_{BB}$  from 0V to 15V, note down readings of base current  $I_B$  and base to emitter voltage  $V_{BE}$  in the observation table.

Repeat above procedure for  $V_{CC} = +5V$  and  $V_{CC} = +10V$

Draw input characteristics curve. Plot  $V_{BE}$  on X axis and  $I_B$  on Y axis.

### Observation table:

Sr. No.	$V_{CC} = 0V$		$V_{CC} = +5V$		$V_{CC} = +10V$	
	$V_{BE}$	$I_B$	$V_{BE}$	$I_B$	$V_{BE}$	$I_B$
1						
2						
3						
4						
5						
6						

### Circuit setup for output characteristics:

### Experiment Procedure (for output characteristics):

Connect circuit as shown in the circuit diagram for output characteristics

Connect variable power supply 0-15V at base circuit and collector circuit.

Keep base current fix (Initially 0)

Increase  $V_{CC}$  from 0V to 15V, note down readings of collector current  $I_C$  and collector to emitter voltage  $V_{CE}$  in the observation table.

Repeat above procedure for base currents  $I_B = 5\mu A$ ,  $50\mu A$ ,  $100\mu A$ . Increase base current by increasing  $V_{BB}$ .

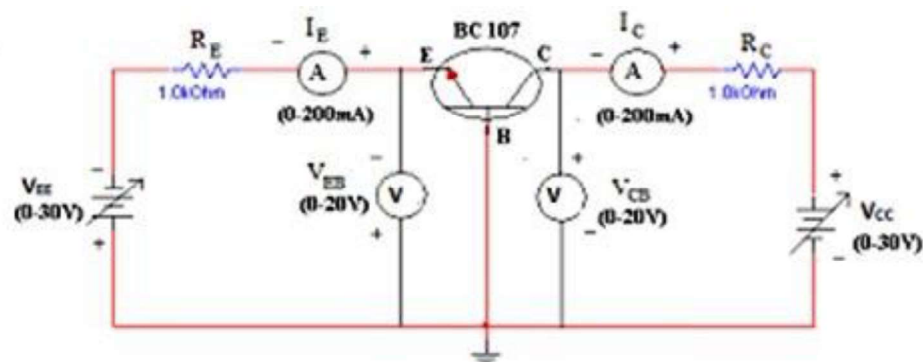
Draw output characteristics curve. Plot  $V_{CE}$  on X axis and  $I_C$  on Y axis.

Sr. No.	$I_B = 0$		$I_B = 5\mu A$		$I_B = 50\mu A$		$I_B = 100\mu A$	
	$V_{CE}$	$I_C$	$V_{CE}$	$I_C$	$V_{CE}$	$I_C$	$V_{CE}$	$I_C$
1								
2								
3								
4								
5								
6								
7								
8								

### Common Base characteristics of NPN transistor

In a common base configuration, base terminal is common between input and output. The output is taken from collector and the input voltage is applied between emitter and base. The base is grounded because it is common. To obtain output characteristics, we will measure collector current for different value of collector to base voltage ( $V_{CB}$ ). Input current is emitter current  $I_E$  and input voltage is  $V_{EB}$ . To plot input characteristics we will plot  $V_{EB}$  versus  $I_E$ . Current gain for CB configuration is less than unity. CB configuration is used in common base amplifier to obtain voltage gain. Output impedance of common base configuration is very high. CB amplifier is used in multi-stage amplifier where impedance matching is required between different stages.

**Circuit diagram to obtain input and output characteristics:**



**Experiment Procedure to obtain input characteristics:**

Connect circuit as shown in the circuit diagram for input characteristics

Connect variable power supply 0-15V (VEE) at emitter base circuit and another power supply 0-30V at collector base circuit (Vcc).

Keep Vcc fix at 0V (Or do not connect Vcc)

Increase VEE from 0V to 15V, note down readings of emitter current  $I_e$  and emitter to base voltage  $V_{eb}$  in the observation table.

Repeat above procedure for  $V_{cc} = +5V$  and  $V_{cc} = +10V$

Draw input characteristics curve. Plot  $V_{eb}$  on X axis and  $I_e$  on Y axis.

**Experiment Procedure to obtain output characteristics:**

Connect circuit as shown in the circuit diagram for output characteristics

Connect variable power supply 0-15V at emitter circuit and collector circuit.

Keep emitter current fix (Initially 0)

Increase VCC from 0V to 15V, note down readings of collector current  $I_c$  and collector to base voltage  $V_{cb}$  in the observation table.

Repeat above procedure for base currents  $I_e = 1mA$ , 5 mA and 10mA. Increase emitter current by increasing VEE.

Draw output characteristics curve. Plot  $V_{cb}$  on X axis and  $I_c$  on Y axis.

**Observation table for input characteristics:**

Sr. No.	Vcc = 0V		Vcc = +5V		Vcc=+10V	
	Veb	Ie	Veb	Ie	Veb	Ie
1						
2						
3						
4						
5						
6						

**Observation table for output characteristics:**

Sr. No.	I <sub>e</sub> = 0		I <sub>e</sub> = 1 mA		I <sub>e</sub> = 5 mA		I <sub>e</sub> = 10 mA	
	V <sub>cb</sub>	I <sub>c</sub>	V <sub>cb</sub>	I <sub>c</sub>	V <sub>cb</sub>	I <sub>c</sub>	V <sub>cb</sub>	I <sub>c</sub>
1								
2								
3								
4								
5								
6								
7								
8								

**Result:** Draw the input and output characteristics of CE and CB configuration.