GENERAL CHAIRS
Ben Juurlink,
TU Berlin, Germany

Keshav Pingali,
University of Texas Austin, USA

PROGRAM CHAIRS
André Seznec,
INRIA/IRISA, France

Lawrence Rauchwerger,
Texas A&M University, USA

EDITOR-IN-CHIEF OF ACM TACO
Tom Conte
Georgia Institute of Technology, USA

WORKSHOPS/TUTORIAL CHAIR
Sascha Uhrig
TU Dortmund, Germany

POSTER CHAIRS
Koen De Bosschere
Ghent University, Belgium

Qing Yi
University of San Antonio, USA

INDUSTRIAL EXHIBIT AND EU PROJECTS
Henri-Pierre Charles
CEA, France

Rosa M. Badia
BSC, Spain

SPONSORSHIP CHAIR
Albert Cohen
INRIA, France

PUBLICITY CHAIRS
Philip Brisk
U.C Riverside, USA

Nikola Puzovic
BSC, Spain

FINANCE CHAIR
Jeroen Borghs
Ghent University, Belgium

SUBMISSION CHAIR
Michiel Ronsse
Ghent University, Belgium

WEB AND REGISTRATIONS CHAIR
Klaas Millet
Ghent University, Belgium

LOCAL ORGANIZING COMMITEE
Nico Moser
TU Berlin, Germany

Paula Herber
TU Berlin, Germany

Reinier van Kampenhout
Fraunhofer FIRST, Germany

Organized by

CALL FOR PAPERS

HiPEAC 2013
8th International Conference on High-Performance and Embedded Architectures and Compilers

January 21-23, 2013
Berlin, Germany
http://www.hipeac.net/conference

Sponsored by

The HiPEAC conference is the premier scientific networking forum for experts in computer architecture, programming models, compilers and operating systems for embedded and general-purpose systems. Emphasis is given on cross-cutting research and innovative ideas (new programming models, novel architecture approaches, new technologies, etc.). The conference hosts a number of associated workshops, tutorials, a large poster session and an exhibition that run in parallel with the conference.

JOURNAL FIRST PUBLICATION MODEL

In 2011, TACO and HiPEAC jointly carried out an experiment with a publication model where original contributions on HiPEAC topics were solicited for TACO. Record-high numbers of submitted and accepted papers witness the significant interest in this model.

This year, TACO seeks original submissions at any time. Accepted TACO papers by November 15, 2012 and whose topics match those of HiPEAC will be selected and invited for presentation at HiPEAC conference. In order to enjoy two rounds of review, papers should be submitted no later than June 18, 2012.

Topics of interest include, but are not limited to:
- Processor architectures
- Memory system optimization
- Power, performance and implementation efficient designs
- Reliability and real-time support in processors, compilers and run-time systems
- Network and security processors
- Application-specific processors and accelerators
- Reconfigurable architectures
- Simulation and methodology
- Hardware and run-time support for programming languages
- Compiler techniques
- Feedback-directed optimization
- Program characterization and analysis techniques
- Dynamic compilation, adaptive execution, and continuous profiling/optimization
- Binary translation/optimization
- Code size/memory footprint optimizations

IMPORTANT DATES
- Workshops/tutorials: **June 1, 2012**
- Papers: **June 18, 2012**
- Posters: **October 15, 2012**
- Paper selection date: **November 15, 2012**