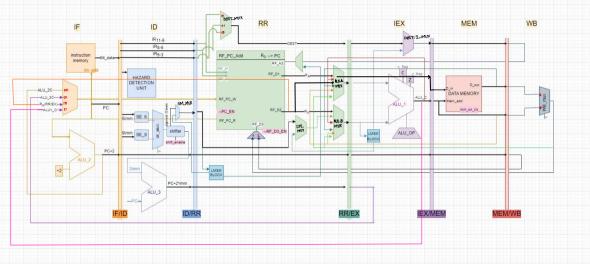
ADD NAND RA+6irm — RB (") 0000 ADI 0011 LLI LW 0100 M (RB+6imm) - RA (Din) 0101 SW if Branch PC'= PC+ 2 imm 1000 BEB 1001 BLT 1010 BLE Dest RA. (PC+2); PC+2 imm 1100JAL : Dest RA (PC+2); RB JLR 1101 : to RA + 2 imm - 1111 TRI



	000 -000 000	o Anin Onio	alu ob		
	000 0000 0000 0011 ADD, ADC, ADZ, ACA		alu-op		
1		(//////////////////////////////////////	YOU, NEW, NDZ	NCU, NCC, NCZ }	0010
- F	AWC, ACW		0100	0101	0010
C					
1F	ID	RR	€X	mem	พธ
RF_PC_R → IM	add IR11-9	→ (R _{II} -a —+ RF_A,			-dul - RF_Az_mux
RE_PC_R ALU-	-2A IR8-6		→ Dest2) - OUST - 1 KF-113 -
'+2' ALU-		→ → Dust → —	<u> </u>		RF_A3
ALV-2C - MUX			A_mux ALU_A		
MUX	ю	RA (RF_DI) - ALV. RB (RF_D2) - ALV.	_B-mux		
lm_data		KB UKF = F2) · I ·····	B_MUX ALV-B		
	\longrightarrow		ALU-C-	}	-ALV-C-+WB_mux-
PC+2 PC		\rightarrow		>	RF_D3 ← RF_D3_mux←
PL —	7		DO DEST_2_MUX=	0	1 100 14111 0
MUX: 00	SH_EN=0	DEST_MUX =	ALU_CHaq = 1		
PC_EN=1	SM_MUX =0	LMLMUX =0	ALU - 2 - 100 = 1		RF_D_3 _MUX = 0
	SE_MUX = 0	ALU-A-MUX =	000		RF_D3-EN= 1
		ALU-B-MUX	-000		RF_A3_MVX = D
0000		ob an aplada	(Ra	+ 9mm6)-	→ Rh
F	ADI ALU-	op: 00000de	~ (na	עס ויווייט ד	עיו
			1	1	1
IF	ID	RR	ε×	mem	WB
	110.	10 1050			(188-6)
RF_PC_R → IMC		\rightarrow $ R_{11}-a \rightarrow R_{1}-A_{1} $	Dest2	, ————————————————————————————————————	-dul -1 RF-A3_mux
RF_PC_R →ALU_		→ h	T Desk Z		1
'+2' ALU-2	B	L> Dest → —	A_mux - ALU_A		RF_A3
ALV-2C - MUX	SEG - SE-MUX-151	hijten RA (RF_DI) → ALU-			
MUX	0 I I I I	X-1-LM_MUX -+ ALU-B.			
lm_data	SMILIM				ALV-C-+WB_mux-
PC+2 -		\rightarrow	ALU-C		RF_D3 ← RED3_mux←
PC —	→	→			, , , , , ,
	SH_EN=D	DEST_MUX =	OI DEST_2_MUX=0	MEM_WR_EN=0	WB_MVX = 0
MUX: DD		LM_MUX = 0	ALU-Ctag = 1		RF_D3_MUX = 0
PC_EN=1	SM_MUX = 0 SE_MUX = 0	ALU-A-MUX =	ALU - 2- floor = 1		RF_D3_EN= I
	2E-1410 x = 0	ALU-B-MUX=			RF_A3_mux=b
		A LV-B-MOX-	901		10.2.3



SW 0101	alu	_ap = add 10	000)	sw ra	, rb, 9 mm
IF	ID	RR	ϵ^{x}	mem	WB
$\begin{array}{ccc} RF_PC_R & \longrightarrow IMods \\ RF_PC_R & \longrightarrow ALV_2s \\ +2 & \longrightarrow ALV_2B \\ ALV_2C & \longrightarrow MUX \end{array}$	1R8-6	RB (RF-D2) - ALU-A-MUX	A_mux — ALU_A	RA—1 Din	
MUX — RF_PC W m_data PC+2 PC	SM_mux	-ILM_MUX -I PLU-B_MUX		alu_c —1 mem_add	
MUX: 00 PC_EN=	SH_EN=0 SM_MUX=0 SE_MUX=0	DEST_MUX = 00 LM_MUX = 0 ALU-A_MUX = 001 ALU-B_MUX = 001	Dest_2_mux=0 ALU_C flag = 0 ALU_2_flag = 0	MEM_WREN=1	WB_MUX = 0 RF_D3_MUX = 0 RF_D3_EN= 0 RF_A3_mux = 0
Bes 100			LE 1010		
1F	ID	RR	ex	mem	WB
$\begin{array}{cccccccccccccccccccccccccccccccccccc$	1R8-6	Ra(RF-D1) - ALU-A-MUX RBCRF-D2) - ALU-B-MUX	.		
Im_data Pc+2————————————————————————————————————	from ALV3, PC+2imm	KB CK	B_mux		
MUX: 00 PC_EN=1	SH_EN= 1 SM_MUX =0 SE_MUX = 0	DEST_MUX=00 LM_MUX=0 ALU-A_MUX=000 ALU-B_MUX=000	=01 14 =010 =	MEM_WREN=0	WB_MUX = 0 RF_D3_MUX = 0 RF_D3_EN= 0 RF_A3_MUX = 0
		W	PC-EN=1		

