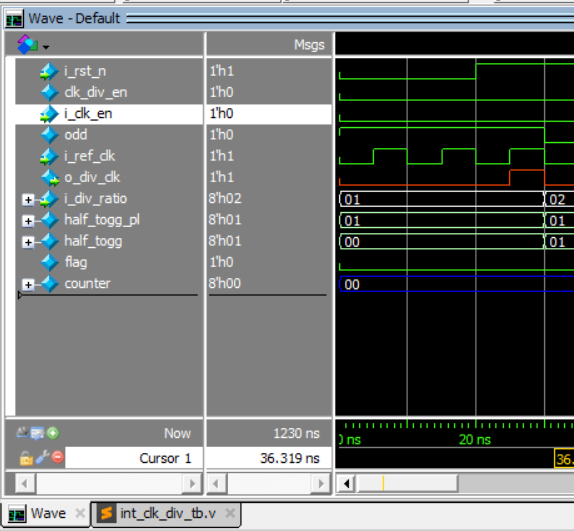
**Assignment Integer Clock Divider**

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| --- |
| Name |
| Shams Khaled Ezzat |

1. Initializing ports



1. Dividing clk frequency by 2 with enable =0

A computer screen shot of a screen

AI-generated content may be incorrect.

1. Dividing clk frequency by 2 with active enable A screen shot of a computer

   AI-generated content may be incorrect.
2. Dividing clk frequency by 4 with active enable A screenshot of a computer

   AI-generated content may be incorrect.
3. Dividing clk frequency by 6 with active enable A screen shot of a computer

   AI-generated content may be incorrect.
4. Dividing clk frequency by 1 with active enable A screen shot of a computer

   AI-generated content may be incorrect.
5. Dividing clk frequency by 3 with active enableA screen shot of a computer

   AI-generated content may be incorrect.
6. Dividing clk frequency by 5 with active enable A screen shot of a computer

   AI-generated content may be incorrect.
7. Dividing clk frequency by 7 with active enable A screenshot of a computer

   AI-generated content may be incorrect.
8. Full wave form

A screen shot of a computer

AI-generated content may be incorrect.

* Design A screen shot of a computer program

  AI-generated content may be incorrect.
* Testbench

A screen shot of a computer program

AI-generated content may be incorrect.

* Tasks

A computer screen shot of a program

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* Run file

A screen shot of a computer

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* Source list

A screenshot of a computer program

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* Wave.do A screenshot of a computer

  AI-generated content may be incorrect.