30.2.1.8 SPI Transfer Format

Exynos 4412_UM

The Exynos 4412 supports four different formats for data transfer.

Figure 30-1 illustrates four waveforms for SPICLK.

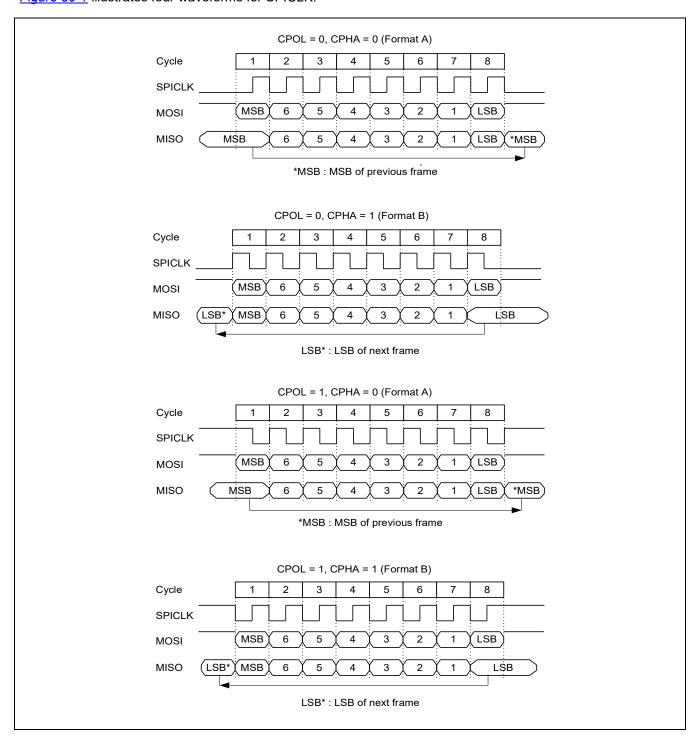


Figure 30-1 SPI Transfer Format

