FPGA使用说明

设置触发方式

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| E | ADD | ?? | TRI | A | 5 |
| 命令 | 4位地址 |  | 触发方式 | 结束符 | |

ADD表示设置哪个通道

Trig[3]=1表示本通道使能

Trig[2..0]=00,11,01,10分别表示低电平，高电平，上升沿，下降沿触发

初始化Trig（清空Trig寄存器）

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| D | ?? | ?? | ?? | A | 5 |
| 命令 |  |  |  | 结束符 | |

Enable Trig

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| C | ?? | ?? | ?? | A | 5 |
| 命令 |  |  |  | 结束符 | |

Disable Trig

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| B | ?? | ?? | ?? | A | 5 |
| 命令 |  |  |  | 结束符 | |

设置触发位置

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| A | ?? | Len | A | 5 |
| 命令 |  | 后六位表示触发深度 | 结束符 | |

允许下一次触发

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| 9 | ?? | ?? | ?? | A | 5 |
| 命令 |  |  |  | 结束符 | |

设置TimeStep

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| 8 | 时钟设置 | ?? | ?? | A | 5 |
| 命令 | 0:500M  8：外部时钟1  9：外部时钟2  A:50M  B:5M  C:500K  D:50K  E：5K  F：500Hz |  |  | 结束符 | |