

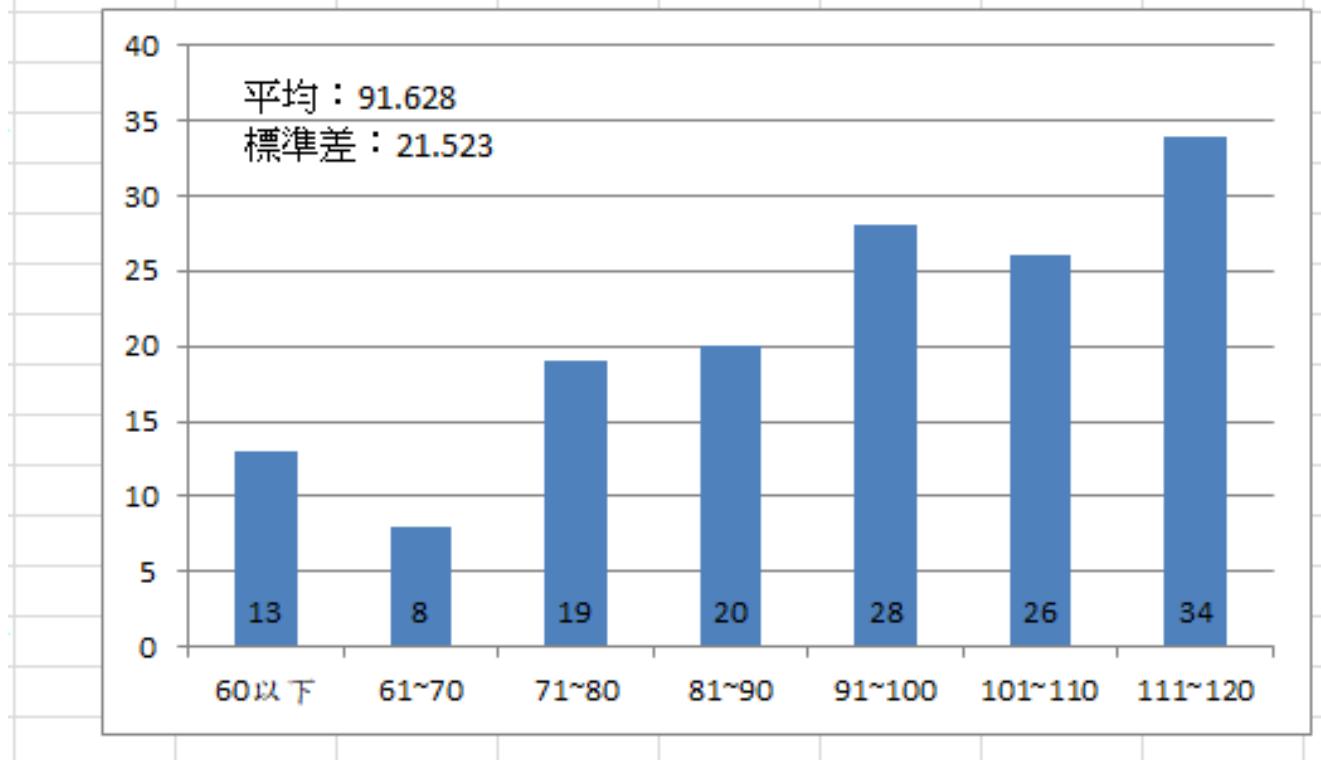
# 電子電工學

## Lecture 6



# Midterm I Results

60 以下	60	13	平均	91.62838
61~70	70	8	標準差	21.52274
71~80	80	19		
81~90	90	20		
91~100	100	28		
101~110	110	26		
111~120	120	34		



Chapter 5

# **CONTROLLED SOURCES AND NONLINEAR COMPONENTS**

# Voltage-controlled current source (dependent source)

INDEPENDENT SOURCE

⇒ DEPENDENT SRC

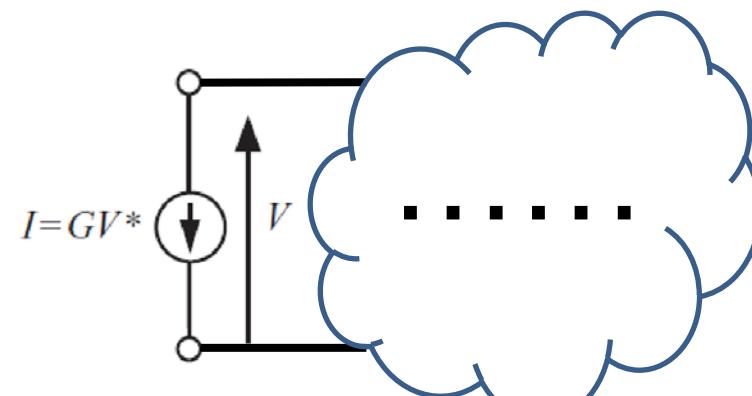
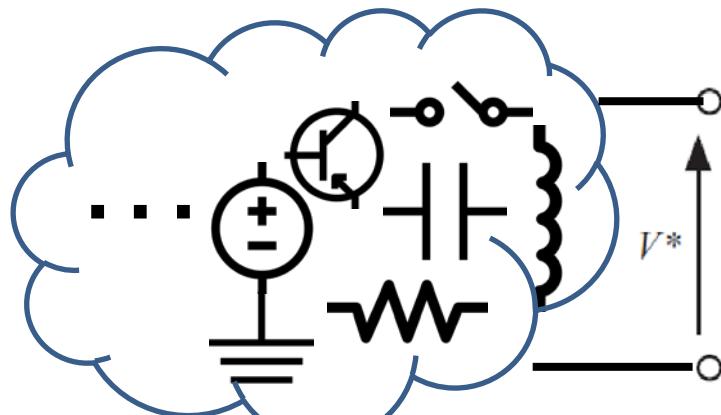
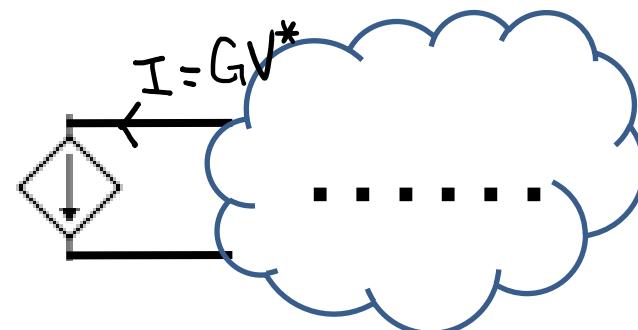
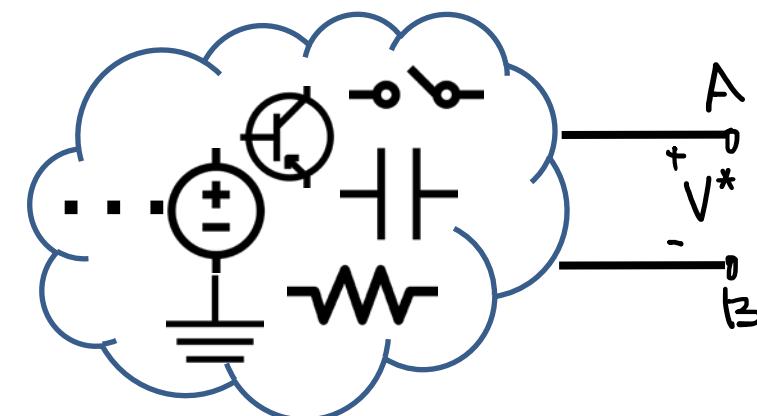
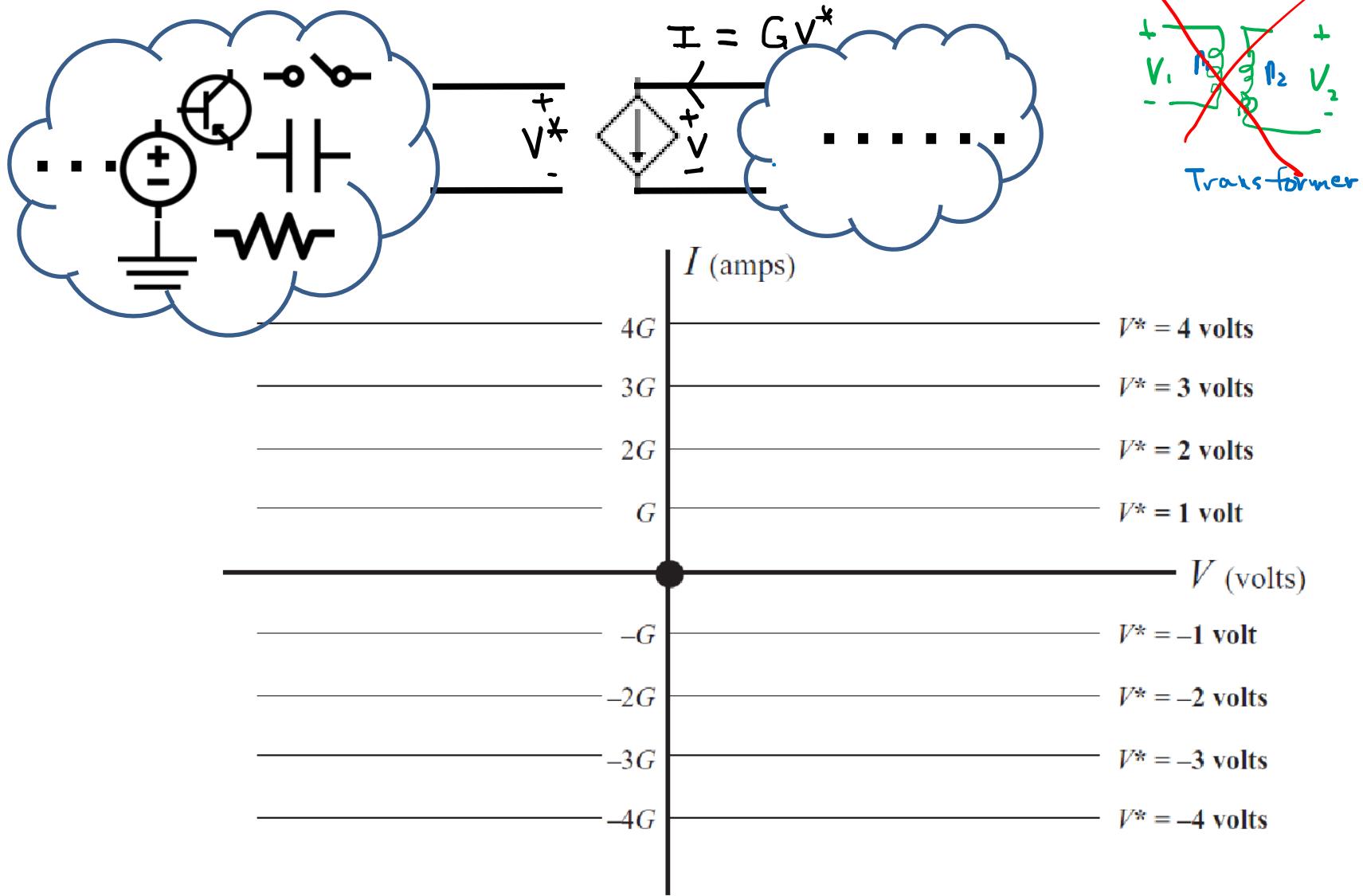


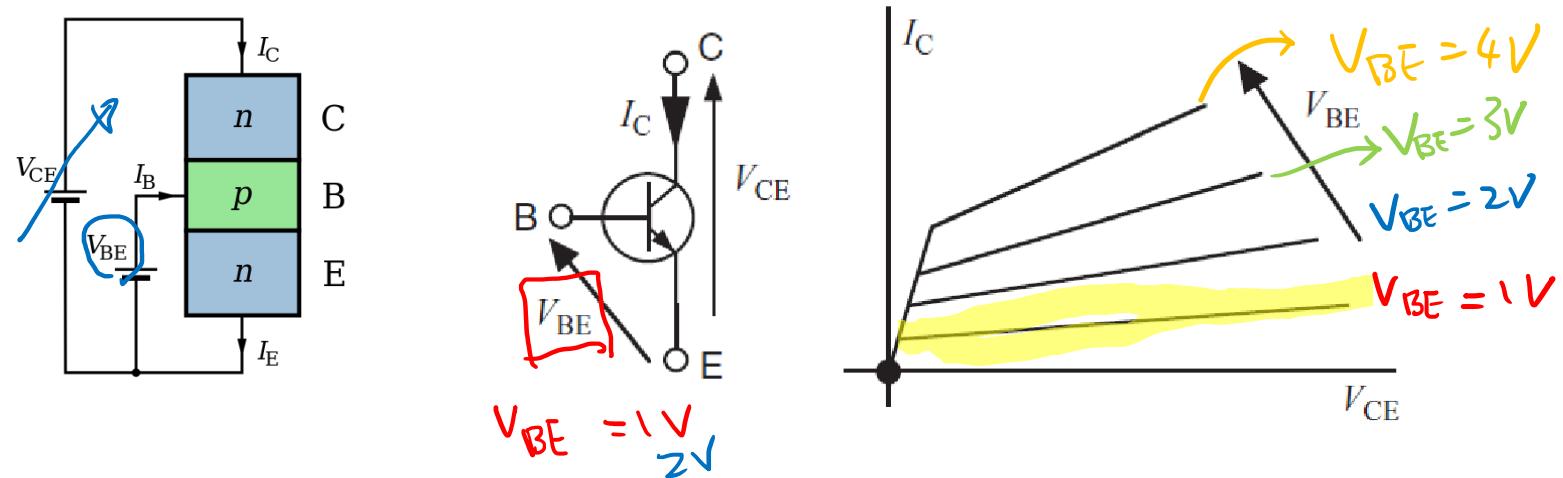
Figure 5.1 Representation of a voltage-controlled current source

# Voltage-controlled current source



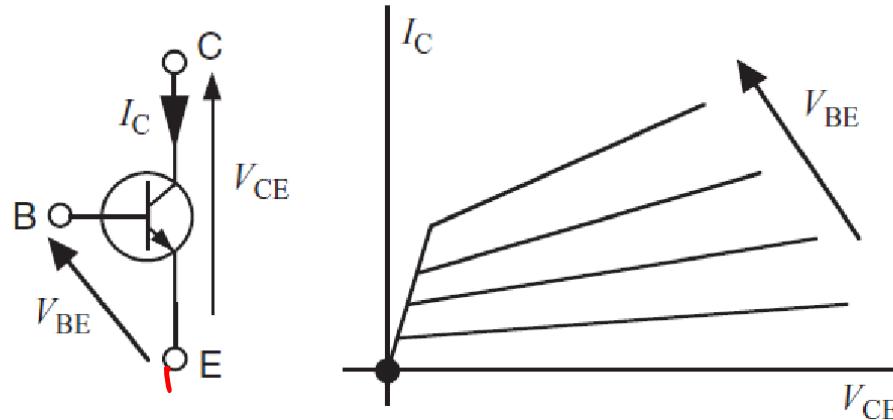
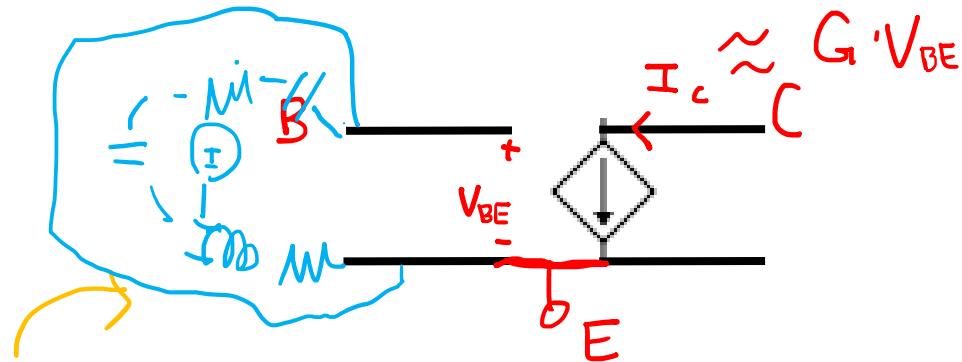
**Figure 5.2** The voltage-current characteristics of a voltage-controlled current source

# Transistor characteristics



**Figure 5.3** The symbol for a transistor, with terminals E (emitter), B (base) and C (collector) and the (sketched) measured characteristics showing the control of the current  $I_C$  by the voltage  $V_{BE}$

# Transistor characteristics



**Figure 5.3** The symbol for a transistor, with terminals E (emitter), B (base) and C (collector) and the (sketched) measured characteristics showing the control of the current  $I_C$  by the voltage  $V_{BE}$

# Transistor characteristics

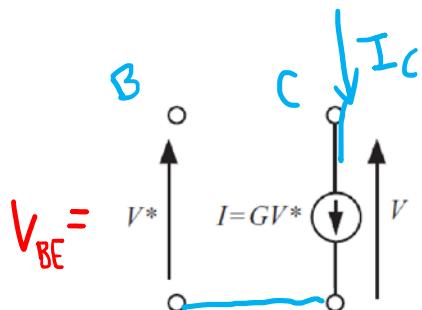


Figure 5.1 Representation of a voltage-controlled

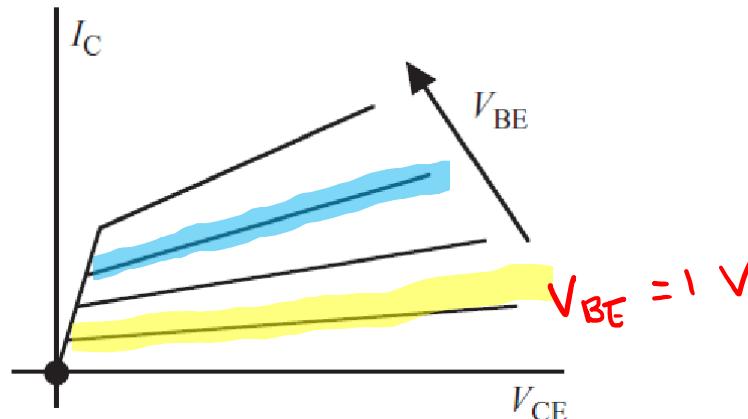
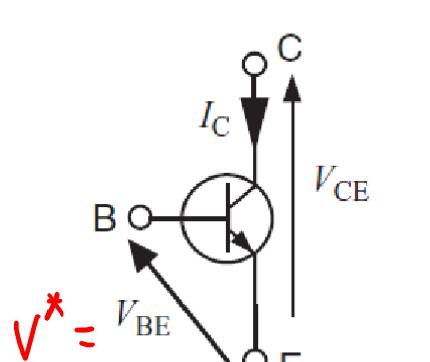
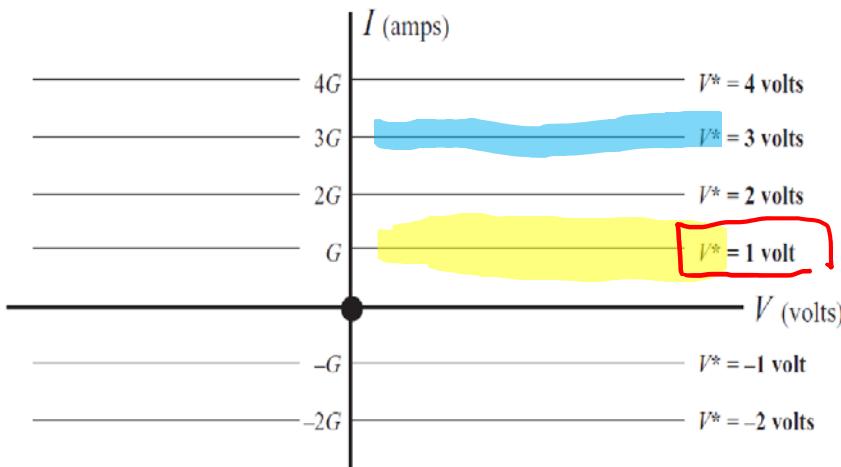
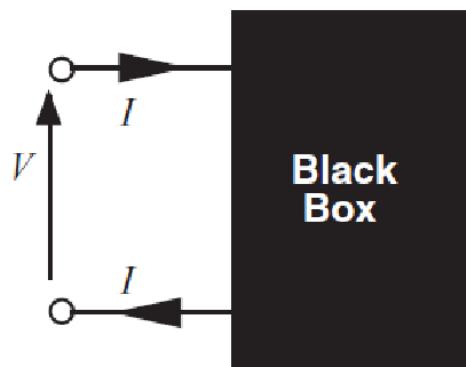


Figure 5.3 The symbol for a transistor, with terminals E (emitter), B (base) and C (collector) and the (sketched) measured characteristics showing the control of the current  $I_c$  by the voltage  $V_{BE}$

# Active component

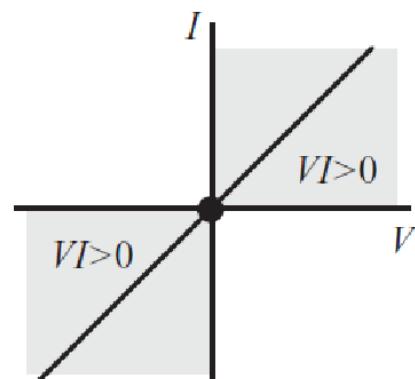
$$\text{Power} = V \times I > 0 \quad \text{dissipate}$$

$$V \times I < 0 \quad \text{provide}$$

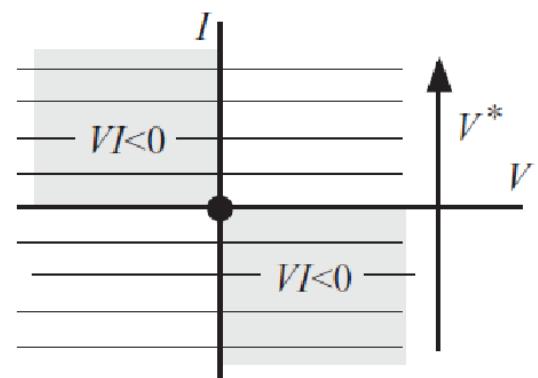


Power supplied to box =  $VI$

(a)



(b)



(c)

**Figure 5.4** (a) The condition for a component to be passive; (b) a resistor is passive: it cannot supply energy; (c) a VCCS is said to be active because it can supply energy if it is operating in the appropriate shaded region

# Example 5.1 (Nodal analysis)

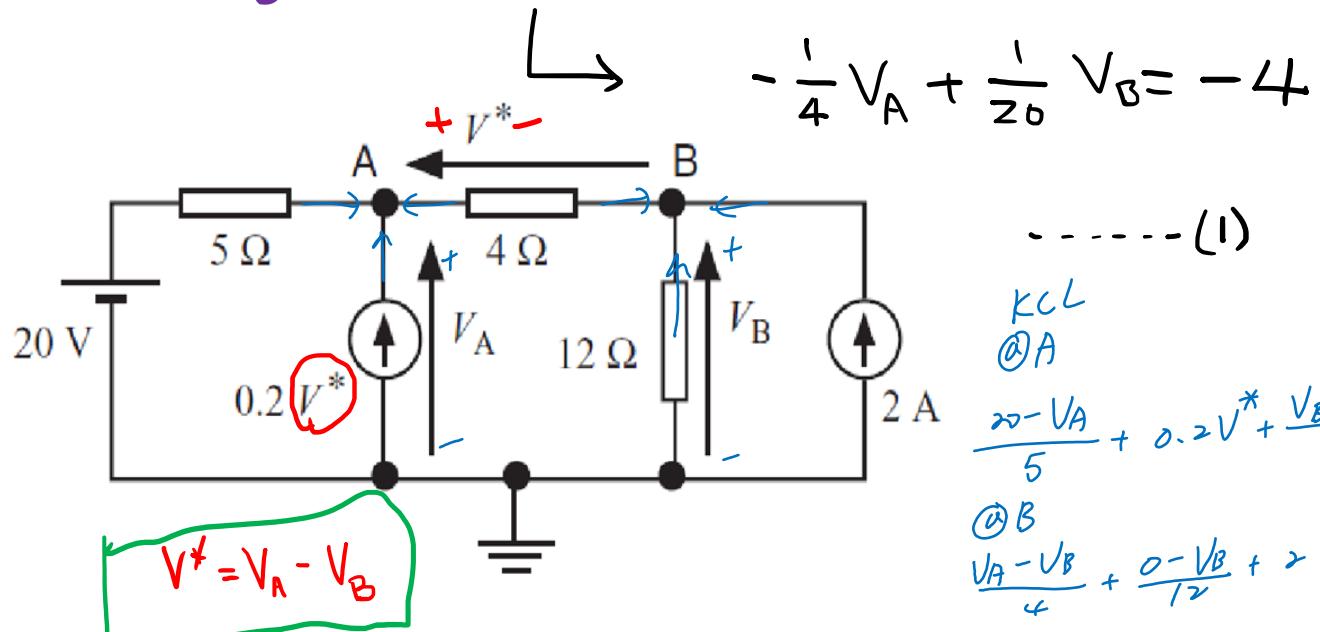
$V_A=? \quad V_B=?$

← { Ohm's  
KCL  
KVL }

KCL @ A (INWARD +)

$$\frac{20 - V_A}{5} + 0.2 V^* + \frac{V_B - V_A}{4} = 0$$

$$-\frac{1}{4} V_A + \frac{1}{20} V_B = -4$$



..... (1)

KCL  
@A

$$\frac{20 - V_A}{5} + 0.2 V^* + \frac{V_B - V_A}{4} = 0$$

@B

$$\frac{V_A - V_B}{4} + \frac{0 - V_B}{12} + 2 = 0$$

**Figure 5.5** The circuit analysed in Example 5.1. It contains one voltage-controlled current source whose mutual conductance is  $0.2 \text{ A/V}$  (i.e.,  $0.2 \text{ S}$ )

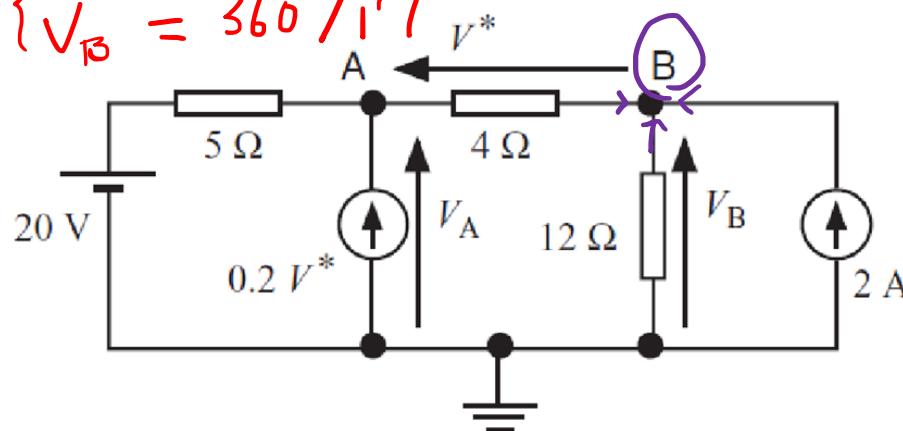
## Example 5.1

KCL @ B (INWARD +)

$$\frac{V_A - V_B}{4} + \frac{0 - V_B}{12} + 2 = 0$$

$$\Rightarrow \frac{V_A}{4} - \frac{V_B}{3} = -2 \quad (2)$$

$$(1)(2) \Rightarrow \begin{cases} V_A = 344/17 \\ V_B = 360/17 \end{cases}$$



**Figure 5.5** The circuit analysed in Example 5.1. It contains one voltage-controlled current source whose mutual conductance is 0.2 A/V (i.e., 0.2 S)

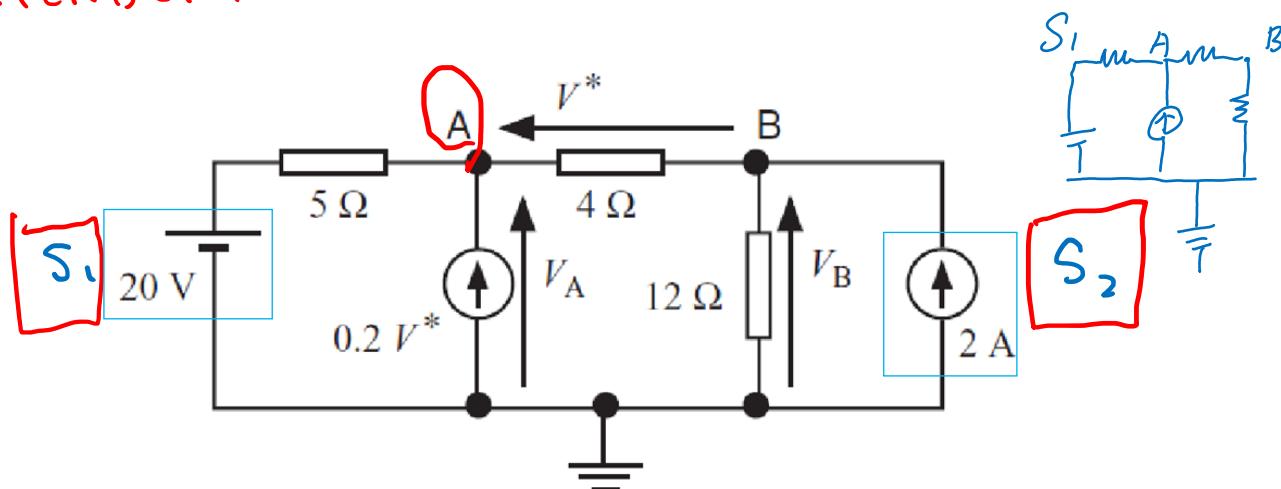
## Example 5.2 (Superposition)

$$V_A = V_A|_{S_1} + V_A|_{S_2}$$

( ONLY INDEPENDENT SOURCE )

INDEPENDENT Sources : Activate one-by-one  
Disable others

DEPENDENT Sources : "LEFT ALONE"



**Figure 5.5** The circuit analysed in Example 5.1. It contains one voltage-controlled current source whose mutual conductance is 0.2 A/V (i.e., 0.2 S)

## Example 5.2

Source 1 only

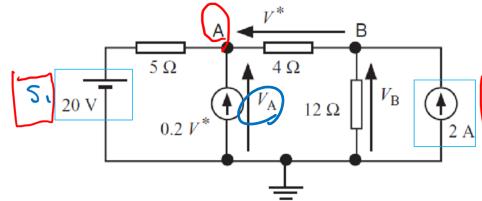
KCL @ A :

$$V_A = V_A|_{S_1} + V_A|_{S_2}$$

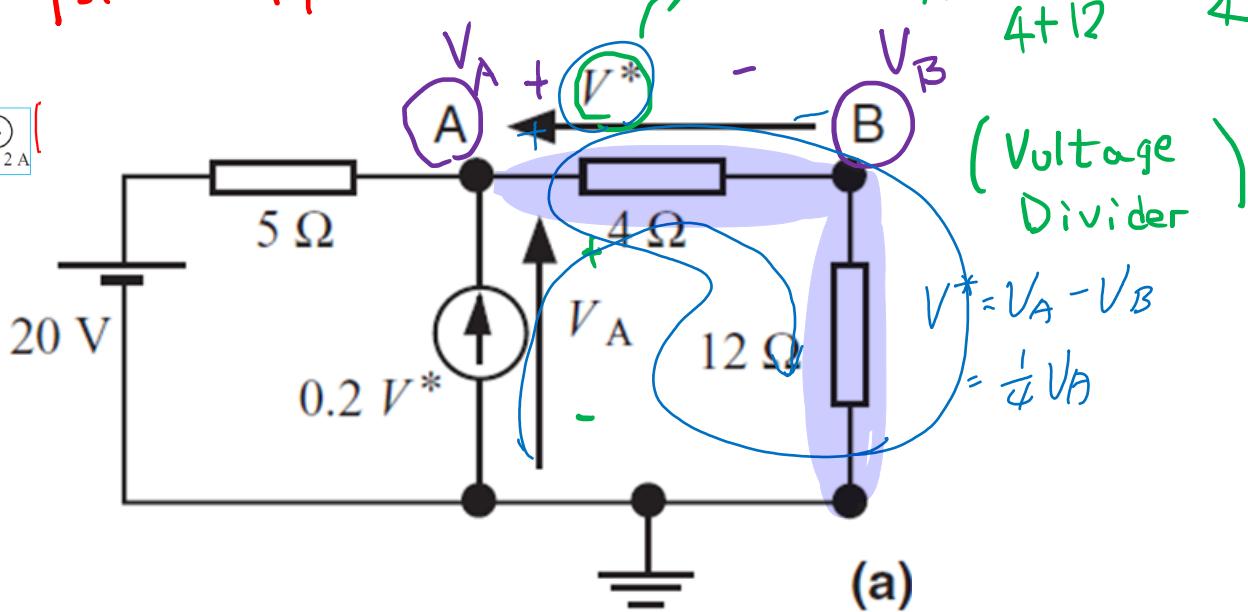
$$S_1 : 20V \quad -V^* = -\frac{V_A}{4}$$

$$\frac{20-V_A}{5} + 0.2V^* + \frac{[V_B-V_A]}{4} = 0$$

$$\Rightarrow V_A|_{S_1} = \frac{320}{17} (V)$$



$S_1$



(a)

# Example 5.2

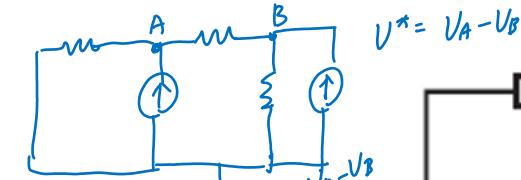
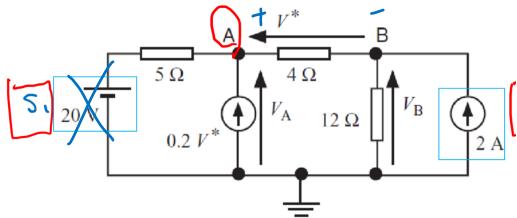
Source 2 only

$S_2 : 2A$  (By Nodal Analysis)

$$\text{KCL @ A} : \frac{0-V_A}{5} + 0.2(V_A - V_B) + \frac{V_B - V_A}{4} = 0 \rightarrow \left\{ -\frac{1}{4}V_A + \frac{1}{20}V_B = 0 \right.$$

$$\text{KCL @ B} : \frac{V_A - V_B}{4} + 0 - \frac{V_B}{12} + 2 = 0 \rightarrow \left. \frac{1}{4}V_A - \frac{1}{3}V_B = -2 \right.$$

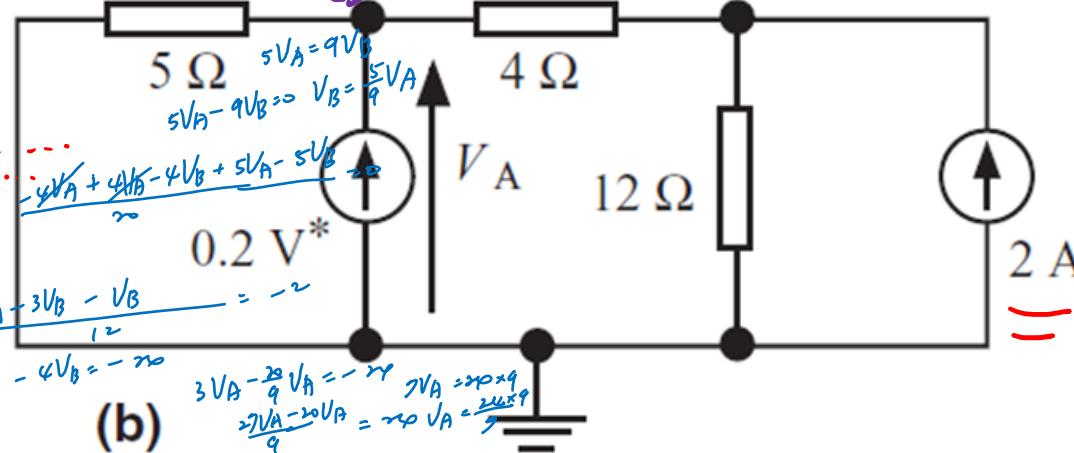
$$\Rightarrow V_A \Big|_{S_2} = \frac{24}{17} \text{ (v)}$$



$$\text{KCL @ A} : \frac{0-V_A}{5} + 0.2V^* + \frac{V_B - V_A}{4} = 0$$

$$\text{KCL @ B} : \frac{V_A - V_B}{4} + \frac{0-V_B}{12} + 2A = 0$$

$$V^* = V_A - V_B$$



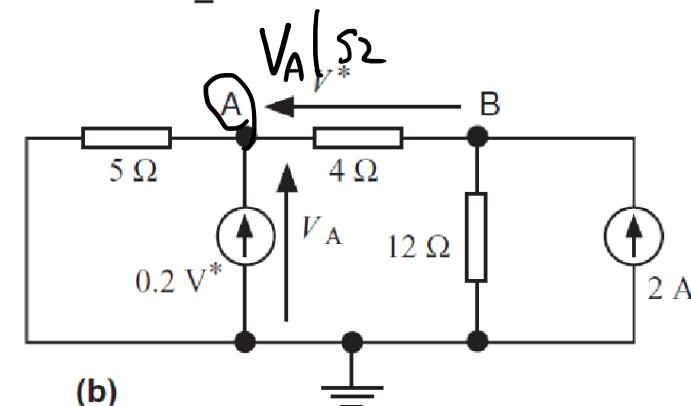
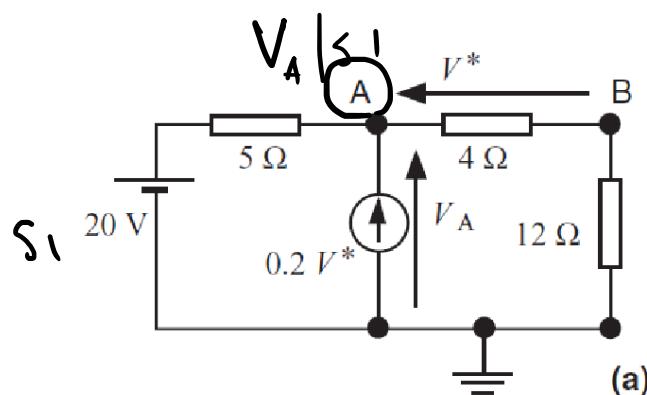
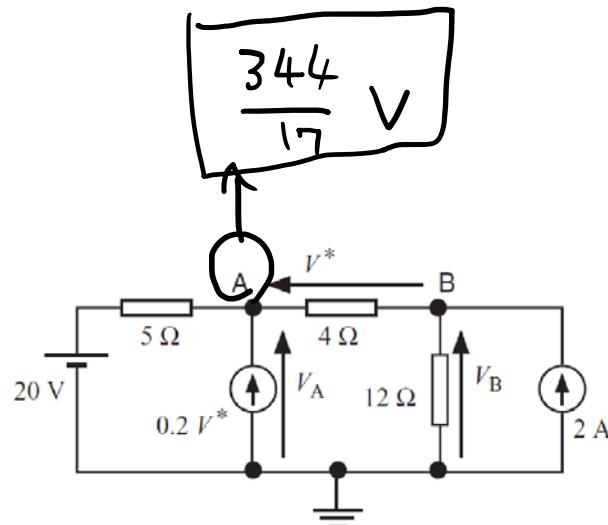
(b)

## Example 5.2

$$V_A = V_{A|s_1} + V_{A|s_2}$$

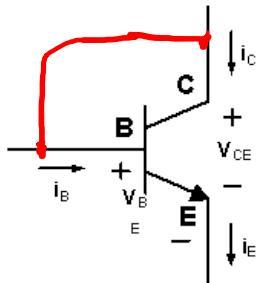
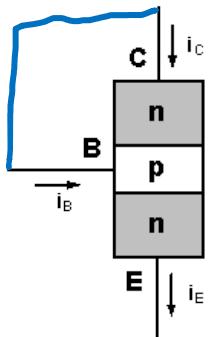
$$= \frac{320}{17} + \frac{24}{17}$$

$$= \frac{344}{17} \text{ (V)}$$

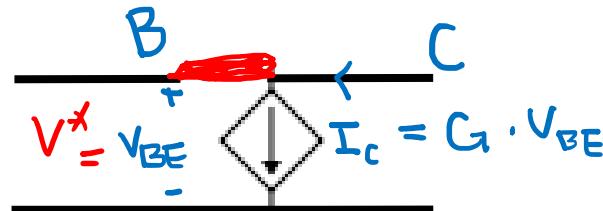


**Figure 5.6** Circuits prepared for application of the superposition principle to the analysis of the circuit of Figure 5.5. From (a) it is found that the component of  $V_A$  due to the independent 20 V source is  $320/17$  volts. From (b) it is found that the component of  $V_A$  due to the independent 2 A source is  $24/17$  V. Superposition allows us to state that the actual value of  $V_A$  in the circuit of Figure 5.5 is  $320/17 + 24/17 = 344/17$  V

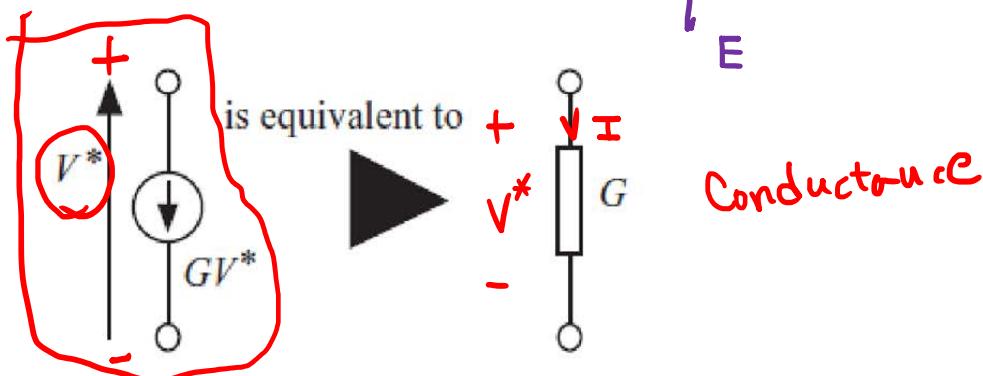
# Equivalent resistor



$$V_{BE} = V_{CE} = V^*$$

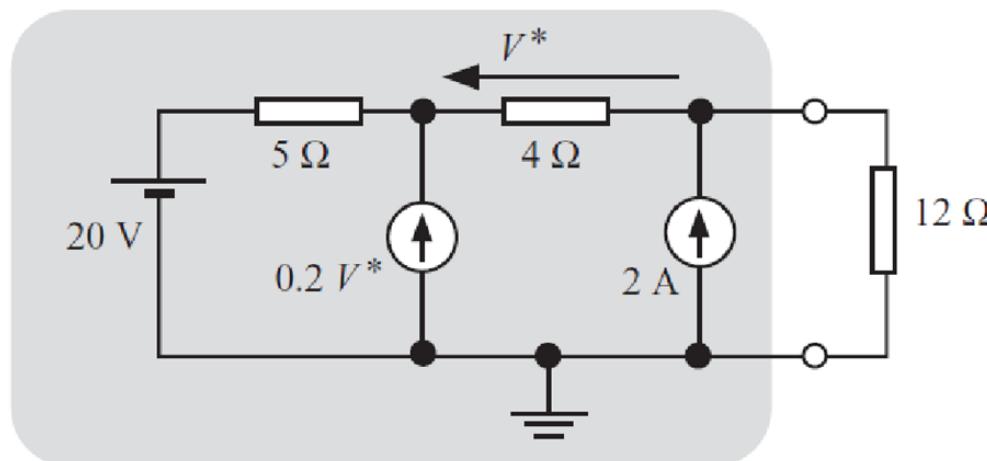
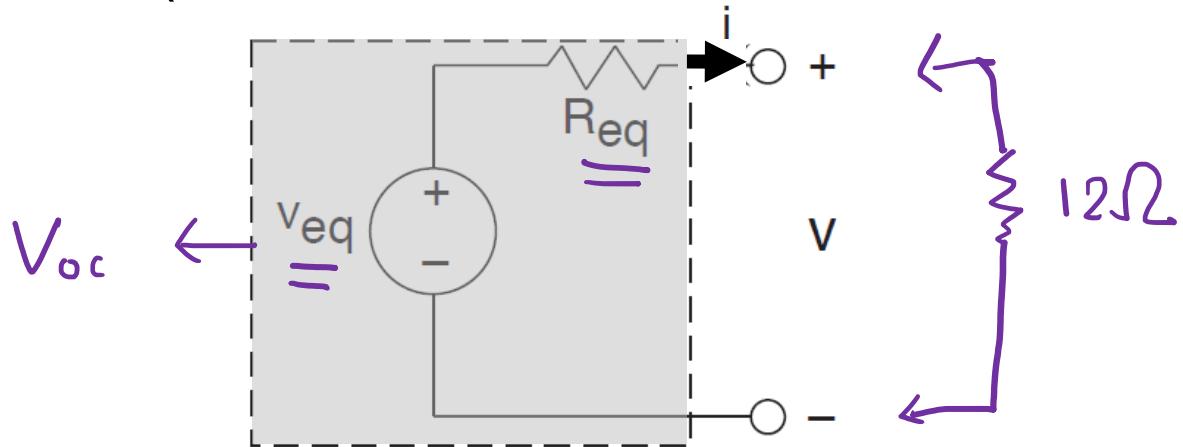


$$I = G V^*$$



**Figure 5.10** A VCCS whose controlling voltage appears directly across the controlled current source is equivalent to a resistor of appropriate value

## Example 5.3 (Thevenin model)



**Figure 5.7** The circuit of Figure 5.5 rearranged to show that part for which a Thevenin model is to be found

## Example 5.3 (2)

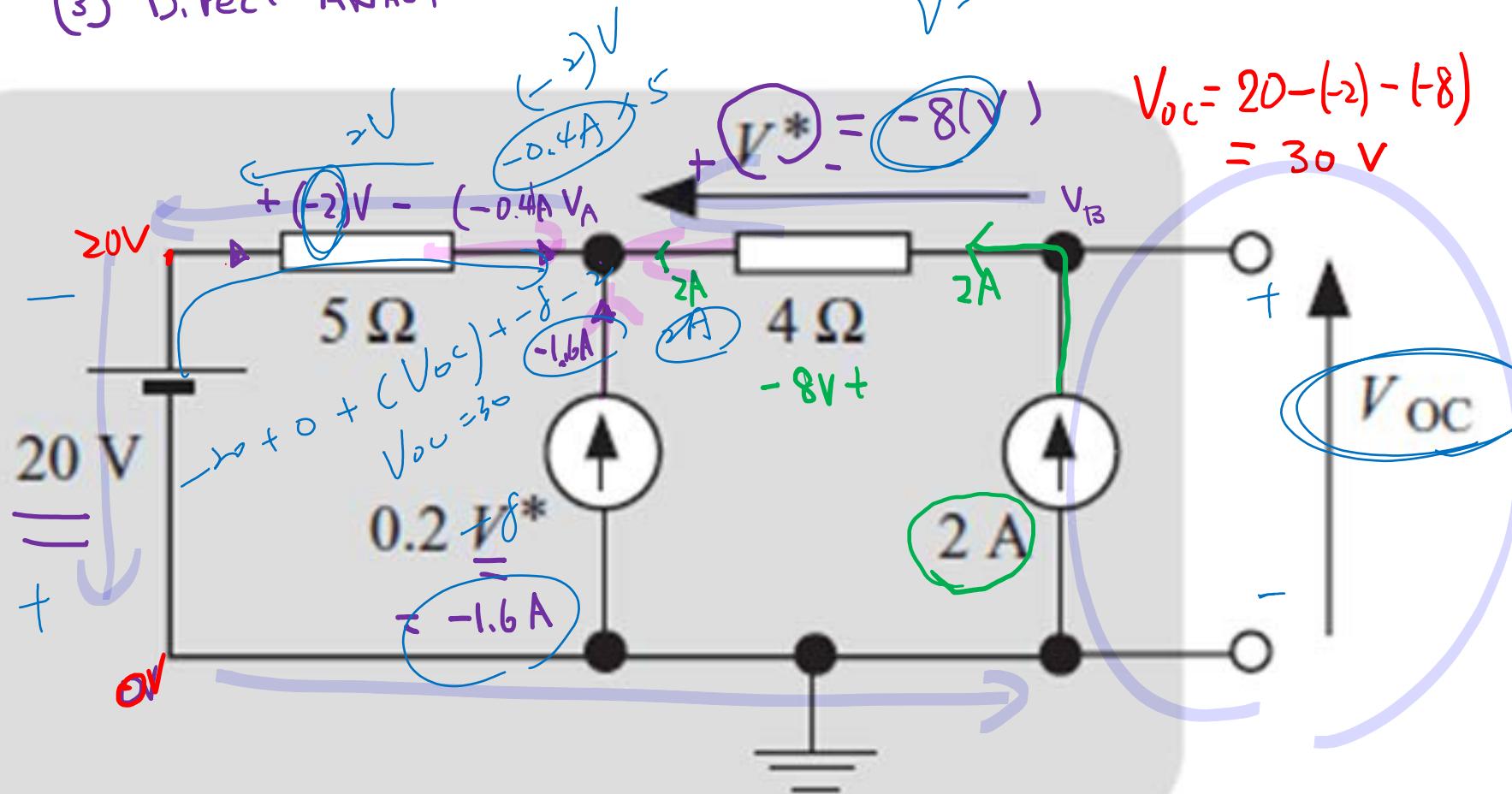
① NODAL ANALYSIS  $\Rightarrow$  2 eqns  $\Rightarrow V_B = V_{OC} = 30 V$   
 $(\text{Textbook})$

② Superposition

③ Direct ANALYSIS

$$V^* = -2 \times 4 = -8$$

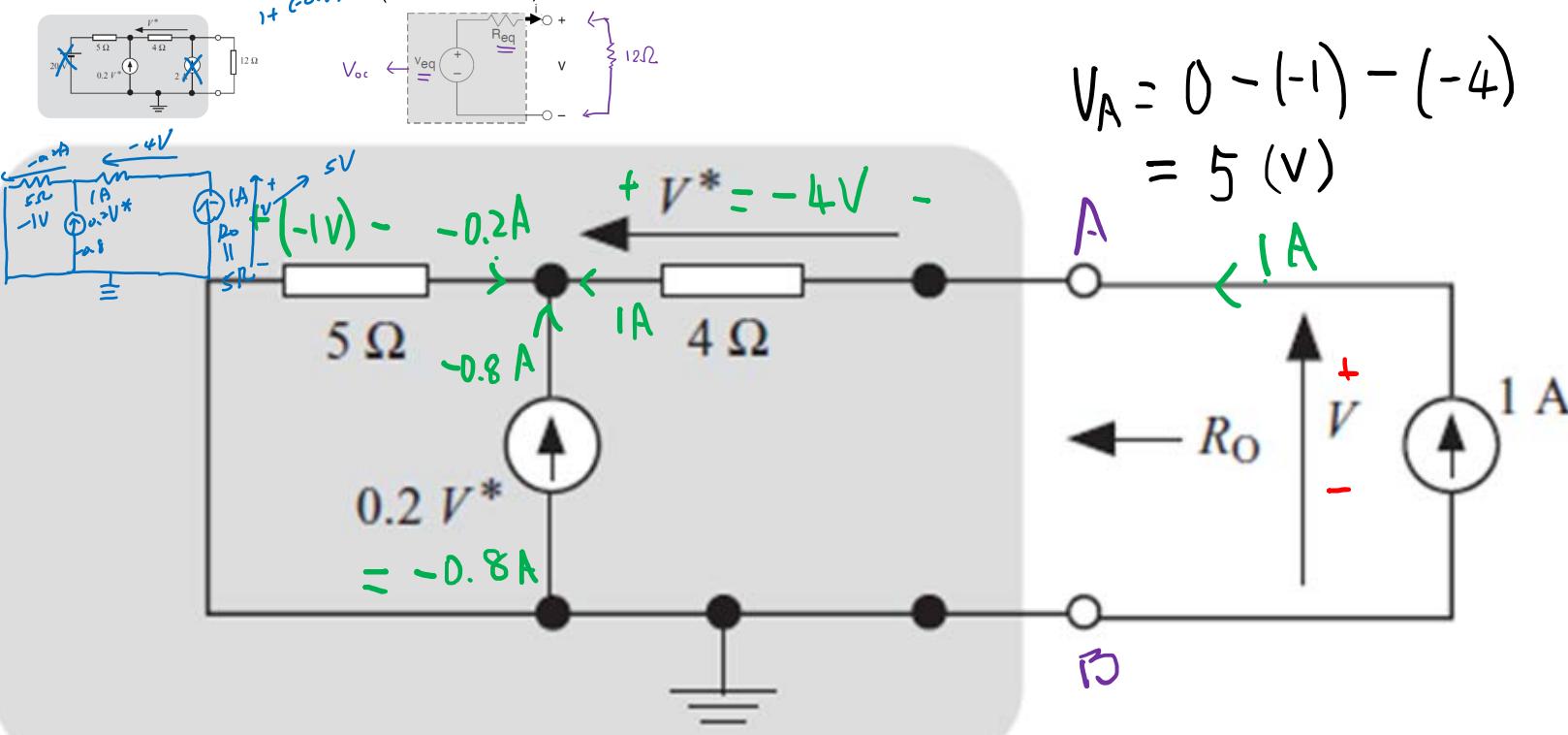
$$V_{OC} = 20 - (-2) - (-8) \\ = 30 V$$



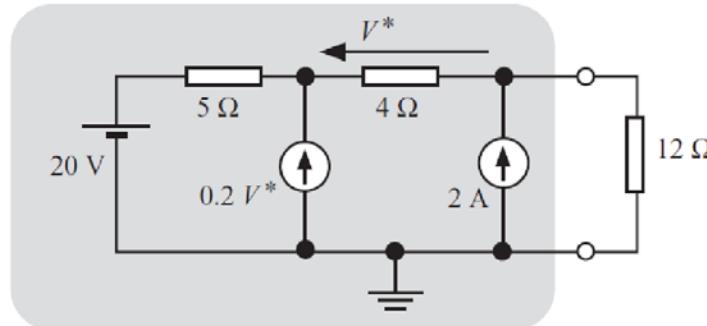
## Example 5.3 (3)

To find  $R_o$ , Apply 1A current source  
measure  $V_{AB} = 5V$   
 $\Rightarrow R_o = V_{AB}/1A = 5 \Omega$

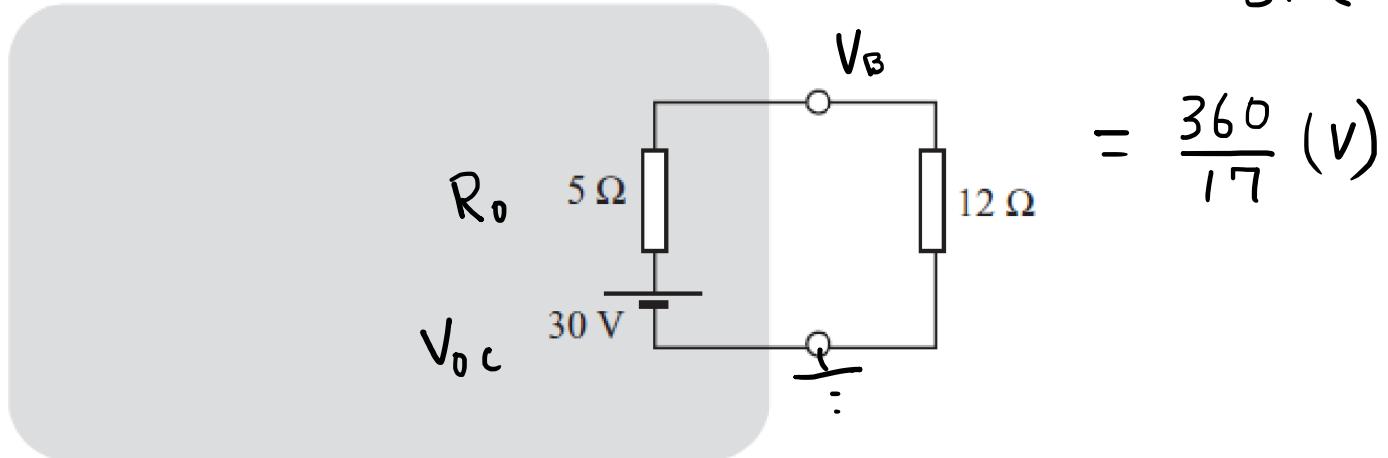
Set [ Independent Source  $\rightarrow 0$   
Dependent Source (Left alone! )



## Example 5.3 (4)



$$V_B = 30 \times \frac{12}{5+12}$$

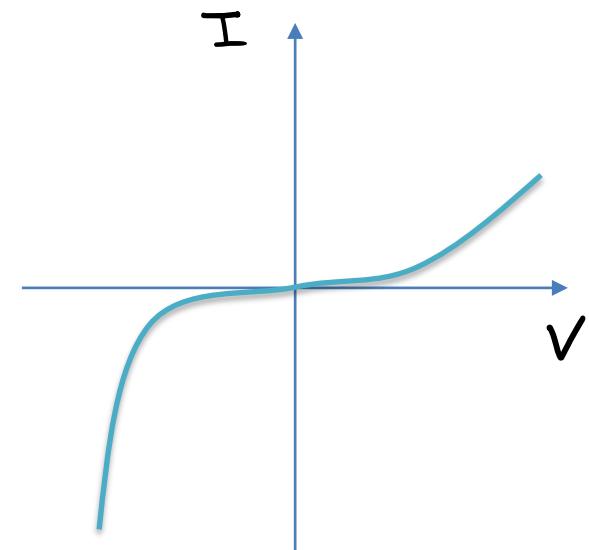


**Figure 5.9** Use of the Thevenin model of the circuit of Figure 5.7 to calculate the voltage across the  $12\ \Omega$  resistor. If the  $12\ \Omega$  resistor is replaced by another resistor having a different value only a very simple calculation is involved, invoking Ohm's law

# Nonlinear components

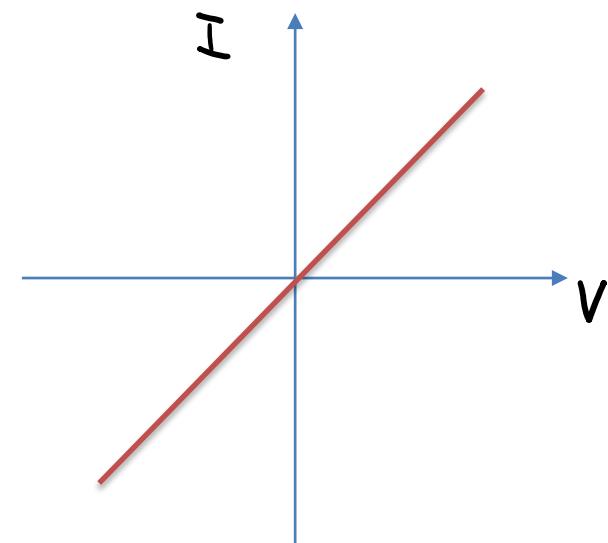
{ Transistor  
Diode }

$v_s$   $\updownarrow$

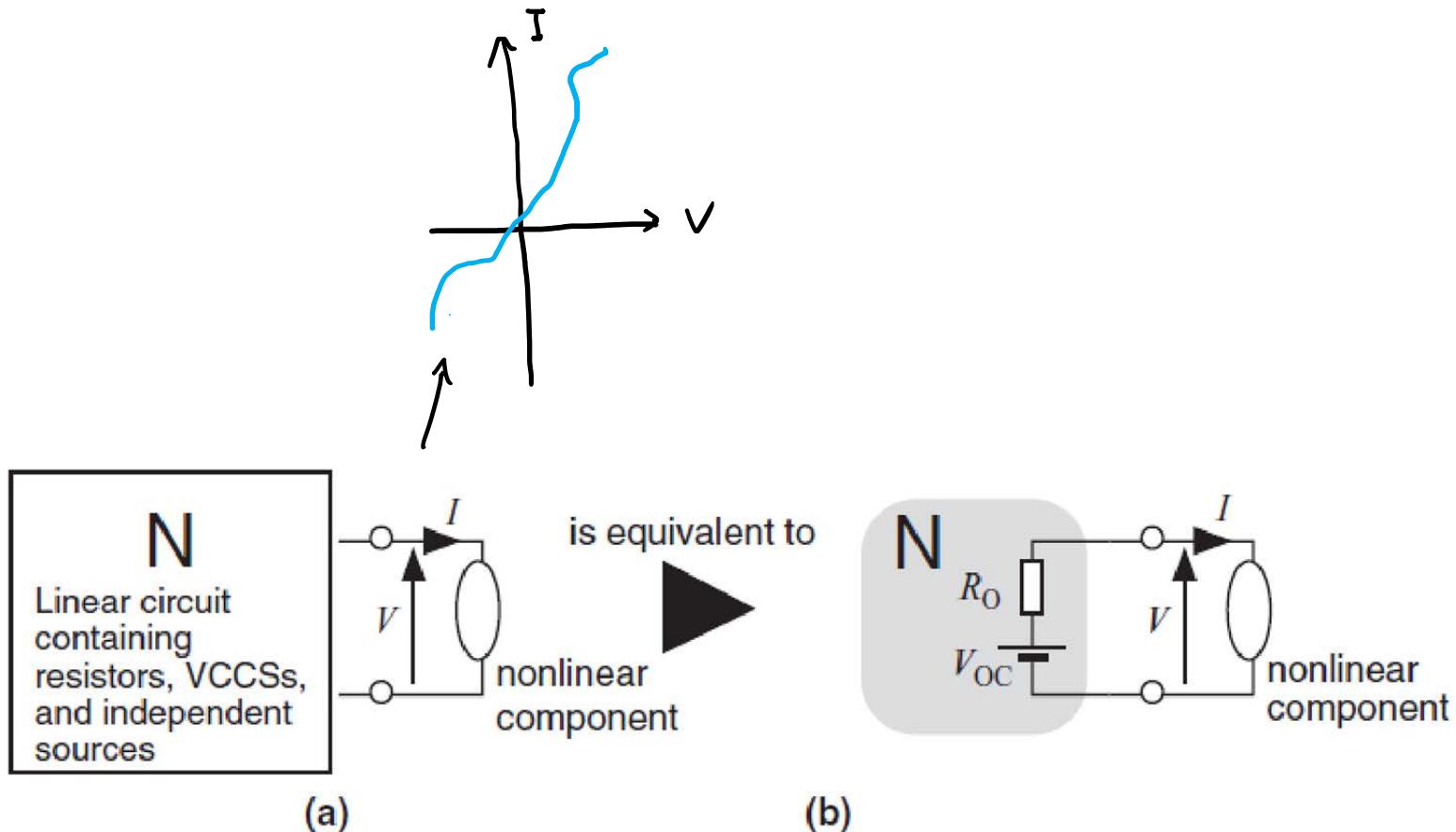


# Linear components

$R, L, C,$  Sources

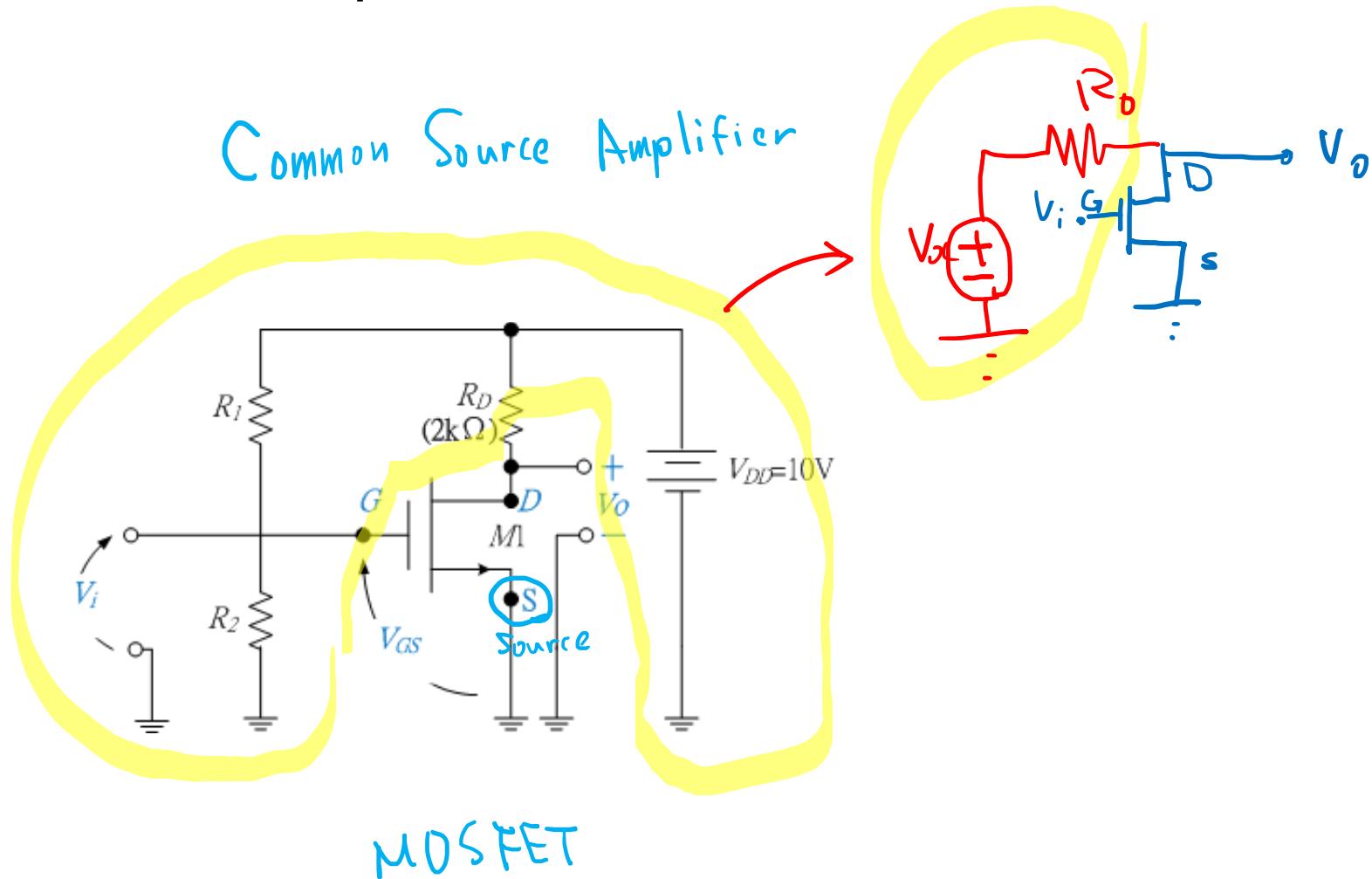


# Nonlinear components in circuits

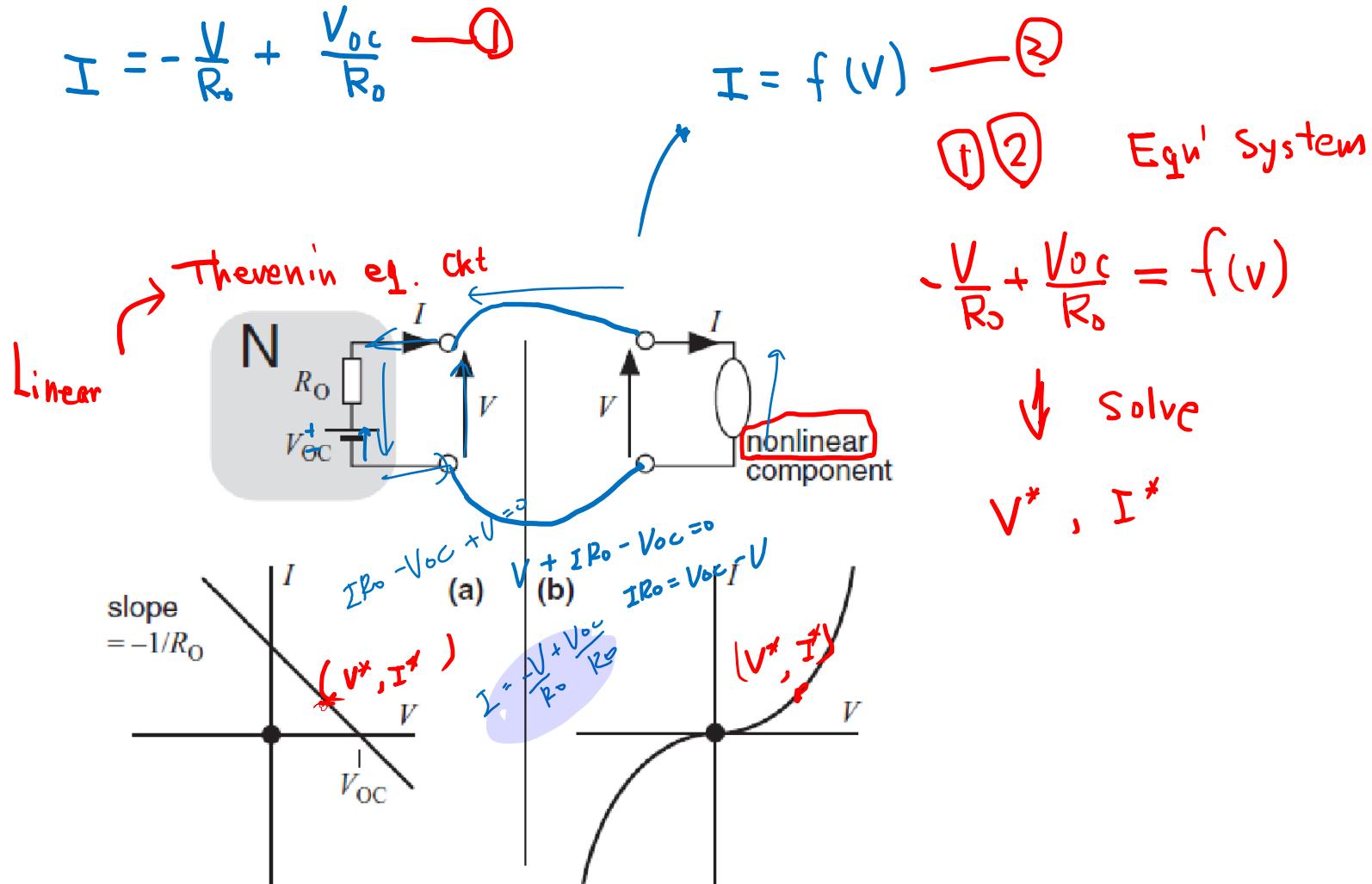


**Figure 5.11** (a) A circuit containing one nonlinear component whose voltage and current are of interest; (b) a representation of the linear part of the circuit by a Thevenin model

# Example: transistor



# Load-line construction



**Figure 5.12** Separation of the linear and nonlinear parts of the circuit in Figure 5.11, and their characterization by current~voltage plots

# Load-line construction

Solve  $-\frac{V}{R_o} + \frac{V_{oc}}{R_o} = f(v)$   
 $\Rightarrow V^*, I^*$

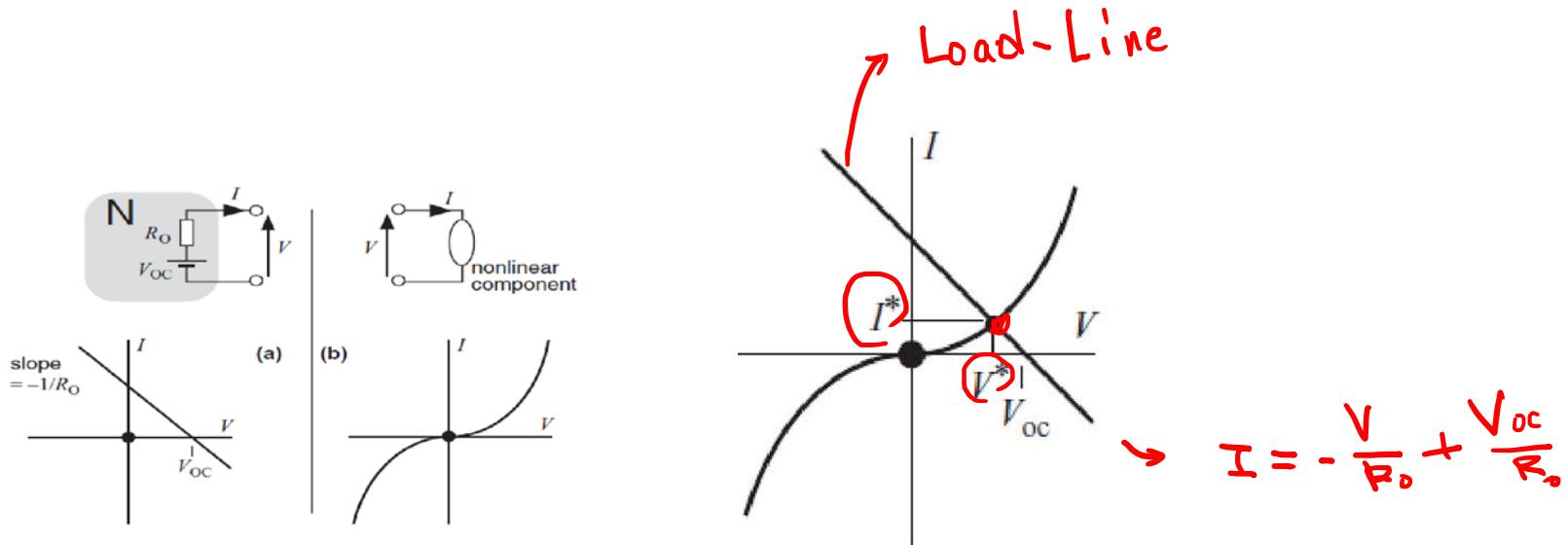


Figure 5.12 Separation of the linear and nonlinear parts of the circuit in Figure 5.11, and their characterization by current~voltage plots

Figure 5.13 The point  $I, V$  must lie on both characteristics, and hence at their intersection  $(I^*, V^*)$

# Problem 5.9

Maximum Power rating = 300 mW

$$V = IR$$

Find ① minimum  $R$  s.t.  $VI \leq 300 \text{ mW}$   
 ② Power dissip. in  $R = ?$

$$\begin{array}{ll} R \downarrow |I| \uparrow & P = |IV| \uparrow \\ R \uparrow |I| \downarrow & P = |IV| \downarrow \end{array}$$

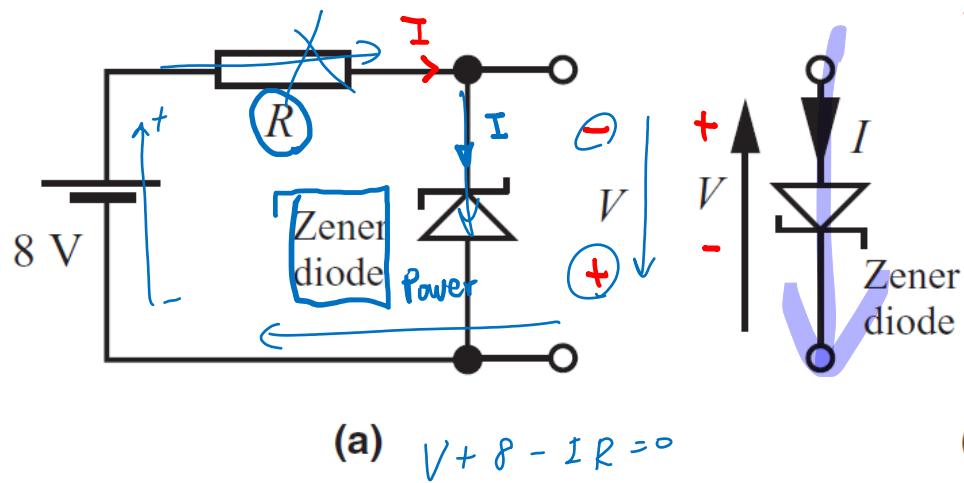


Figure P5.9

# Problem 5.9

$$P = VI \leq 300 \text{ mW}$$

$$V^* \approx -5.5 \text{ V}$$

$$I^* \approx -50 \text{ mA}$$

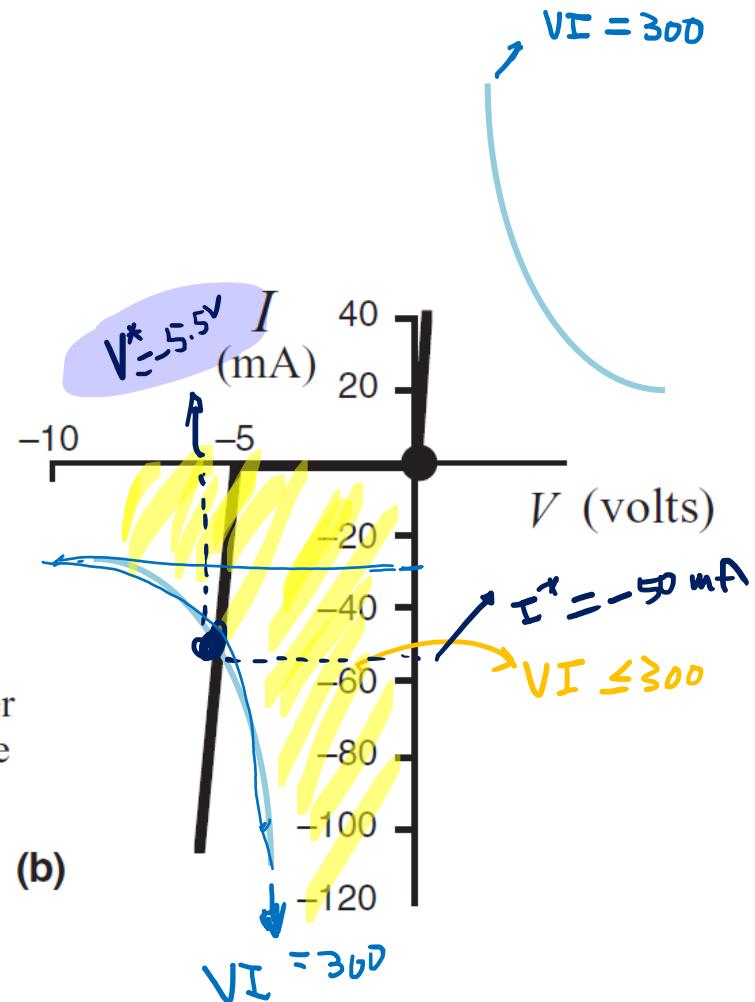
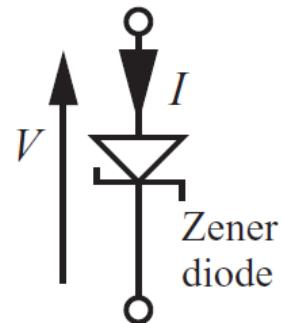
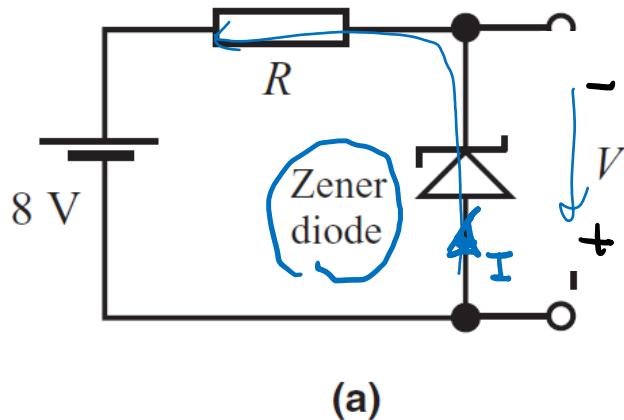


Figure P5.9

## Problem 5.9

$$V^* = -5.5 \text{ V}$$

$$I^* = -500 \text{ mA} \Rightarrow R = ?$$

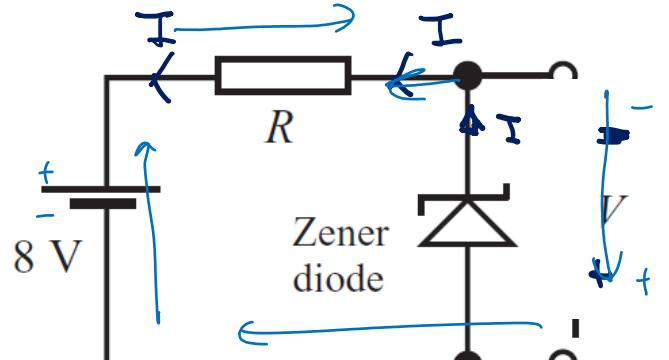
KVL :  $8 + IR + V = 0$

$$I = -\frac{V}{R} - \frac{8}{R}$$

$$I^*, V^* \Rightarrow R_{\min} = 50 \Omega$$

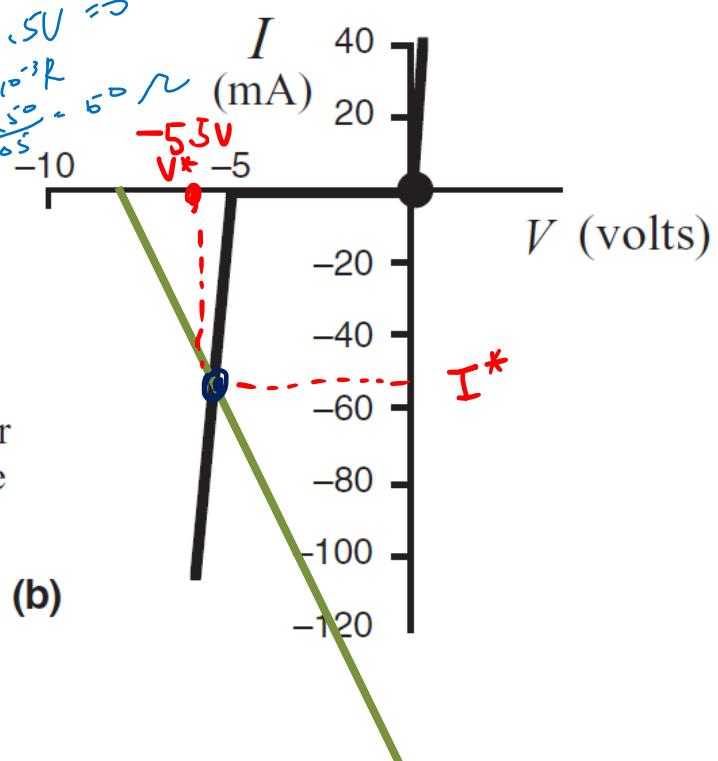
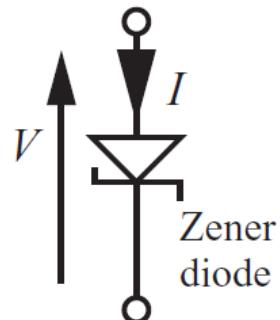
$$8 + (-50)12 = -5.5 \text{ V} \Rightarrow$$

$$2.5 = \frac{50 \times 10^{-3} R}{50} \Rightarrow R = 50 \Omega$$



$$V + I_R + I_Z = 0$$

Figure P5.9



## Problem 5.9

Power dissipated on  $R = 50 \Omega$

$$I^* \hat{=} -50 \text{ mA}$$

$$P_R = I^*^2 R$$

$$= (-50)^2 \cdot 50$$

$$= \underline{\underline{125 \text{ mW}}}$$

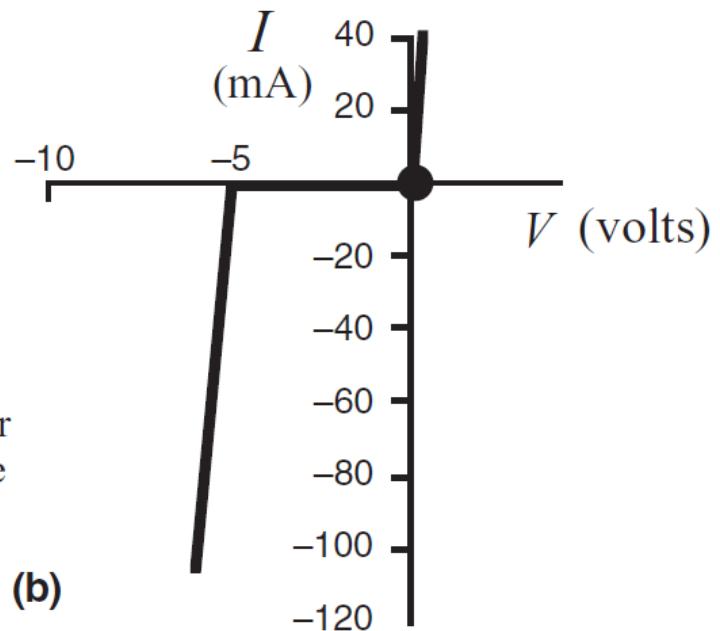
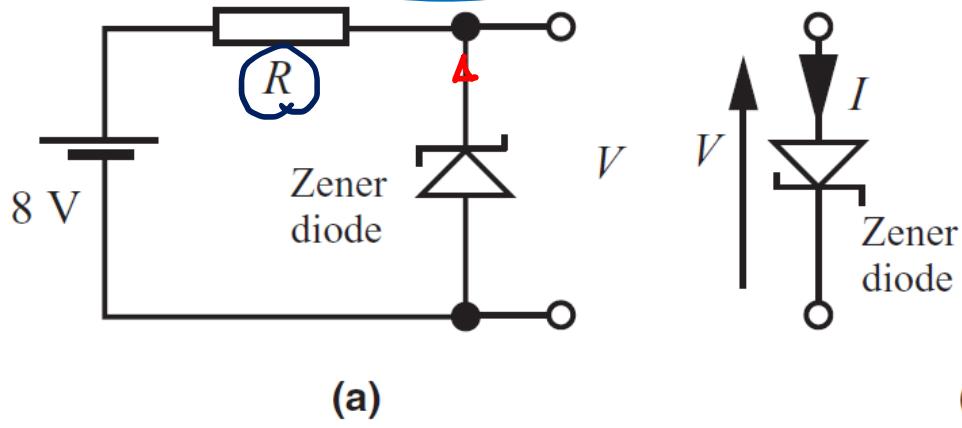


Figure P5.9

# Example: transistor

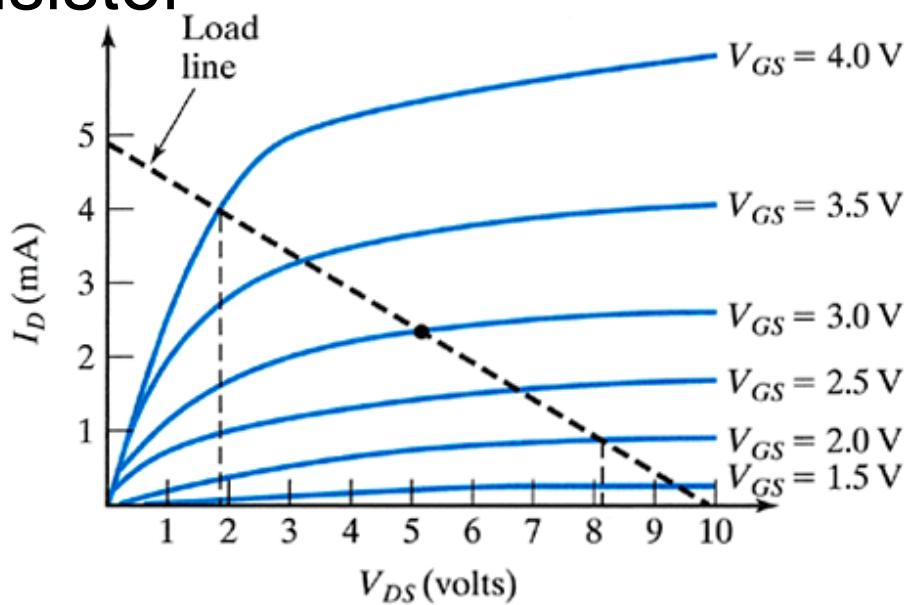
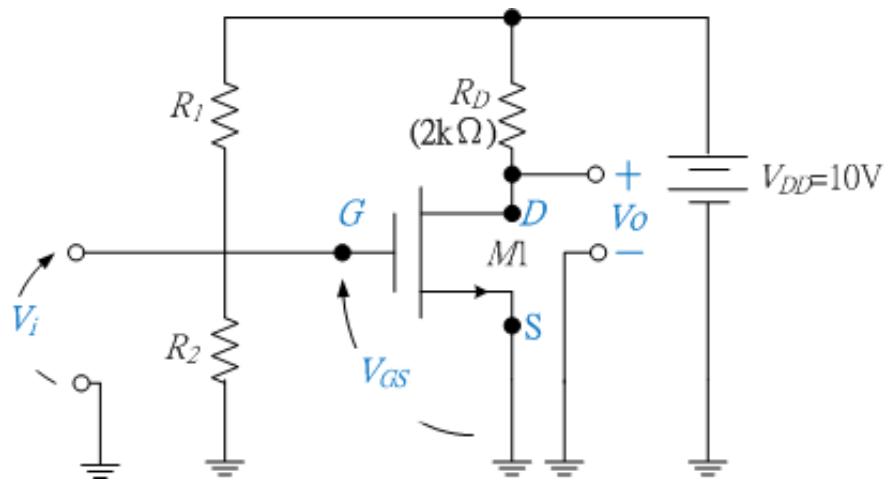


FIGURE 11.22 Enhancement-mode MOSFET transistor output characteristics.



## Quiz 4

The circuit of Fig. 1 contains a Zener diode (as in Fig. 2). A good approximation to its  $I$ - $V$  relation is shown in Fig. 3. The maximum power that can be absorbed by the diode and dissipated as heat is 250 mW.

1. Calculate the minimum value of  $R$  that will keep the Zener diode in operation.
2. Calculate the power dissipated by the resistor  $R$ .

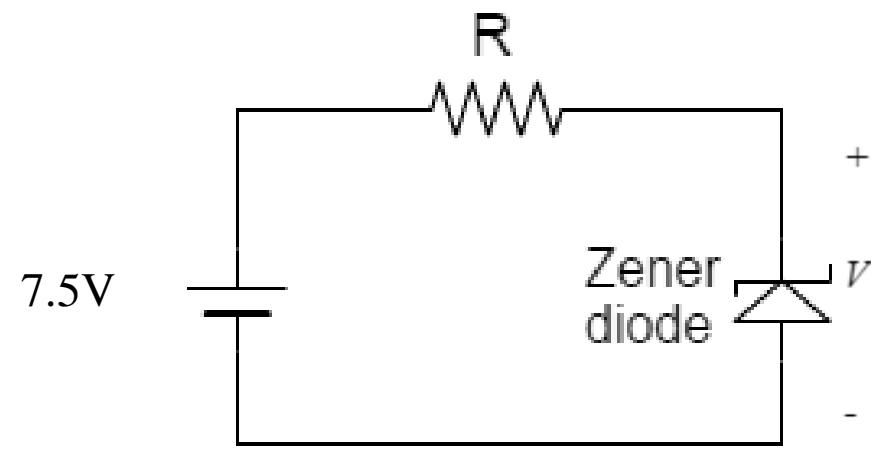


Fig. 1

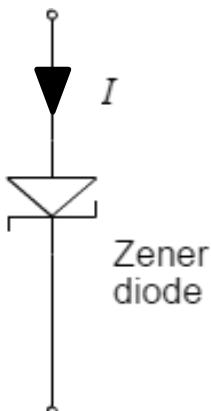


Fig. 2

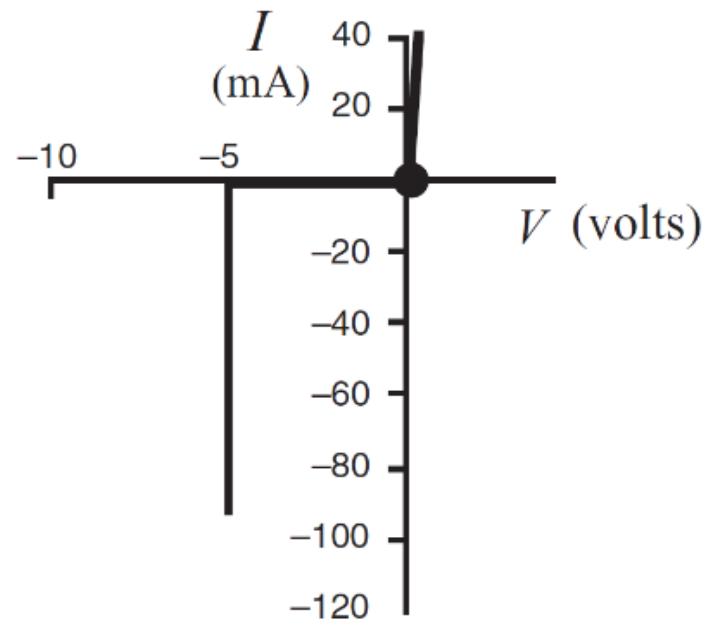


Fig. 3

# Quiz Review

$$P_{\max} = 250 \text{ mW}$$

$$VI \leq 250$$

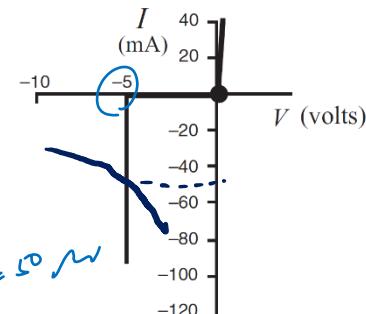
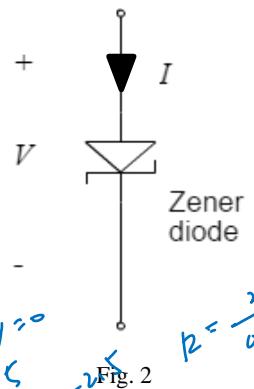
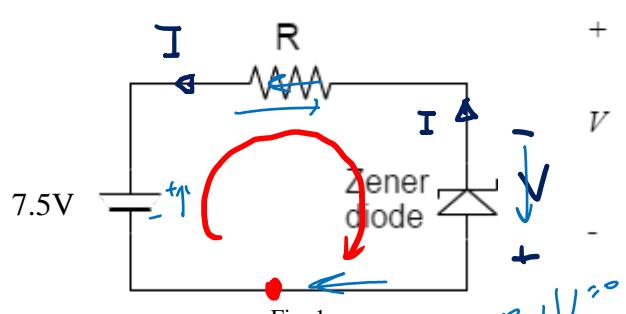
$$V^* = -5V$$

$$I^* = -50 \text{ mA}$$

KVL

$$7.5 + I^* R + V^* = 0$$

$$\Rightarrow R = 50 \Omega$$



# Quiz Review

Power on  $R$

$$P_R = I^2 R$$

$$= (-50 \text{ mA})^2 \times 50$$

$$= 125 \text{ mW}$$

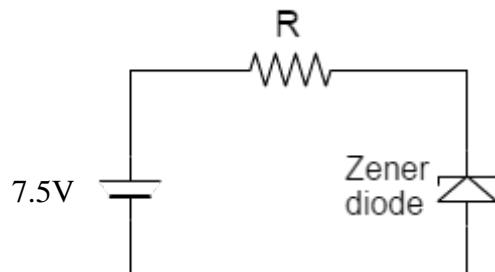


Fig. 1

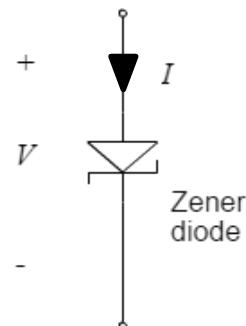


Fig. 2

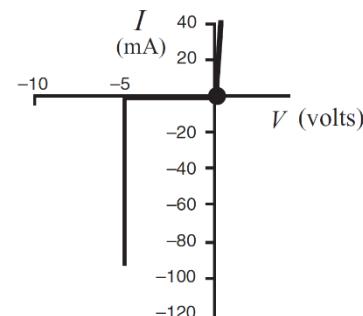


Fig. 3

# Example: transistor

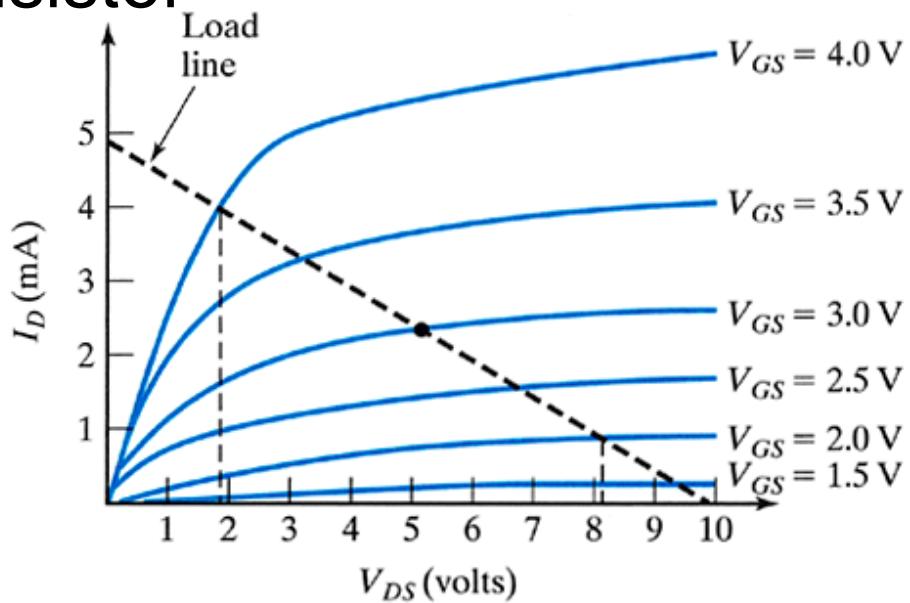


FIGURE 11.22 Enhancement-mode MOSFET transistor output characteristics.

