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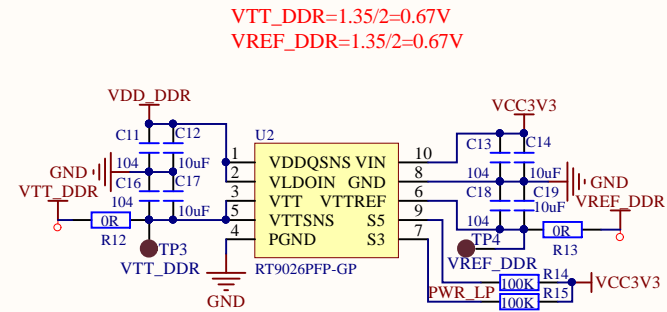
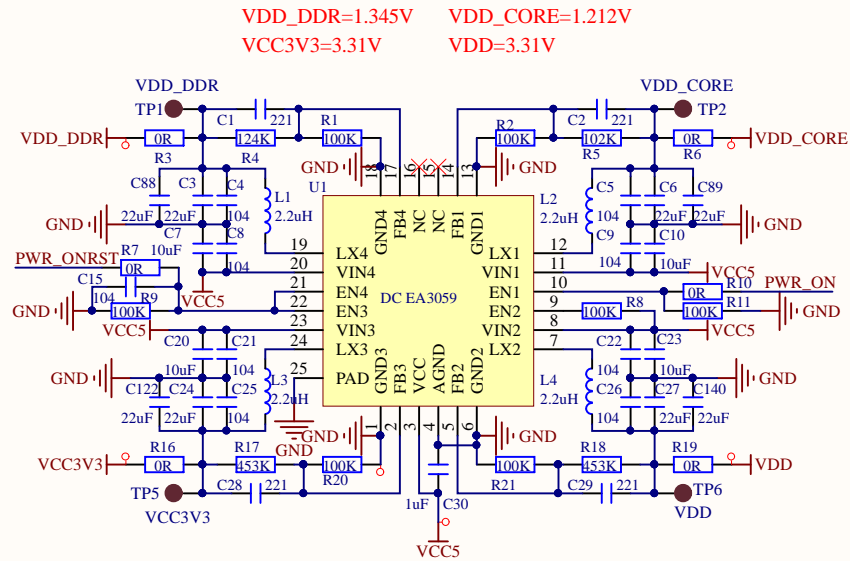
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Project: STM32MP157_Core_V1.2.PrjPcb			
Size: A4	Author: lycreturn@ALIENTEK		
Date: 2020-11-05	Version: V1.0	Sheet: 1 of 10	



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U3B

NAND_D2	PD0	C10	PD0/I2C6_I2C5_SDA/DFSDM1_CKIN6/SAI3_SCK_A/UART4_RX/FDCAN1_RX/SDMMC3_CMD/DFSDM1_DATIN7/FMC_D2/FMC_DA2
NAND_D3	PD1	B10	PD1/I2C6_I2C5_SCL/DFSDM1_DATIN6/SAI3_SD_A/UART4_TX/FDCAN1_TX/SDMMC3_D0/DFSDM1_CKIN7/FMC_D3/FMC_DA3
SDMMC1_CMD	PD2	D12	PD2/TIM3_ETR/I2C5_SMBA/UART4_RX/UART5_RX/SDMMC1_CMD/DCMI_D11
SDMMC2_D7	PD3	B11	PD3/HDP5/DFSDM1_CKOUT/SP12_SCK/I2S2_CK/DFSDM1_DATIN0/UART2_CTS_NSS/SDMMC1_SDMMC2_D123DIR/SDMMC1_SDMMC2_D7/FMC_CLK/DCMI_D5/LCD_G7
NAND_NOE	PD4	C9	PD4/SAI3_FS_A/USART2_RTS_DE/SDMMC3_D1/DFSDM1_CKIN0/FMC_NOE
NAND_NWE	PD5	A9	PD5/USART2_TX/SDMMC3_D2/FMC_NWE
NAND_NWAIT	PD6	L3	PD6/TIM16_CH1N/SAI1_D1/DFSDM1_CKIN4_DATIN1/SP13_MOSI/I2S3_SDO/SAI1_SD_A/USART2_RX/FMC_NWAIT/DCMI_D10/LCD_B2
SDMMC3_D3	PD7	F10	PD7/DFSDM1_DATIN4_CKIN1/I2C2_SCL/USART2_CK/SPDIFRX_IN0/SDMMC3_D3/FMC_NE1
USART3_TX	PD8	M1	PD8/DFSDM1_CKIN3/SAI3_SCK_B/USART3_TX/SPDIFRX_IN1/FMC_D13/FMC_DA13/LCD_B7
USART3_RX	PD9	M2	PD9/DFSDM1_DATIN3/SAI3_SD_B/USART3_RX/FMC_D14/FMC_DA14/DCMI_HSYNC/LCD_B0
uSD_DETECT	PD10	A8	PD10/RTC_REFIN/TIM16_BKIN/DFSDM1_CKOUT/I2C5_SMBA/SP13_MISO/I2S3_SDI/SAI3_FS_B/USART3_CK/FMC_D15/FMC_DA15/LCD_B3
NAND_CLE	PD11	AB9	PD11/LPTIM2_IN2/I2C4_I2C1_SMBA/USART3_CTS_NSS/QUADSPI_BK1_IO0/SAI2_SD_A/FMC_CLE/FMC_A16
NAND_ALE	PD12	W12	PD12/LPTIM1_LPTIM2_IN1/TIM4_CH1/I2C4_I2C1_SCL/USART3_RTS_DE/QUADSPI_BK1_IO1/SAI2_FS_A/FMC_ALE/FMC_A17
LCD_BL	PD13	V14	PD13/LPTIM1_OUT/TIM4_CH2/I2C4_I2C1_SDA/I2S3_MCK/QUADSPI_BK1_IO3/SAI2_SCK_A/FMC_A18
NAND_D0	PD14	M3	PD14/TIM4_CH3/SAI3_MCLK_B/UART8_CTS/FMC_D0/FMC_DA0
NAND_D1	PD15	L1	PD15/TIM4_CH4/SAI3_MCLK_A/UART8_CTS/FMC_D1/FMC_DA1/LCD_R1
SAI2_MCLKA	PE0	C5	PE0/LPTIM1_LPTIM2_ETR/TIM4_ETR/SP13_SCK/I2S3_CK/SAI4_MCLK_B/UART8_RX/SAI2_MCLK_A/FMC_NBL0/DCMI_D2
DCMI_RESET	PE1	D7	PE1/LPTIM1_IN2/I2S2_MCK/SAI3_SD_B/UART8_TX/FMC_NBL1/DCMI_D3
ETH_TXD3	PE2	Y2	PE2/SAI1_CK1/I2C4_SCL/SP14_SCK/SAI1_MCLK_A/QUADSPI_BK1_IO2/ETH1_GMII_MII_RGMII_TXD3/FMC_A23
SDMMC2_CK	PE3	A10	PE3/TIM15_BKIN/SAI1_SD_B/SDMMC2_CK/FMC_A19
SDMMC1_CKIN	PE4	F15	PE4/SAI1_D2/DFSDM1_DATIN3/TIM15_CH1N/SP14_NSS/SAI1_FS_A/SDMMC1_SDMMC2_CKIN/SDMMC1_SDMMC2_D4/FMC_A20/DCMI_D4/LCD_B0
SDMMC2_D6	PE5	C12	PE5/SAI1_CK2/DFSDM1_CKIN3/TIM15_CH1/SP14_MISO/SAI1_SCK_A/SDMMC1_SDMMC2_D0DIR/SDMMC1_SDMMC2_D6/FMC_A21/DMIC_D6/LCD_G0
DCMI_D7	PE6	E9	PE6/TIM1_BKIN2/SAI1_D1/TIM15_CH2/SP14_MOSI/SAI1_SD_A/SDMMC2_D0/SDMMC1_D2/SAI2_MCLK_B/FMC_A22/DCMI_D7/LCD_G1
NAND_D4	PE7	W10	PE7/TIM1_TIM3_ETR/DFSDM1_DATIN2/UART7_RX/QUADSPI_BK2_IO0/FMC_D4/FMC_DA4
NAND_D5	PE8	Y12	PE8/TIM1_CH1N/DFSDM1_CKIN2/UART7_TX/QUADSPI_BK2_IO1/FMC_D5/FMC_DA5
NAND_D6	PE9	W11	PE9/TIM1_CH1/DFSDM1_CKOUT/UART7_RTS_DE/QUADSPI_BK2_IO2/FMC_D6/FMC_DA6
NAND_D7	PE10	W14	PE10/TIM1_CH2N/DFSDM1_DATIN4/UART7_CTS/QUADSPI_BK2_IO3/FMC_D7/FMC_DA7
DCMI_PWDN	PE11	D5	PE11/TIM1_CH2/DFSDM1_CKIN4/SP14_NSS/USART6_CK/SAI2_SD_B/FMC_D8/FMC_DA8/DCMI_D4/LCD_G3
SAI2_SCKB	PE12	E4	PE12/TIM1_CH3N/DFSDM1_DATIN5/SP14_SCK/SDMMC1_D0DIR/SAI2_SCK_B/FMC_D9/FMC_DA9/LCD_B4
SAI2_FSB	PE13	A4	PE13/HDP2/TIM1_CH3/DFSDM1_CKIN5/SP14_MISO/SAI2_FS_B/FMC_D10/FMC_DA10/DCMI_D6/LCD_DE
SAI2_MCLKB	PE14	B4	PE14/TIM1_CH4/SP14_MOSI/UART8_RTS_DE/SAI2_MCLK_B/SDMMC1_D123DIR/FMC_D11/FMC_DA11/LCD_G0/LCD_CLK
T_MISO	PE15	C4	PE15/HDP3/TIM1_BKIN/TIM15_BKIN/USART2_CTS_NSS/UART8_CTS/FMC_NCE2/FMC_D12/FMC_DA12/LCD_R7
SDMMC3_D0	PF0	E10	PF0/I2C2_SDA/SDMMC3_D0_CKIN/FMC_A0
SDMMC3_CMD	PF1	B9	PF1/I2C2_SCL/SDMMC3_CMD_CDIF/FMC_A1
SDMMC1_D0DIR	PF2	F13	PF2/I2C2_SMBA/SDMMC1_SDMMC2_SDMMC3_D0DIR/FMC_A2
SDMMC3_D1	PF3	V3	PF3/ETH1_GMII_MII_TX_ER/FMC_A3
SDMMC3_D2	PF4	F9	PF4/USART2_RX/SDMMC3_D1/SDMMC3_D123DIR/FMC_A4
QSPI_BK1_IO3	PF5	D9	PF5/USART2_TX/SDMMC3_D2/FMC_A5
QSPI_BK1_IO2	PF6	AA11	PF6/TIM16_CH1/SP15_NSS/SAI1_SD_B/UART7_RX/QUADSPI_BK1_IO3/SAI4_SCK_B
QSPI_BK1_IO2	PF7	AA10	PF7/TIM17_CH1/SP15_SCK/SAI1_MCLK_B/UART7_TX/QUADSPI_BK1_IO2
QSPI_BK1_IO0	PF8	AB10	PF8/TIM16_CH1N/SP15_MISO/SAI1_SCK_B/UART7_RTS_DE/TIM13_CH1/QUADSPI_BK1_IO0
QSPI_BK1_IO1	PF9	AB11	PF9/TIM17_CH1N/SP15_MOSI/SAI1_FS_B/UART7_CTS/TIM14_CH1/QUADSPI_BK1_IO1
QSPI_CLK	PF10	V12	PF10/TIM16_BKIN/SAI1_D3/SAI1_SAI4_D4/QUADSPI_CLK/SAI4_D3/DCMI_D11/LCD_DE
SAI2_SDB	PF11	W8	PF11/SP15_MOSI/SAI2_SD_B/DCMI_D12/LCD_G5/ADC1_INP2
	PF12	V8	PF12/ETH1_GMII_RXD4/FMC_A6/ADC1_INP6_INN2
	PF13	W7	PF13/DFSDM1_DATIN6_DATIN3/I2C1_I2C4_SMBA/ETH1_GMII_RXD5/FMC_A7/ADC2_INP2
	PF14	V7	PF14/DFSDM1_CKIN6/I2C1_I2C4_SCL/ETH1_GMII_RXD6/FMC_A8/ADC2_INP6_INN2
	PF15	W6	PF15/I2C1_I2C4_SDA/ETH1_GMII_RXD7/FMC_A9

STM32MP175DAA1

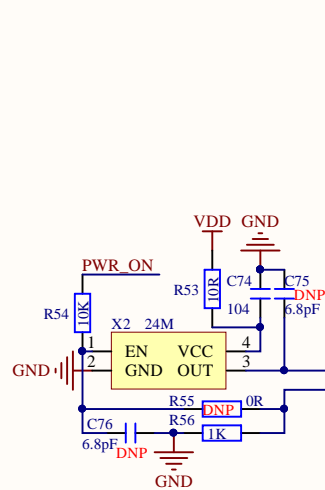
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Size: A4	Author: lycetum@ALIENTEK		
Date: 2020-11-05	Version: V1.0	Sheet: 3 of 10	

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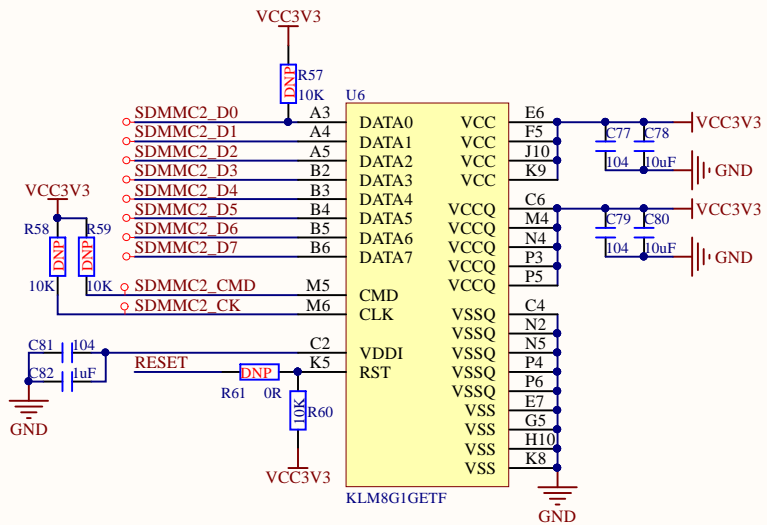
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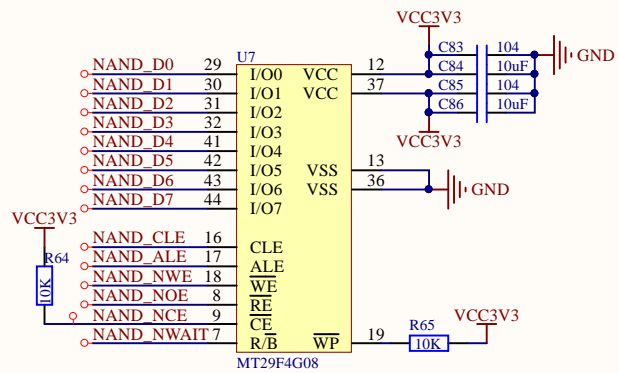
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ETH_MDINT	PG0 W5
MCO2	PG1 Y4
	PG2 W4
	PG3 U4
ETH_GTX_CLK	PG4 AB4
ETH_CLK125	PG5 U8
SDMMC2_CMD	PG6 D11
QSPL_BK2_IO3	PG7 Y11
ETH_CLK	PG8 Y8
NAND_NCE	PG9 W15
QSPL_BK2_IO2	PG10AA9
UART4_TX	PG11U11
SPDIF_RX	PG12 J4
ETH_TXD0	PG13AA1
ETH_TXD1	PG14AA2
SDMMC3_CK	PG15D10
T1	
T2	
QSPL_BK2_IO0	PH2 AB7
QSPL_BK2_IO1	PH3 Y6
I2C2_SCL	PH4 A3
I2C2_SDA	PH5 A2
	PH6 V11
	PH7 W2
DCMI_HSYNC	PH8 D6
DCMI_D0	PH9 E6
DCMI_D1	PH10 B1
DCMI_D2	PH11 B3
DCMI_D3	PH12 F5
CAN1_TX	PH13 D3
DCMI_D4	PH14 C2
CT_RST	PH15 C1
PI0 D1	
CT_INT	PI1 E2
	PI2 E1
	PI3 E3
DCMI_D5	PI4 J6
SAI2_SCKA	PI5 F2
SAI2_SDA	PI6 G5
SAI2_FSA	PI7 F1
	PI8 N1
CAN1_RX	PI9 J5
ETH_Nrst	PI10 W1
MCO1	PI11 T3
LCD_HSYNC	PI12 H2
LCD_VSYNC	PI13 H1
LCD_CLK	PI14 D2
LCD_R0	PI15 F3
STM32MP175DAA1	
PG0/DFSDM1_DATIN0/ETH1_GMII_TXD4/FMC_A10	
PG1/ETH1_GMII_TXD5/FMC_A11	
PG2/MCO2/TIM8_BKIN/ETH1_GMII_TXD6/FMC_A12	
PG3/TIM8_BKIN2/DFSDM1_CKIN1/ETH1_GMII_TXD7/FMC_A13	
PG4/TIM1_BKIN2/ETH1_GMII_RGMII_GTX_CLK/FMC_A14	
PG5/TIM1_ETR/ETH1_GMII_RGMII_CLK125/FMC_A15	
PG6/TIM17_BKIN/SDMMC2_CMD/DCMI_D12/LCD_R7	
PG7/SAI1_MCLK_A/USART6_CK/USART8_RTS_DE/QUADSPI_CLK/QUADSPI_BK2_IO3/DCMI_D13/LCD_CLK	
PG8/TIM2_CH1/TIM2_TIM8_ETR/ETH_CLK/SPI6_NSS/SAI4_D2/USART6_RTS_DE/USART3_NSS_DE/SPDIFRX_IN2/SAI4_FS_A/ETH1_PPS_OUT/LCD_G7	
PG9/DBTRGO/USART6_RX/SPDIFRX_IN3/QUADSPI_BK2_IO2/SAI2_FS_B/FMC_NCE/FMC_NE2/DCMI_VSYNC/LCD_R1	
PG10/UART8_CTS/LCD_G3/SAI2_SD_B/QUADSPI_BK2_IO2/FMC_NE3/DCMI_D2/LCD_B2	
PG11/USART1_UART4_TX/SPDIFRX_IN0/ETH1_GMII_MII_RMII_TX_EN/ETH1_RGMII_TX_CTL/DCMI_D3/LCD_B3	
PG12/LPTIM1_IN1/SPI6_MISO/SAI4_CK2/USART6_RTS_DE/SPDIFRX_IN1/LCD_B4/SAI4_SCK_A/ETH1_PHY_INTN/FMC_NE4/LCD_B1	
PG13/LPTIM1_OUT/SAI1_CK2/SAI4_CK1/SPI6_SCK/SAI1_SCK_A/USART6_CTS_NSS/SAI4_MCLK_A/ETH1_GMII_MII_RGMII_RMII_TXD0/FMC_A24/LCD_R0	
PG14/LPTIM1_ETR/SPI6_MOSI/SAI4_D1/USART6_TX/QUADSPI_BK2_IO3/SAI4_SD_A/ETH1_GMII_MII_RGMII_RMII_TXD1/FMC_A25/LCD_B0	
PG15/SAI1_D2/I2C2_SDA/SAI1_FS_A/USART6_CTS_NSS/SDMMC3_CK/DCMI_D13	
PH0-OSC_IN	
PH1-OSC_OUT	
PH2/LPTIM1_IN2/QUADSPI_BK2_IO0/ETH1_GMII_MII_CRS/LCD_R0	
PH3/DFSDM1_CKIN4/QUADSPI_BK2_IO1/SAI2_MCLK_B/ETH1_GMII_MII_COL/LCD_R1	
PH4/I2C2_SCL/LCD_G5/LCD_G4	
PH5/I2C2_SDA/SPI5_NSS/SAI4_SD_B	
PH6/TIM12_CH1/I2C2_SMB/SAI5_SCK/ETH1_MII_RGMII_RXD2/MDIOS_MDIO/DCMI_D8	
PH7/I2C3_SCL/SPI5_MISO/ETH1_GMII_MII_RGMII_RXD3/MDIOS_MDC/DCMI_D9	
PH8/TIM5_ETR/I2C3_SDA/DCMI_HSYNC/LCD_R2	
PH9/TIM12_CH2/I2C3_SMB/DCMI_D0/LCD_R3	
PH10/TIM5_CH1/I2C4_SMB/SAI2_C1_SMB/DCMI_D1/LCD_R4	
PH11/TIM5_CH2/I2C4_SCL/I2C1_SCL/DCMI_D2/LCD_R5	
PH12/HDP2/TIM5_CH3/I2C4_SDA/I2C1_SDA/DCMI_D3/LCD_R6	
PH13/TIM8_CH1N/UART4_TX/FDCAN1_TX/LCD_G2	
PH14/TIM8_CH2N/UART4_RX/FDCAN1_RX/DCMI_D4/LCD_G3	
PH15/TIM8_CH3N/DCMI_D11/LCD_G4	
PI0/TIM5_CH4/SPI2_NSS_WS/DCMI_D13/LCD_G5	
PI1/TIM8_BKIN2/SPI2_SCK_CK/DCMI_D8/LCD_G6	
PI2/TIM8_CH4/SPI2_MISO/I2S2_SD/DCMI_D9/LCD_G7	
PI3/TIM8_ETR/SPI2_MOSI/I2S2_SDO/DCMI_D10	
PI4/TIM8_BKIN/SAI2_MCLK_A/DCMI_D5/LCD_B4	
PI5/TIM8_CH1/SAI2_SCK_A/DCMI_VSYNC/LCD_B5	
PI6/TIM8_CH2/SAI2_SD_A/DCMI_D6/LCD_B6	
PI7/TIM8_CH3/SAI2_FS_A/DCMI_D7/LCD_B7	
PI8/RTC_OUT2_LSCO/TAMP_IN2/TAMP_OUT3/WKUP4	
PI9/HDP1/UART4_RX/FDCAN1_RX/LCD_VSYNC	
PI10/HDP0/USART3_CTS_NSS/ETH1_GMII_MII_RX_ER/LCD_HSYNC	
PI11/MCO1/I2S_CKIN/LCD_G6/WKUP5	
PI12/HDP0/LCD_HSYNC	
PI13/HDP1/LCD_VSYNC	
PI14/LCD_CLK	
PI15/LCD_G2/LCD_R0	

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Project: STM32MP157_Core_V1.2.PrjPcb			
Size: A4	Author: lycereturn@ALIENTEK		
Date: 2020-11-05	Version: V1.0	Sheet: 4 of 10	

## EMMC



## NAND



## STM32MP157\_JKZ

U3D		
LCD_R1	PJ0	J2
LCD_R2	PJ1	L6
LCD_R3	PJ2	K4
LCD_R4	PJ3	J1
LCD_R5	PJ4	K2
LCD_R6	PJ5	K1
LCD_R7	PJ6	L5
LCD_G0	PJ7	L4
LCD_G1	PJ8	H6
LCD_G2	PJ9	L2
LCD_G3	PJ10	J3
LCD_G4	PJ11	K6
LCD_B0	PJ12	B8
LCD_B1	PJ13	A7
LCD_B2	PJ14	B7
LCD_B3	PJ15	C7
LCD_G5	PK0	D8
LCD_G6	PK1	E7
LCD_G7	PK2	E8
LCD_B4	PK3	B6
LCD_B5	PK4	A6
LCD_B6	PK5	C6
LCD_B7	PK6	A5
LCD_DE	PK7	B5
SPI1_SCK	PZ0	G2
SPI1_MISO	PZ1	H5
SPI1_MOSI	PZ2	K5
SPI1_NSS	PZ3	F4
I2C4_SCL	PZ4	G1
I2C4_SDA	PZ5	H4
I2C6_SCL	PZ6	G3
I2C6_SDA	PZ7	H3

STM32MP175DAA1

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Date: 2020-11-05	Version: V1.0	Sheet: 5 of 10	

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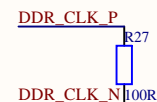
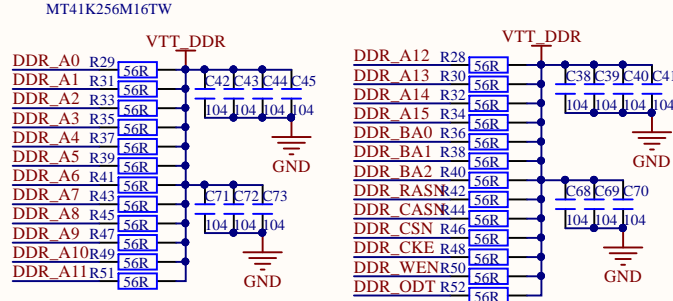
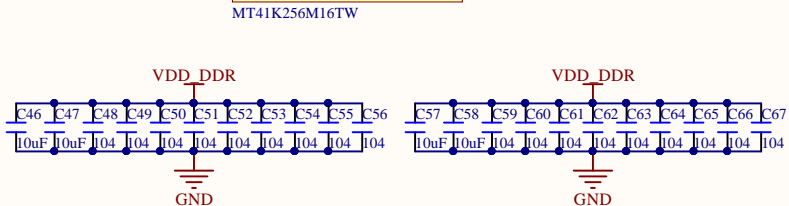
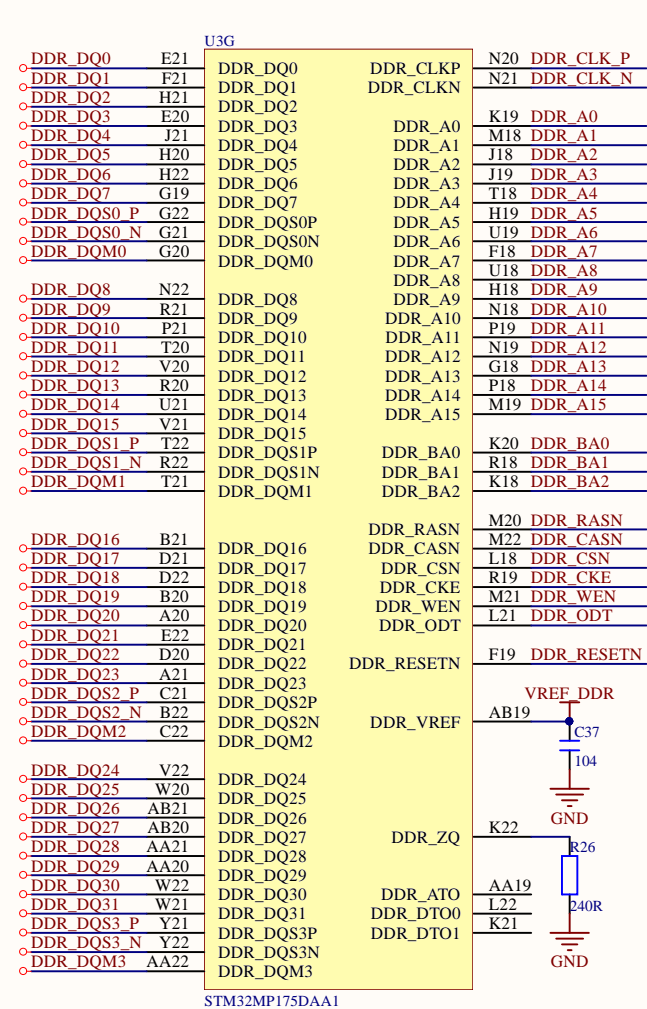
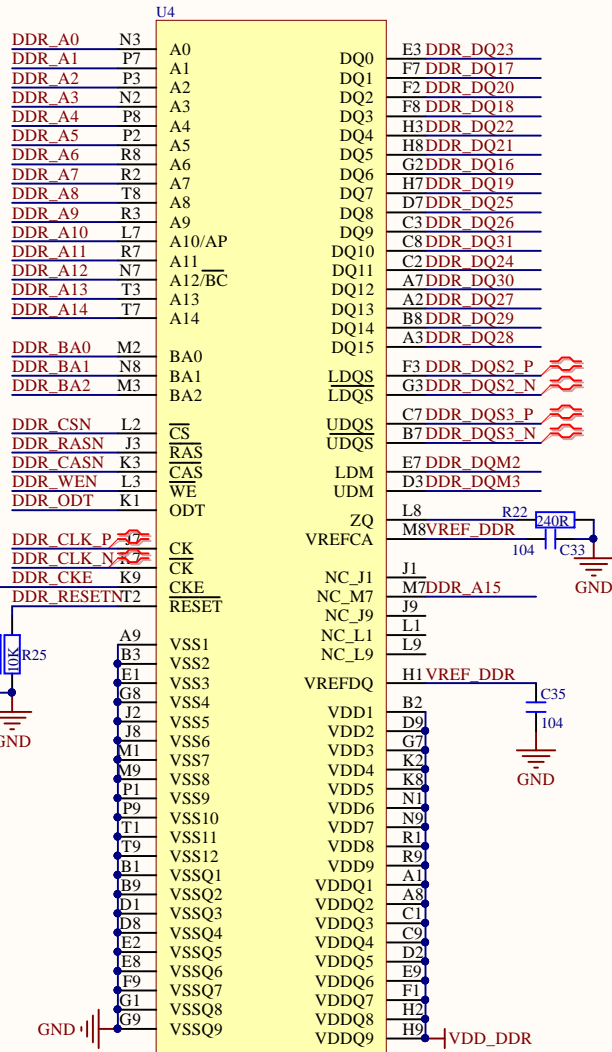
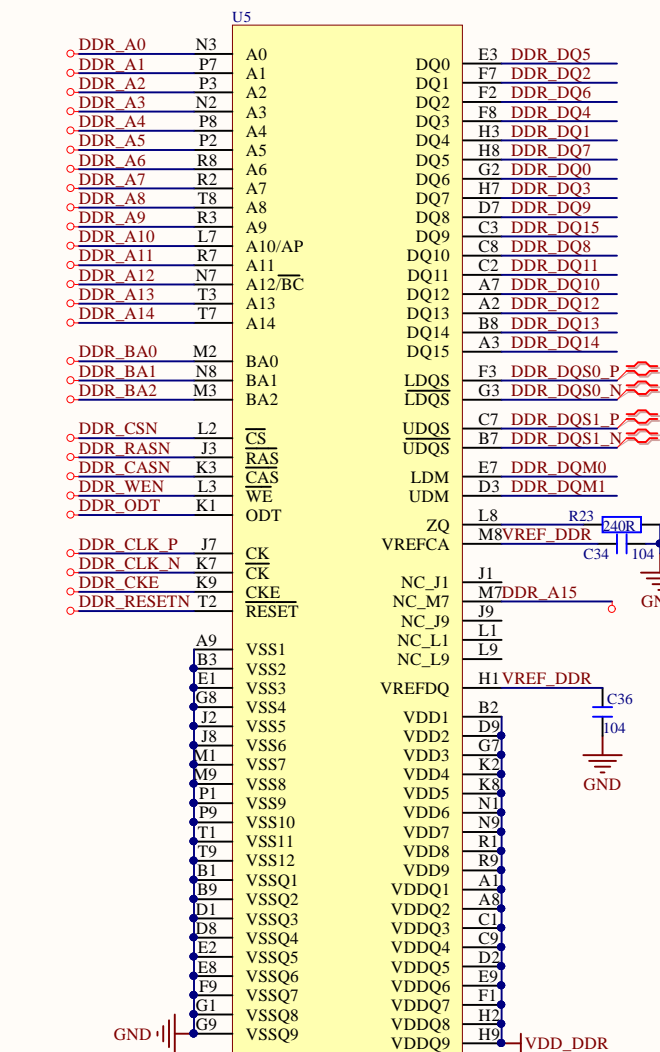
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Date: 2020-11-05	Version: V1.0	Sheet: 6 of 10	

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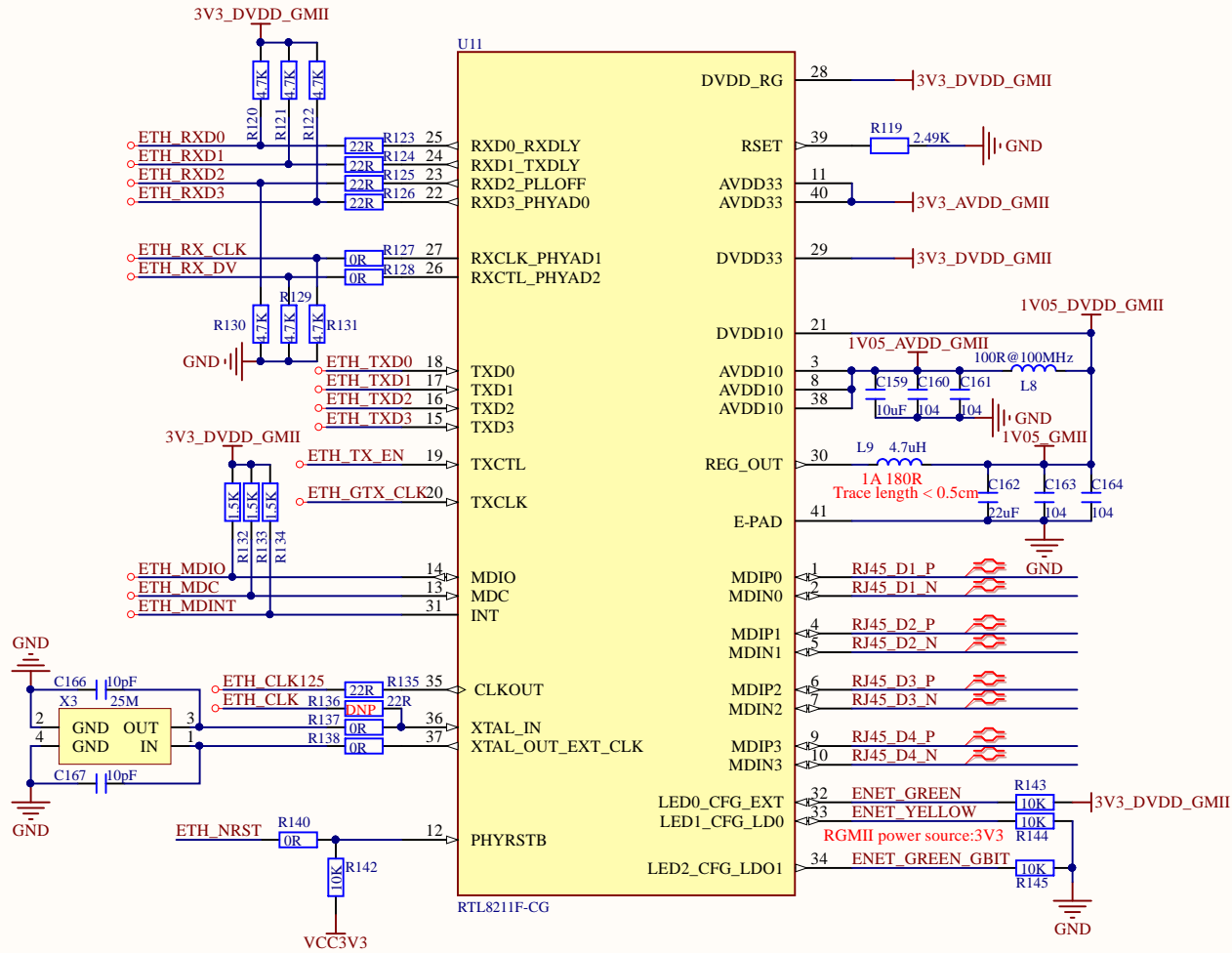
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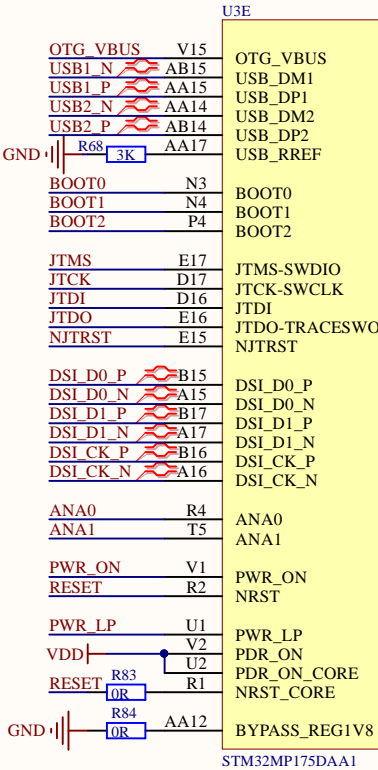
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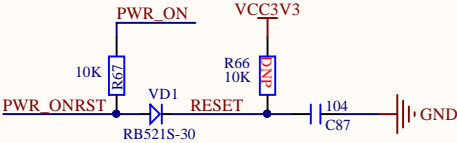


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Date: 2020-11-05	Version: V1.0	Sheet: 8 of 10	

USB&DSI&JTAG



RESET



LED



ESD&FKT

ESD FKT  
ESD FKT  
ESD FKT

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