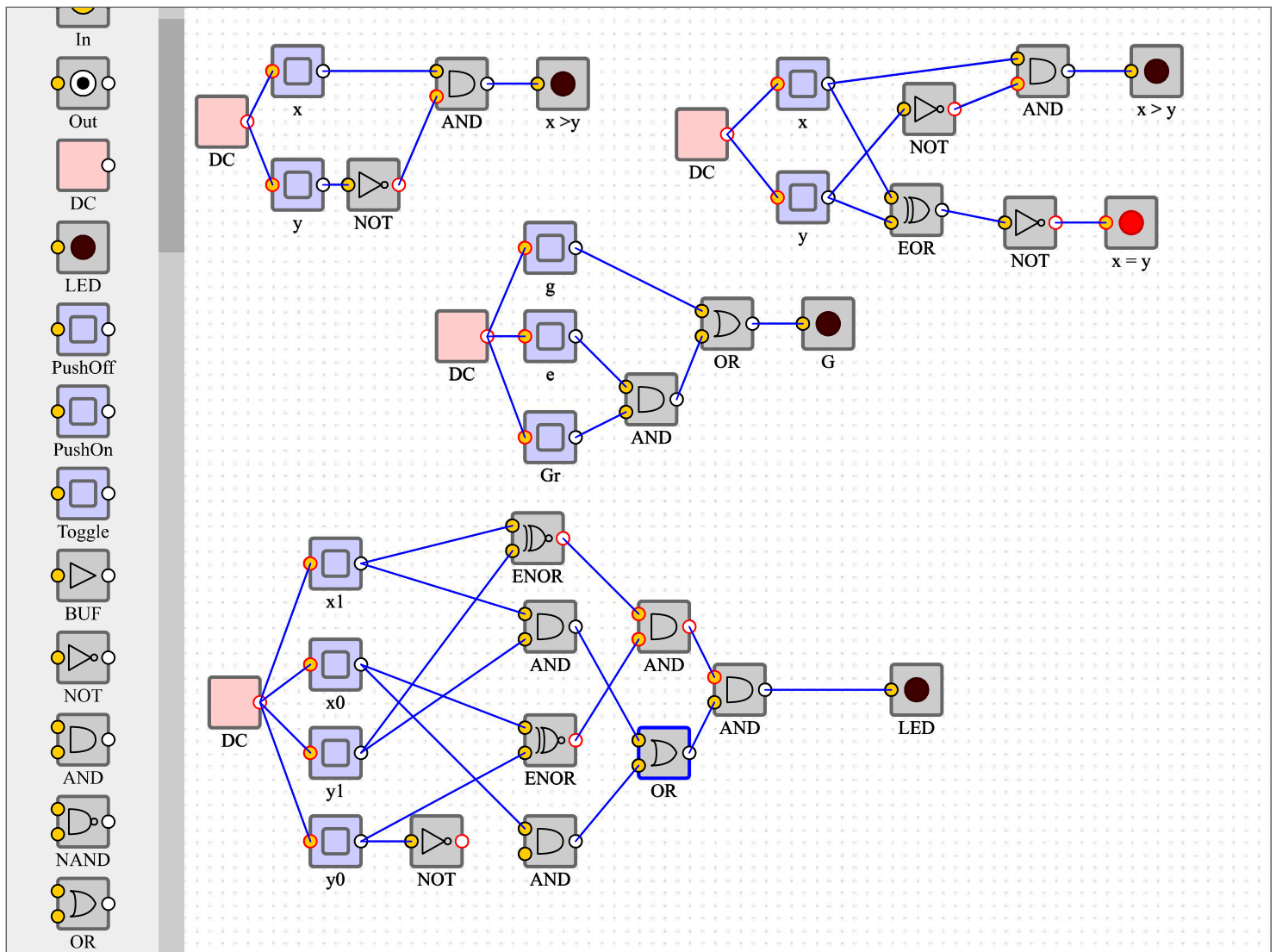


# Digital Logic Workbench



*Developed by Kazuhiko Arase*