

# Shanjit Singh Jajmann

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CONTACT INFORMATION	4403 Walnut Street, Apartment 2C Philadelphia, PA - 19104 United States	+1 720 557 9240 sjajmann@seas.upenn.edu
EDUCATION	<b>University of Pennsylvania</b> , Philadelphia, United States <b>Masters of Science, Embedded Systems Program</b> <b>Department of Computer and Information Science</b>	<b>Aug 2013 – present</b>
	<b>University of Delhi</b> , New Delhi, India <b>Bachelors of Engineering, Electronics and Communication</b>	78% - 3.95/4.0 <b>Aug 2009 – May 2013</b>
HONOURS AND AWARDS	Best B.E. Project Award, Dept. of Electronics and Communication, University of Delhi, 2013 Subroto Memorial Scholarship for Academic Achievement, Indian Air Force, New Delhi, 2009-13 Certificate of Excellence for Academic Achievement, University of Delhi, 2009-12 Ramanujan Award for Exemplary Performance in Mathematics Courses, University of Delhi, 2010 All India Rank 22 in the Mathematics Olympiad for Govt. Schools, Bangalore, 2008	
PUBLICATIONS, PROJECTS & TALKS	<b>Dhananjay Gadre, Rohit Dureja, Shanjit S. Jajmann</b> , “Getting Started with the Stellaris Guru Evaluation Kit, Stellaris ARM Cortex-M3 Lab Manual”, New Delhi: Universities Press (India), 2013.  <b>Hardware-Software Codesign : ARM and FPGAs</b> <b>Sep 2012 – June 2013</b> Developed custom FPGA and ARM boards capable of RSA encryption and decryption, Awarded the Best B.E. Project Award.  <b>Multilingual Health Log Manager</b> <b>July 2011 – Feb 2012</b> Developed a standalone health log manager with Android and Internet functionality to measure, record and save physical parameters online.  <b>Projects on ST and NXP ARM Cortex-M3 based boards</b> <b>July 2011 – Feb 2012</b> Designed and Fabricated Circuit Boards using ‘EagleCAD’ and inhouse technique of etching with printers and ferric chloride, Integrated GNU based toolchains with Eclipse for setting up Open-Source Development Environments for these microcontroller series.  Delivered talks on ‘ARM Cortex-M3 Microcontrollers - Software Environment’ in association with the Texas Instruments and ARM University Program at several locations across India.	
PROFESSIONAL EXPERIENCE	<b>Freelancer &amp; Technology Consultant</b> <b>June 2013 – Present</b> Developed customised Android ROMs for Chinese netbooks based on the WM8850 processor, Rooted and customised android internals for latest Android devices.  <b>Teaching Assistant, Math Department, UPenn</b> <b>Aug 2013 – Present</b> Taught and demonstrated techniques for solving advanced calculus problems, Managed a total of 80 students and ensured that each student received personal attention.  <b>Texas Instruments</b> , New Delhi, India <b>Dec 2011 – May 2013</b> Developed Hardware and Software Design for custom-made circuit boards based on ARM Cortex-M3, Integrated the Development and Debugging Environment (JTAG/Serial Wire Debug interfaces) for ARM micro-controllers with OpenOCD for Eclipse.  <b>Software for Education, Entertainment and Training Activities</b> , <b>Dec 2010 – Nov 2011</b> New Delhi, India Lead developer of a Javascript based Spreadsheet application on Android. Conceptualized the research, design and engineering of various mobile and desktop applications.  <b>Sugarlabs - learning software for children</b> , New Delhi, India <b>Aug 2010 – Dec 2011</b> Open-Source Software Development, Committed a number of important patches to the Sugar Desktop Environment as part of the in-house Sugar Development team.	
SKILL SET	Programming: C, C++, Java, SQL, Linux shell scripting, Uppaal, L <sup>A</sup> T <sub>E</sub> X. - Android, Ruby on Rails Others: Circuit Board Design, Model-based Simulation and Verification of Embedded Systems	