Shankar Prasad

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PROFESSIONAL EXPERIENCE

Design Verification Intern, *Coverify Systems Technology*   04/2025 – Present
Fundamentals of Shakti processor verification, including testbench development,
simulation using open-source tools, and validation of RISC-V.

Student Intern, *NITK Siemens Center of Excellence*   01/2025 – 04/2025
Fundamentals of PLC programming & HMI design, Including ladder logic, control
systems, and user interface creation for industrial automation applications.

EDUCATION

BTech in ECE, *NMAMIT, Karkala*   12/2021 – Present

SKILLS

Technical Skill
C, Embedded C, Verilog, System Verilog, UVM,
MATLAB , Basic Microcontroller, Digital Design,
Linux(Basics)

Soft Skills
Leadership, Adaptability, Time Management

COURSES

Introduction to Embedded System Design, *NPTEL*

Hardware Modeling using Verilog" by Indranil Sengupta, *NPTEL*

PROJECTS

Tic-Tac -Toe
- Using 8051 microcontroller.
- The project seamlessly integrates user input mechanisms with intuitive visual outputs, delivering an
  engaging and interactive number-guessing game experience.

Super Mario
Developed an FSM-based system inspired by Super Mario via VS Code.

Secure Communication System Using RSA Algorithm
- Designed and developed the interface between Alice and Bob using I2C protocols to ensure secure and
  efficient communication using Modus Toolbox .
- Implemented cryptography with the RSA algorithm to guarantee data security.

UART Communication Module in SystemVerilog ⊡
- Developed and verified an UART Communication Module using Verilog and a SystemVerilog testbench.
  Created a layered testbench with generator, driver, monitor, and scoreboard.

Interests

- Instrumental Music
- Drawing