Research Article

Demonstration of fast-acting protection as a key enabler for more-electric aircraft interconnected architectures

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Abstract: Driven by anticipated fuel-burn and efficiency benefits, the more-electric aircraft (MEA) concept is a technological shift in the aviation industry, which seeks to replace mechanical, hydraulic and pneumatic functions with electrical equivalents. This shift has greatly increased the electrical power demands of aircraft and has made MEA networks larger and more complex. Consequently, new and more efficient electrical architectures are required, with interconnected generation potentially being one design approach that could bring improved performance and fuel savings. This study discusses the current state of interconnected generation in the aviation industry and key technological advances that could facilitate feasible interconnection options. This study demonstrates that interconnected systems can breach certification rules under fault conditions. Through modelling and simulation, it investigates the airworthiness-requirements compliance of potential impedance solutions to this issue and quantifies the potential impact on system weight. It concludes by identifying fast fault clearing protection as being a key enabling technology that facilitates the use of light-weight and standards-compliant architectures.

1 Introduction

The more-electric concept for aircraft has been a technological shift in the aviation industry that progressively replaces mechanical, pneumatic and hydraulic systems with their electrical equivalents. Civil-aircraft manufacturers have identified that these 'More-Electric' technologies can help reduce overall weight and life-cycle cost for the aircraft operator, as well as optimise performance [1]. This ever-growing electrification on the other hand increases the necessary on-board power generation and distribution requirements, which in turn increase the overall size and complexity of the electrical system. As a result, a modern civil aircraft may utilise up to four types of voltages, 230 and 115 V AC of fixed or variable frequency, and ±270 and 28 V DC. The power from the engine-driven generators feeds the main 230/115 V AC bus before being fed through transformers, auto-transformer-rectifier units and transformer-rectifier units to power the 115 V AC, ±270 and 28 V DC buses, respectively.

Today, the state-of-the-art in more-electric aircraft is represented by the Boeing 787, as it is the first civil aircraft to have most of its pneumatic systems replaced by electrical equivalents [2, 3]. It features a peak emergency generation capability of 1450 kVA, which is provided by two variable-frequency 250 kVA generators on each of its two engines and two 225 kVA auxiliary power units [4].

Further to the move towards electric equivalents, the aviation industry is shifting to electrical power for flight control as well. By removing heavy hydraulic pipework that is prone to leaks [5] and switching to electrical actuators, this brings efficiency benefits and thus fuel savings. So far however, electrical actuation has been limited to secondary flight-control surfaces due to reliability issues and possible jamming risks [6].

With airplane electrical demands ever-increasing and fluctuating aviation-fuel costs, there is a need for novel, lightweight and efficient power systems. New architectures for optimised power extraction form the aircraft engine need to be considered. In the quest for reduced fuel-burn and improved engine operability benefits, on-airframe or on-engine interconnected generation (supporting multi-shaft offtakes and power sharing in particular) could offer fuel efficiency and potentially further increase the reliability of supply to flight-essential loads [7, 8].

This paper will briefly review the state of interconnected generation in the current aviation industry. It will present the challenges associated with paralleled architectures and identify the key technological drivers that may provide a more feasible route for the implementation of such architectures. For a functional systems analysis of a DC-interconnected electrical network, software models of two and three DC bus systems will be presented to investigate the impact of architecture and protection solutions with respect to standards compliance and system mass. The paper shows the criticality of the operating speed of a protection system in achieving light-weight standards compliant interconnected architectures.

2 Interconnected generation

Interconnected generation is relatively rare in the current aviation industry. Most aircraft power systems feature an isolated, radial architecture with redundant cabling, so that despite faults or transients occurring at any point in the network, essential systems are adequately provided with electrical power [2]. It appears that Airbus has not used an interconnected architecture in any of their platforms up to the A340 [9, 10], while Boeing seems to favour isolated generation in its two-engine aircraft and interconnected generation in its four-engine platforms, such as the 707, 720, 727 and 747 [11, 12].

In contrast to the variable-frequency, 235 V AC generators (VFG) of the B787, the 747 is equipped with constant-frequency, 115 V AC integrated drive generators (IDGs), allowing for a feasible AC bus interconnection without the need for heavy, frequency-regulating equipment. Fig. 1 shows a representative schematic of the interconnected electrical system of the 747. The generators are connected to their respective AC buses through individual generator circuit breakers. The closure of the bus tie breakers allows the AC buses to be synchronised, thus achieving AC interconnection. In a similar manner, the closure of all four DC isolation relays allows the interconnection of the DC buses. For the majority of the flight profile, the electrical system is in interconnected generation mode, however below 1500 ft on approach to land, all interconnecting breakers are opened so that the three autopilot systems are powered from independent power supplies [14]. This is to satisfy the higher level of redundancy required by the auto-land system.
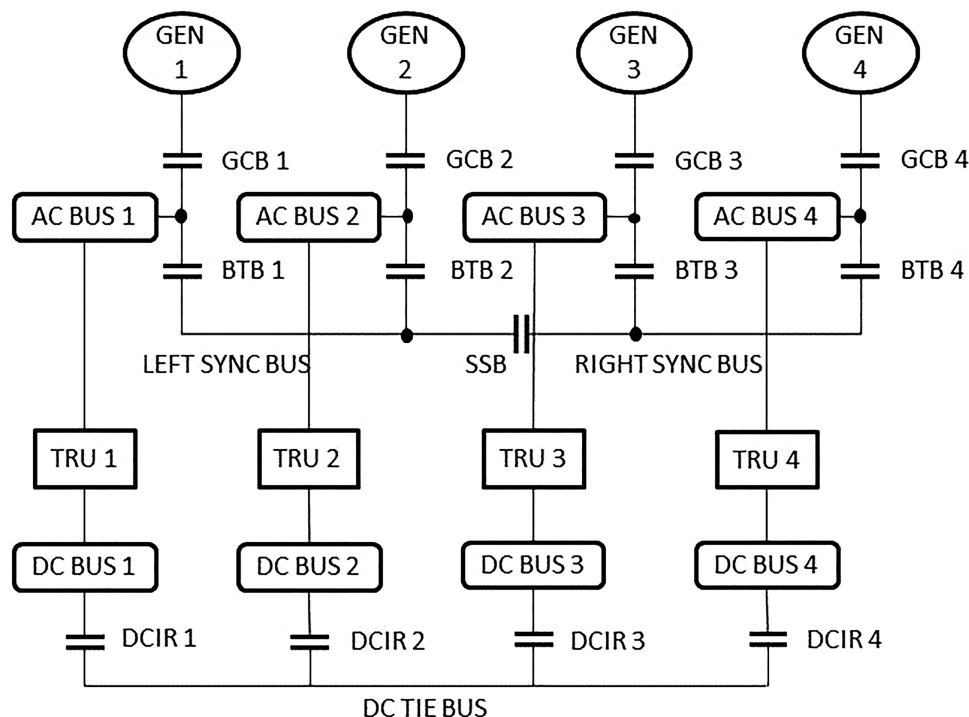
Fig. 1 *Depiction of the B747 interconnected electrical system (adapted from [13])*

Even though the constant-400 Hz IDG has been the predominant civil-aircraft generator technology, new more-electric aircrafts (MEAs) are turning towards the more efficient VFG. Although it too produces three-phase AC power, the frequency varies from 320 to 800 Hz depending on the engine spool speed. The advantages of a VFG include the elimination of the heavy constant-speed gearbox that was used to couple the IDG to the engine, the ability to support electric engine starting, and is also considered to be the most reliable aircraft generator yet [15].

3 Drivers for change

In the proposal of interconnected architectures, together with multi-shaft power offtakes from the gas turbine engines of the aircraft, many authors are claiming that notable improvements in aircraft fuel efficiency could be possible [16–18]. Additionally, recent advancements in fast-acting solid-state protection devices, along with research into engine operability and fuel efficiency benefits may provide key enabling technologies to realise such interconnected architectures. A brief summary of these drivers will be presented in this section.

3.1 Protection equipment

Interconnected generation reduces the level of electrical isolation and allows a greater portion of the aircraft's systems to be exposed to faults at any one point in the network. As such, fast-acting protection strategies and equipment are therefore required to detect and clear faults before network conditions breach the power quality requirements and potentially affect the operation of critical loads. Advances in the field of fault isolation devices and solid-state power controllers (SSPCs) [19–21] may provide a safe and feasible means of interconnection. SSPCs offer very fast protection operation (25–50 μs) and have no moving parts [19, 21]. This makes them less susceptible to wear and tear issues and as such, are good candidates for the harsh operating environments of aviation.

3.2 Fuel efficiency gains through multi-shaft offtakes

In terms of fuel consumption, it is most beneficial for a jet engine to be operated as close to the surge line as possible [22]. However, operation too close or beyond this limit leads to the reversal of airflow through the engine and the catastrophic failure of the

compressor. Underground-idle or low-thrust conditions, engine designs extracting all electrical power from the high-pressure shaft require careful engine-stability control and are therefore operated at higher idle speeds [23]. Multi-shaft engine architectures which extract electrical power from the low-pressure or intermediate-pressure shaft allow for lower idle speeds, thus reducing fuel consumption [24].

In addition to reducing fuel burn, multi-shaft offtakes are thought to offer engine operability benefits, either by regulating the power offtake throughout the various stages of the flight cycle, therefore reducing the surge margin held for electrical transients [25, 26], or by aiding in functions such as electric engine starting [27, 28]. The benefits offered by multi-shaft offtakes however need to be traded off against the weight penalty additional equipment incur for the implementation of these solutions.

3.3 Growing use of DC

Higher-voltage DC distribution is of growing interest within MEA [1] and more-electric engine systems [29] for several reasons. As utilising DC eliminates the need for frequency and phase synchronisation, the paralleling of non-synchronous power sources is better facilitated. DC power distribution also provides a reduction in cable size and weight compared with AC distribution [30]. Research has shown that in compared to an AC architecture, a DC architecture may, firstly by allowing the generators to operate at more efficient operating points [31] and secondly, by reducing the number of power conversion stages between source and load [30], provide a more efficient electrical network [15, 32]. Based on these potential benefits offered by DC distribution, the primary platform studied in this paper will be a DC interconnected electrical architecture.

4 Review of relevant literature

To date, the academic literature has mainly focused on the benefits of interconnected generation and generator/power control strategies and schemes. However, the systems-level impact that interconnected generation may have during abnormal operation conditions is not well documented.

Michalko in [33] proposes a multi-shaft off-take method in which the engine shaft-driven generators of the engine are paralleled onto a common DC bus. Although this might be the simplest multi-shaft off-take arrangement, its most significant
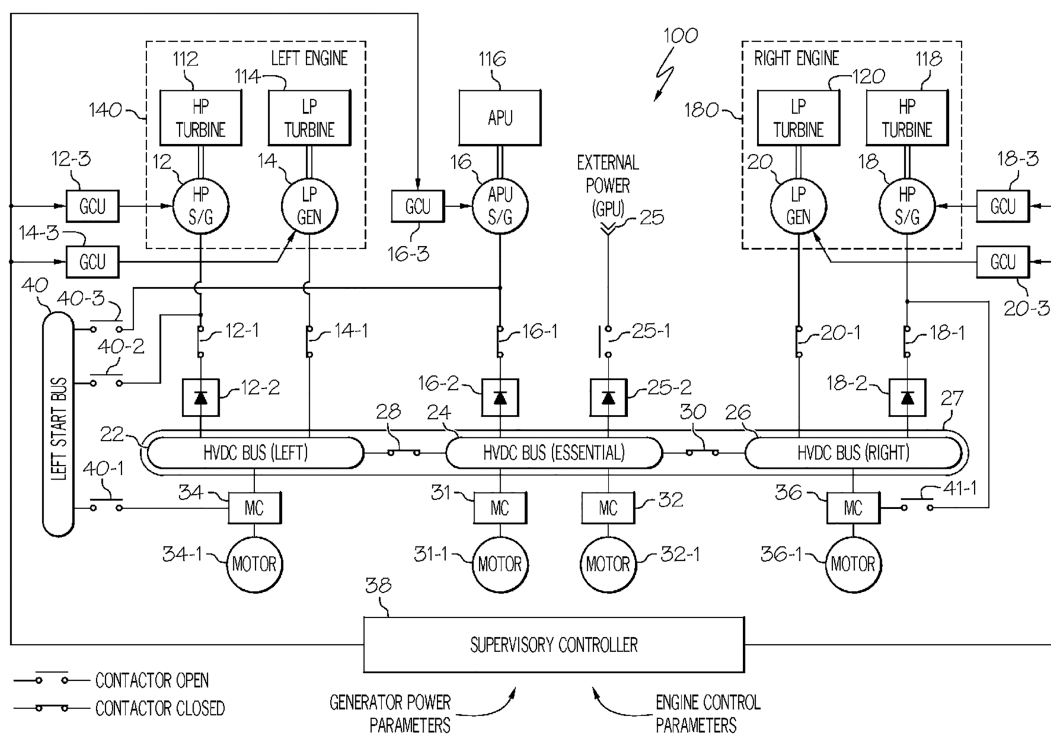
Fig. 2 *Paralleled HVDC bus electrical power system [34]*

drawback is that a fault on the DC bus will result in the loss of supply from both generators. As a means to provide greater flexibility on the control of the extracted power-mix, Yue *et al.* [34] propose the paralleling of all generators onto a common DC bus, shown in Fig. 2. Even though bus tie contactors or other protection equipment may provide some fault isolation capabilities, yet again a fault on the common bus will disrupt all generation sources across the entire network.

Other off-take methods seeking to achieve power transfer between shafts, such as [35, 36], although they differ in their implementation approach, assume normal engine operating conditions and do not consider the effect of an on-engine electrical fault. Overall, relevant patents seem to offer significant gains in terms of engine operability and fuel-burn reduction, however they do not address the certification implications regarding protection methods and the interconnection of power sources.

Research into novel distribution systems [37] and control strategies [38] address the MIL-STD-704F power quality requirements, explained at a later section in this paper, however they do so at a component level under normal operating conditions. Although Muehlbauer and Gerling [38] do consider three different fault types, the loss of generator current control, the loss of a generator and current mismatch between generation and load, these are not indicative of all abnormal operating conditions as defined in the power quality requirements. Abdel-Hafez [39] reviews four 'fault-tolerant' distribution system topologies for MEA, with two of them implementing a paralleled generation approach to some extent, as all generated power is either connected and supplied through the 'primary power distribution system' or via source/load switch matrices. However, in all but one of the topologies reviewed, a fault in the distribution system may interrupt the power supply to the entire network. Chang and Wang [40] propose a distributed system architecture with two parallel power conversion systems, implemented using bidirectional power converters and load grouping, depending on whether the loads require constant- or variable-frequency AC power. Although there is mention of the power quality requirements in this research, it is carried out from the perspective of input current harmonics and total harmonic distortion.

In comparison to faults within AC systems, DC system faults can present very demanding protection challenges with regards to fault current magnitude and propagation speed [41, 42]. To mitigate these issues, converter designs have evolved to provide more fault ride-through capabilities and current limiting to suppress fault magnitude [43–45]. However, the use of current limiting could disrupt the coordination of network protection devices as many fault locations could present similar fault current. To overcome this problem protection devices are often time-graded, thus operating at a slower protection speed, leaving the electrical network exposed to fault conditions for a larger time period [43, 46–48]. In turn, this would further disrupt power supply and power quality to flight-critical loads throughout the network.

Overall in the current literature, the protection challenges and requirements of an interconnected network at a systems-level have received little attention.

5 Implementation challenges of aircraft interconnected systems

The aircraft electrical system is required to provide acceptable power 'during all operations of the power system'. These operations are defined in Military Standard 704F (MIL-STD-704F) [49] and other standards, and govern elements such as voltage magnitude and transients, harmonics, frequency for AC and DC systems. Fig. 3 illustrates the restrictions on the DC voltage profiles during a transient under normal operation and under fault conditions. These standards do not draw a notable distinction between isolated radial architectures and interconnected architectures other than specifying some restrictions on the disruption of power to critical loads. As such, the authors assume that the electrical system of the B747 adheres to the faulted system power quality requirements (as specified in Mil-704F) or similar, under fault conditions, even when interconnected.

However, modern MEAs are generating much more electrical power and at a higher voltage level than traditional aircraft. This electrical energy is used to power a wider variety of systems than ever before, from safety-critical systems to in-flight entertainment consoles. The A380 for instance uses electrical actuation on all control surfaces as a supplement to the hydraulic system [50]. On this basis, the authors feel it appropriate to consider whether adherence to the faulted condition power quality requirements would be still be sufficient for the greater number of higher-power flight-critical electrical loads on-board an interconnected MEA architecture. More specifically, effective 'buffers' should be implemented such that non-faulted parts of the network only experience a 'normal transient' event when the fault occurs
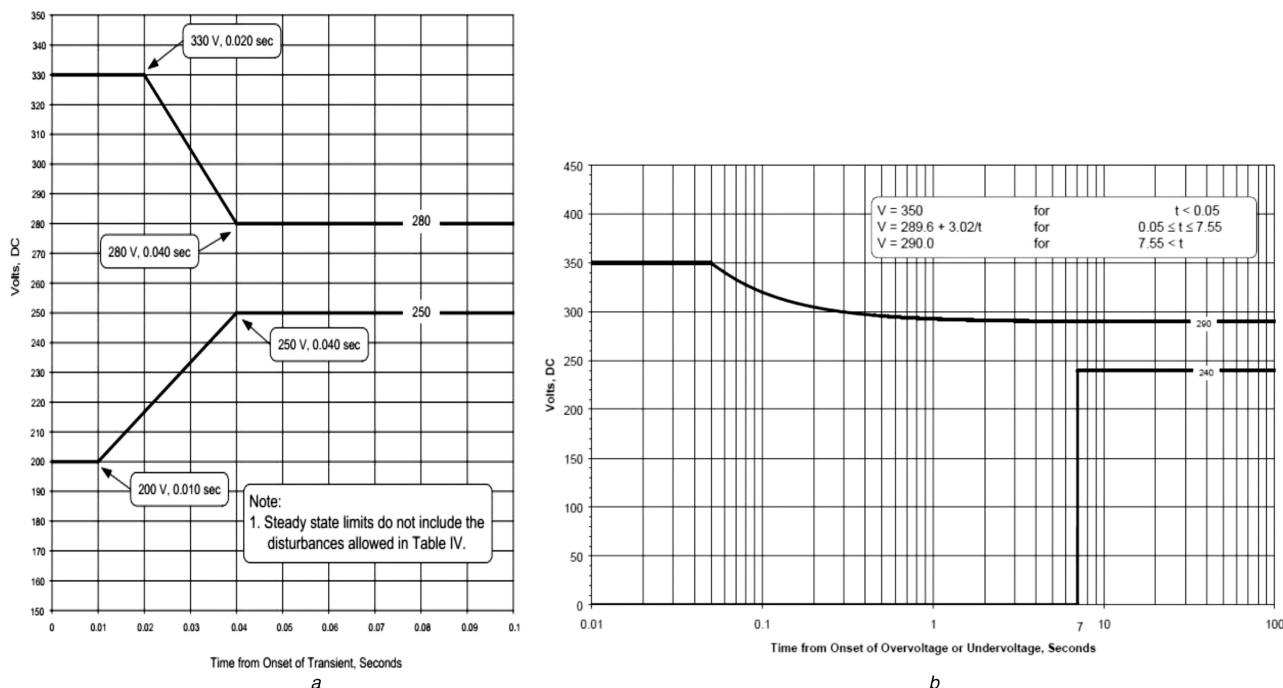
Fig. 3 *Restrictions on the DC voltage profiles during a transient under normal operation and under fault conditions*
(a) Normal voltage transient envelope for 270 V DC systems [49], *(b)* Overvoltage and undervoltage limits for 270 V DC systems [49]
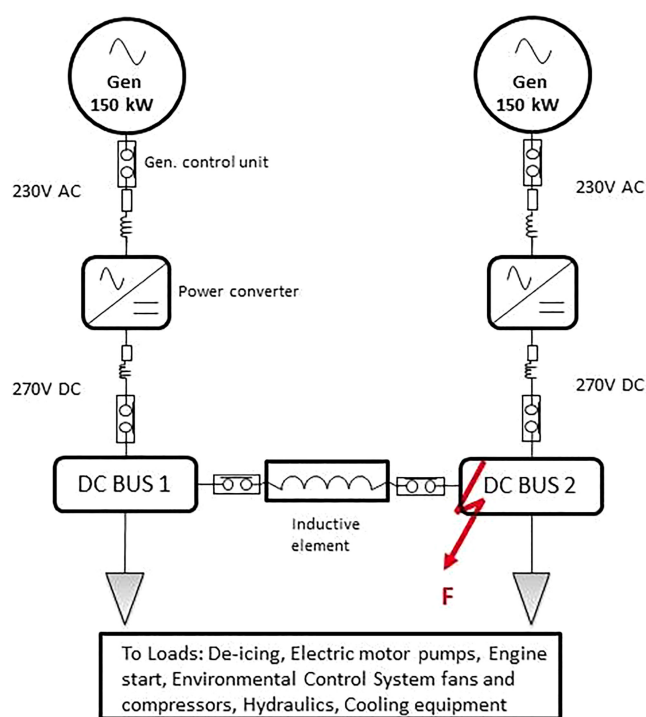

Fig. 4 *Representative single-line diagram of twin-bus interconnected generation Simulink model*

elsewhere on the network, therefore allowing the electrical system to continue to operate under interconnected mode without an unacceptable loss in the performance of critical loads.

Therefore, in order for flight-critical electrical loads to satisfactorily be provided with acceptable power 'during all operations' [49], there must be adequate impedance between the various buses otherwise a single electrical fault will cause the whole network voltage to collapse, breaching power quality standards. This paper will consider the use of an inductive impedance buffer. Whilst a simplistic solution, it will enable clearer analysis of the system dynamics and identification of key performance parameters. This underpinning knowledge can then be readily applied to more complex solutions. This aspect is discussed

with reference to some potential innovative technologies in the conclusion of this paper.

It is expected that the size of this bus-decoupling impedance will be driven by the speed at which the fault can be detected and cleared. Indeed the following analysis will show that faster fault-clearing times can significantly reduce the extent of the propagation of voltage transients throughout the electrical network, requiring less impedance to stabilise the bus voltage within the normal limits. However, it is recognised that available technologies may constrain the possibilities of operation in this aspect.

To date, aircraft systems make use of contactors and electromechanical relays for higher power DC bus fault interruption, as the available solid-state circuit breakers are not suitable for higher power and voltage application within MEA [51]. Whilst this potentially limits the potential minimum fault clearance time possible in an interconnected system, the analysis conducted does not factor in these constraints in order to consider a wide range of system behaviour. Such practical aspects are instead considered in this paper conclusion.

This paper will investigate whether impedance solutions, complete with appropriate fault protection systems can maintain the bus voltage within the set limits and try to estimate the weight penalty incurred for two-bus voltage-compliant interconnected systems. For a more complete study, three-bus interconnected systems will also be analysed, indicative of more-electric engine systems. As the main rationale of the analysis is to assess voltage compliance during fault conditions, only solid short-circuit faults are considered. Whilst other higher impedance or intermittent faults will also be considered as a future work, the impact of these on voltage compliance is expected to be less significant than for short-circuit faults.

6 Simulation analysis

6.1 DC network model

To investigate the effectiveness of potential solutions and their certification implications for the feasible realisation of voltage-compliant DC interconnected networks, a two-generator, 270 V DC paralleled distribution network has been created. A representative single-line diagram of the 300 kVA model is shown in Fig. 4. Key model parameters are summarised in Table 1. A three-generator model of the same rated power will be presented later in this paper.
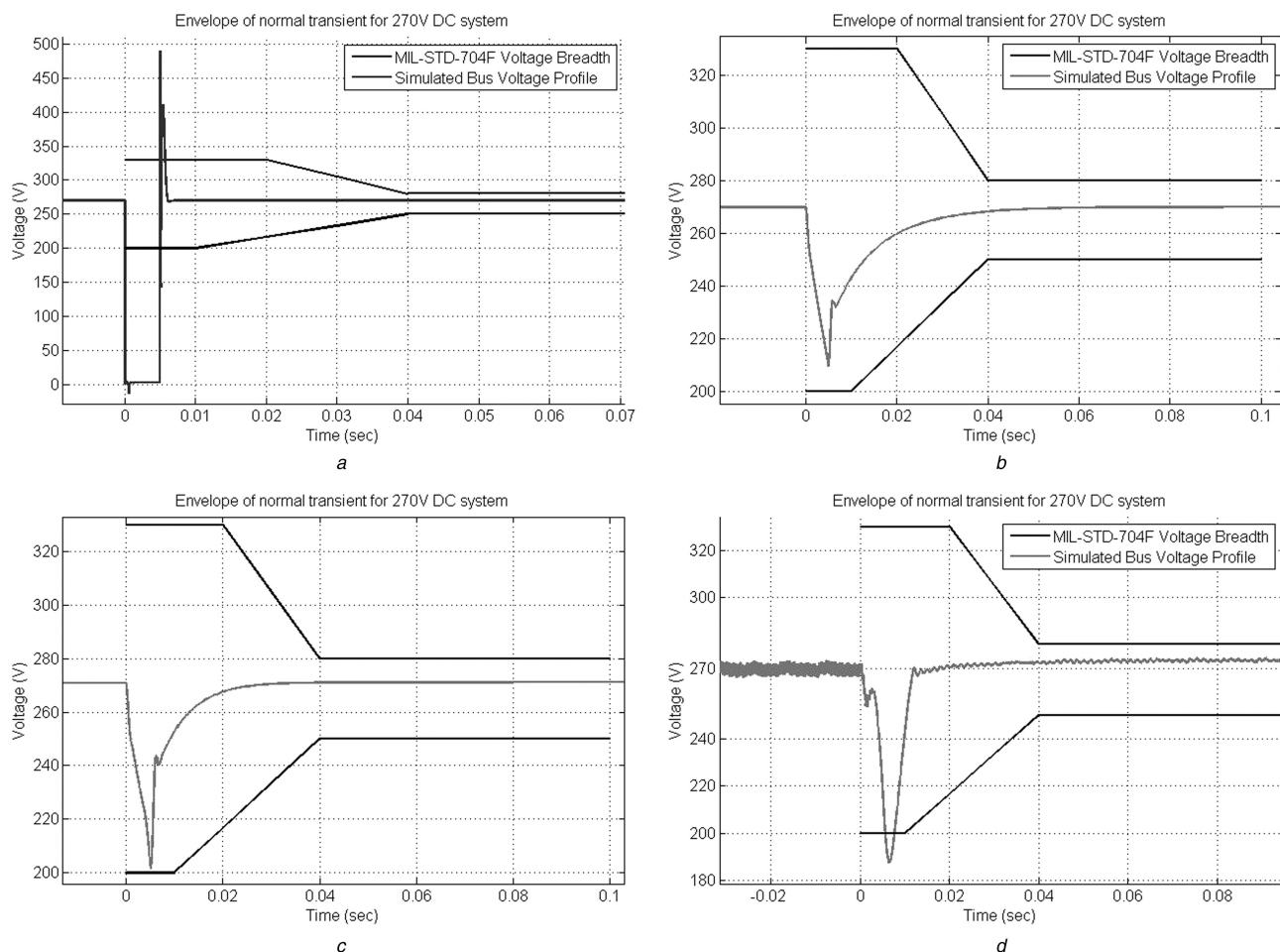
Fig. 5 *Voltage profiles of the non-faulted bus during various fault scenarios for 2 and 3 bus architectures*
(a) Voltage profile of non-faulted bus with negligible inter-bus inductance for a 5 ms fault-clearing time, *(b)* Voltage profile of non-faulted bus with 3 mH inter-bus impedance connection for a 5 ms fault-clearing time, *(c)* Voltage profile of non-faulted bus with 3.5 mH inter-bus impedance connection for a 5 ms fault-clearing time, *(d)* Voltage profile of non-faulted bus with 3 mH inter-bus impedance connection for a 5 ms fault-clearing time

The models of interconnected DC architectures developed for this investigation were created at a functional level of fidelity in accordance with guidance provided in [52] using the Matlab/ Simulink software package. These functional models neglect switching level transients in order to minimise computational burden and facilitate time-efficient extensive simulations, but still capture the power system and controller dynamics and at a sufficient fidelity.

The generation systems are comprised of 4-pole permanent magnet machines, interfaced with controlled rectifiers operating with drooped voltage control which in turn are connected to lumped loads via DC buses. The per-meter feeder resistance and inductance have been adapted from [53] to a quarter of the length of the B787-8, i.e. 14.2 m. Serving as a decoupling mechanism, an inductor is used to interconnect the DC buses. The rating of the inductor will vary for different simulation scenarios, as will be explained later in this paper. Solid short-circuit faults are introduced under full-load, balanced conditions on DC Bus 2 to investigate the behaviour of the interconnected system under fault conditions and the impact a varied fault-clearing time has on the voltage profile of DC Bus 1.

Table 1 Network parameters of paralleled distribution model

rated power	300 kVA
operating voltage	270 V DC
2-generator nom. current	555 A
3-generator nom. current	370 A
feeder resistance	0.801 mΩ/m
feeder inductance	0.65 µH/m

To set a baseline, Fig. 5a depicts the voltage measured on the non-faulted DC Bus 1 during a short-circuit fault on DC Bus 2, with a negligible inter-bus impedance connection. The fault is applied at $t = 0$ s and cleared after 5 ms, realising a 5 ms protection operation speed. This protection speed will be varied for different simulation scenarios. During this transient event, the simulated voltage (blue line) collapses to near-zero during the fault and then overshoots the compliant voltage breadth (red lines) once the fault has been cleared. Clearly, the voltage profile of the non-faulted bus exceeds the bounds of the normal voltage envelope defined in MIL-STD-704F.

It is therefore apparent that in order to decouple the transient responses of the interconnected DC buses to some extent, an effective inter-bus impedance is required. In this approach, although the voltage of the faulted bus will collapse, the non-faulted bus should then only experience a standards-compliant voltage transient. The following simulation cases will investigate this for two and three DC bus architectures.

6.2 Simulation analysis of two-bus network

To observe the impact on the transient response of the non-faulted bus, an inductive inter-bus impedance connection was employed and a wide range of inductance values were considered. From this analysis, it was possible to identify suitable inductor ratings, which could enable standards-compliant interconnection options for a range of simulated fault clearance times. In comparison to the baseline profile of Fig. 5a, the voltage profile of the non-faulted bus using a 3 mH inductor for a fault-clearing time of 5 ms is illustrated in Fig. 5b. Clearly, the voltage profile is within the defined limits. Aggregated data regarding inductor ratings for
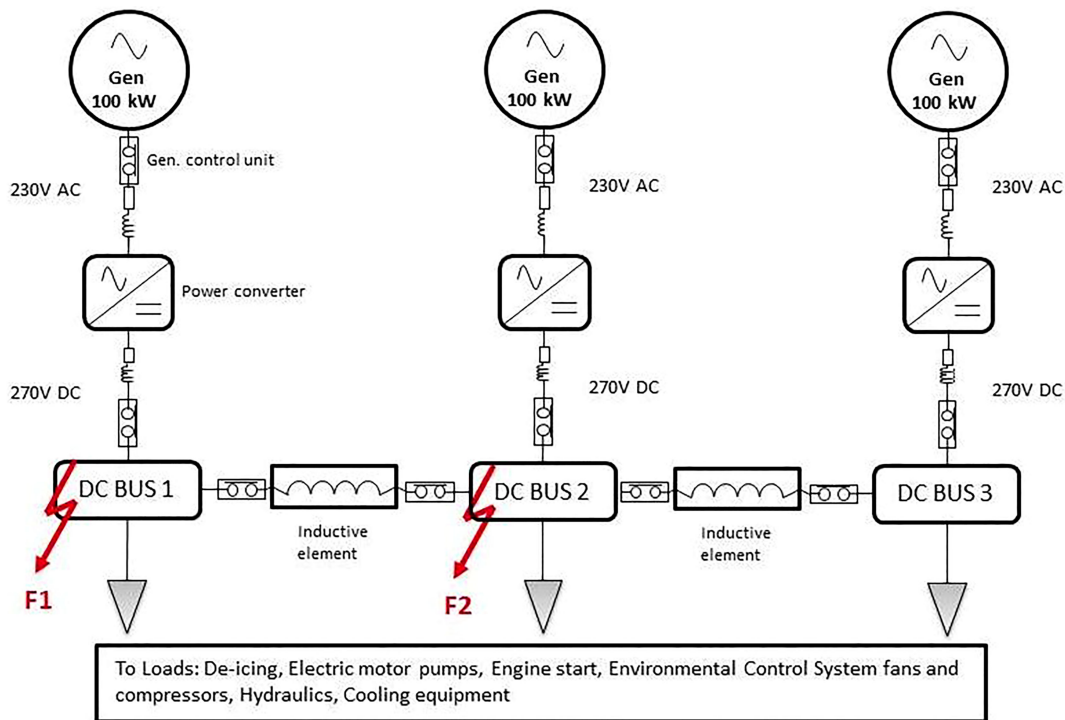
Fig. 6 *Representative single-line diagram of three-bus interconnected generation Simulink model*

Table 2 Inductor ratings and through fault current

Architecture type	2 Bus architecture				3 Bus architecture			
fault-clearance time, ms	5	1	0.5	0.1	5	1	0.5	0.1
inductor, mH	3	1	0.5	0.1	3.5	1	0.7	0.1
fault current, A	389	237	236	237	334	237	182	236

different fault-clearing times will be presented at a later section in this paper.

6.3 Simulation analysis of three-bus network

Besides the addition of a second, equally-rated inductor, a similar approach was adopted for a three-bus network, as shown in Fig. 6. Due to the symmetry of the network, only two fault locations were considered, F1 and F2, however there appeared to be no significant difference in fault response between these fault locations in terms of peak fault current and voltage profile. Fig. 5*c* illustrates the voltage profile of the non-faulted DC Bus 3 during a fault sustained for 5 ms with 3.5 mH of inter-bus impedance.

7 Inductor rating against protection speed

As previously stated, the inductor rating is in direct correlation to the operation speed of the protection system. Faster fault-clearing times are expected to lessen voltage transients propagating throughout the network, thus reducing the required impedance necessary to achieve compliant interconnections. In turn, this reduces the added weight penalty for each interconnected architecture.

Following extensive simulations, aggregated inductor rating data, along with the sensed fault current passing through the interconnecting inductor for two and three DC bus architectures are summarised in Table 2. It is clear from the table that compliance is achievable with smaller inductors if faults are cleared in a shorter time frame.

As system mass is an effective illustrator for the apparent trade-off between inductance sizing and protection operation speed, in order to achieve a uniform comparison, a kg mass per unit mH-A rating was derived by the authors. This figure was derived from a lightweight, high-current, aviation-grade inductor [54] available commercially to be 0.025 kg/mH-A.
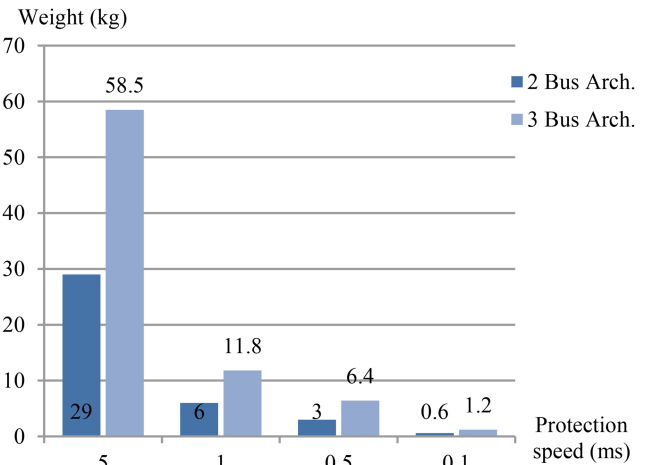

Fig. 7 *Required inductor weight against protection operation speed*

The individual inductor weights, as shown in Fig. 7, are calculated using the following equation:

$$\text{Weight} = k \cdot L \cdot I_{\text{fault}} \tag{1}$$

where k equals 0.025 kg/mH-A, L represents the necessary impedance and I_{fault} is the maximum current passing through the inductor during the fault. It should be noted that these weights do not include bus-tie breakers or contactors.

Overall, it is evident that the 3 Bus architecture carries approximately twice the weight penalty of the 2 Bus architecture, which is to be expected as the former requires one more interconnecting inductor than the latter. More specifically, it would appear that for any one architecture, any change in the protection speed is mirrored by the same change in the inductor weight. In the

Table 3 Inductor ratings for generator imbalance conditions

Architecture type	2 Bus architecture				3 Bus architecture			
fault-clearance time, ms	5	1	0.5	0.1	5	1	0.5	0.1
inductor, mH	8.5	2	1	0.15	10.5	2.5	1.5	0.15

Table 4 Inductor rating for fluctuating voltage conditions

Architecture type	2 Bus architecture				3 Bus architecture			
fault-clearance time, ms	5	1	0.5	0.1	5	1	0.5	0.1
inductor, mH	3.5	1	0.6	0.3	6.5	2	1	0.5
fault current, A	384	314	330	338	208	198	278	260

2 Bus architecture, for example making the protection speed five times faster, from 5 to 1 ms, means that the required inductor is approximately five times lighter. This demonstrates that fast protection operation speed is crucial to the potential feasibility of interconnected systems.

8 Adverse factors on inductance ratings

In previous sections, the identified impedance ratings were derived under full-load, balanced operation conditions. This section will briefly discuss the effects of unbalanced conditions and power quality on the required impedance ratings necessary to achieve compliant interconnection. The adverse effects of adding inductance in-between interconnected buses will also be considered with respect to transient load sharing and protection relay coordination.
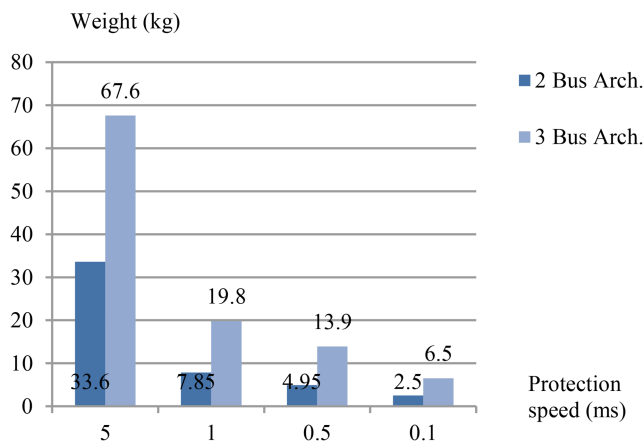

Fig. 8 *Required inductor weight against protection operation speed for fluctuating voltage conditions*
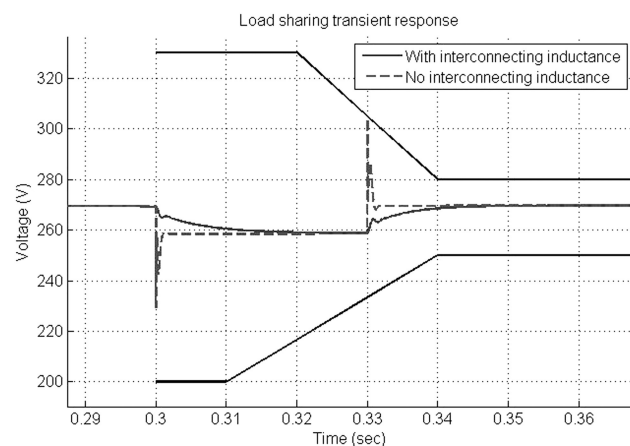

Fig. 9 *Load sharing transient response during a 50 kW step change with interconnecting inductance (blue line) and without (dashed line)*

8.1 Generator imbalance

For both architectures, a similar simulation analysis was carried out with one generator operating at 50% of its rated power and the adjacent generator operating at 150%, indicative of an engine/ throttle push-back emergency. From this analysis, it was evident that if the fault is applied on the respective DC bus of the under-performing generator, there is no breach of the voltage envelope, however if the fault is on the respective DC bus of the over-performing generator, then the impedance ratings identified previously cannot maintain the non-faulted bus voltage within the defined limits. In the latter case, the interconnecting inductors should be over-rated, as shown in Table 3, further increasing the weight penalty of the architecture.

8.2 Power quality

As a means of introducing a degree of added instability into both simulated power networks, whilst complying with a maximum of 6 V voltage ripple imposed by the standards, a 10 kVA converter fed constant-power load was attached to each DC bus. Due to the introduced voltage fluctuations, the initially identified inductors were unable to stabilise the non-faulted bus voltage inside the bounds of MIL-STD-704F, as shown in Fig. 5d for the 2 Bus architecture for a 5 ms clearing time. Therefore new minimum inductor ratings are required for both architectures, as shown in Table 4, and their respective mass shown in Fig. 8. The 1:1 ratio between protection speed and inductor weight identified in a previous section does not hold in this case as, with the addition of voltage fluctuations, larger inductors are required, resulting in a new ratio of 1:0.68–0.85 depending on the specific architecture. This ratio becomes worse in protection speeds of 0.5 and 0.1 ms as it appears the inductor weight reduces by a factor of 0.43 and 0.39 for the 3 and 2 Bus architectures, respectively.

8.3 Potentially undesired effects due to interconnecting inductance

The addition of inductance between adjacent buses could potentially compromise the stability of the electrical network with

regards to the transient load sharing of generators following step changes in load. Although this poses a serious problem in islanded microgrids [55], where poor transient load sharing is exhibited when synchronous generators are paired with inverters, this does not seem to be an issue with more interconnected systems [56], or a wider threat to the stability of the system [57]. Although there are means of control to mitigate for such issues, such as droop-based and master–slave control, a deeper analysis of those is out of the scope of this paper. Fig. 9 shows a comparison of voltage response to a 50 kW load step and down on the three-bus network both with and without the inclusion of inter-bus inductance. From these results, it can be seen that the response of this particular inductive system is over damped, reducing the peak of the voltage transients but extending the settling time of the system.

During a fault, the fault-current magnitude and voltage-disturbance propagation are expected to be greater in an interconnected network than in an isolated system. Therefore the protection system devices and relays must be reconfigured to operate and coordinate accordingly. Although research has shown

that high-impedance faults can result in dampened protection operation [58] or protection blinding [59], in the simulations presented earlier, the ratings of the interconnecting solutions proposed are driven by low-impedance short-circuit faults. This along with novel research into high-impedance fault mitigation [60], leads the authors to believe that relay coordination poses no greater problem compared to other protection issues, however a relay-coordination analysis is out of the scope of this paper.

9 Conclusion

This paper briefly reviewed the state of interconnected generation within the current aviation industry and presented the challenges associated with interconnected architectures, as well as key technological drivers that could provide a more feasible route for the implementation of such architectures. It was also demonstrated that closely coupled DC systems could breach power quality requirements under fault conditions.

To mitigate this, inductive impedance connection solutions acting as a DC-bus voltage decoupling mechanism for two and three DC bus architectures were analysed from a power quality and system mass perspective. From the results presented, the inductive decoupling method was shown to be critical to the standards compliance, suggesting that other active solutions such as solid-state current limiting for example [51] may not be sufficient in isolation unless new standards for interconnection are developed which permit very short duration but significant magnitude transients across the wider network, or the faulted system voltage envelopes can be shown to be acceptable for higher power critical loads.

In addition, the rating/mass of the interconnecting solution was shown to be in direct correlation to the operating speed of the protection system. Although there are no weight or noise thresholds clearly set out in the airworthiness standards concerning the electrical system, it is believed that there can be weight restrictions or budgets at a manufacturer systems-design level and/or that these restrictions may also be customer driven. It is therefore important that any modifications to the electrical system incur the smallest weight penalty possible, thus demonstrating that fast-acting protection and fault clearance technologies are key enablers towards feasibly-implemented interconnected systems. From a technical perspective, this suggests added value in the development of SSPC technologies for future MEA applications, as well as smaller aviation-grade higher-voltage/power inductors necessary for such applications.

10 Acknowledgments

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