Register No. 312126510073

VADHRA UNIVERSITY

053300

Official Memo No. E V(Sup)/2016

This is to certify that Mr./Ms. MULLAPUDI SHANMUKHA S/O/D/O MULLAPUDI VENKATESWARA RAO B.E./B. Tech Degree Provisional Certificate cum Consolidated Memorandum of Grades

12 he/she has done all that is necessary for the formal presentation of the Degree. inersity, he/she having been declared to have passed the examinations prescribed therefor held in MARCH, 2016 and s qualified himself herself for the Degree of Bachelor of Technology (Computer Science Engineering) award of this

LECTRONICS - I LEMENTS OF ELECTRICAL ENGINEERING SATA STRUCTURES I- STRUCTURES - I	DEC'5013 DEC'5013 DEC'5013	† † †	0 A 0	OPERATIONS RESERRCH DISCRETE MATHEMATICAL STRUCTURES - II MICROPROCESSORS - I COMPUTER ORGANIZATION	4102,894 4102,894 4102,894 4102,894	7 7 7	8 8
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CGPA: 8.53

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SGPA: 7.57	CGPA: 8.15		
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SGPA: 7.52 CGPA: 8.28

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80.8: A908

MICROPROCESSORS - I LAB

ENVIRONMENTAL STUDIES

OBJECT ORIENTED PROGRAMMING LAB

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Examined: () Read:

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Passing

Month & Year of

CGPA: 8.71

DEC'5013

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Credits Obtained

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200

SOFT SKILLS

MICROPROCESSOR-II LAB

OPERATING SYSTEMS LAB

FORMAL LANGUAGES & AUTOMATA THEORY

PROBABILITY STATISTICS & QUEUEING THEORY

Subjects

OPERATING SYSTEMS

COMPUTER GRAPHICS

SYSTEM PROGRAMMING

SGPA: 8.00

DATA STRUCTURES LAB

DIGITAL LOGIC DESIGN

ELECTRONICS LAB

WICKOPROCESSOR-II

FILE STRUCTURES

7102-40-01 : etsC