ZORRO

File: sheet1.kicad_sch

CPLD_L

File: sheet2.kicad_sch

PCI – Part1

File: sheet3.kicad_sch

PCI – Part2

File: sheet4.kicad_sch

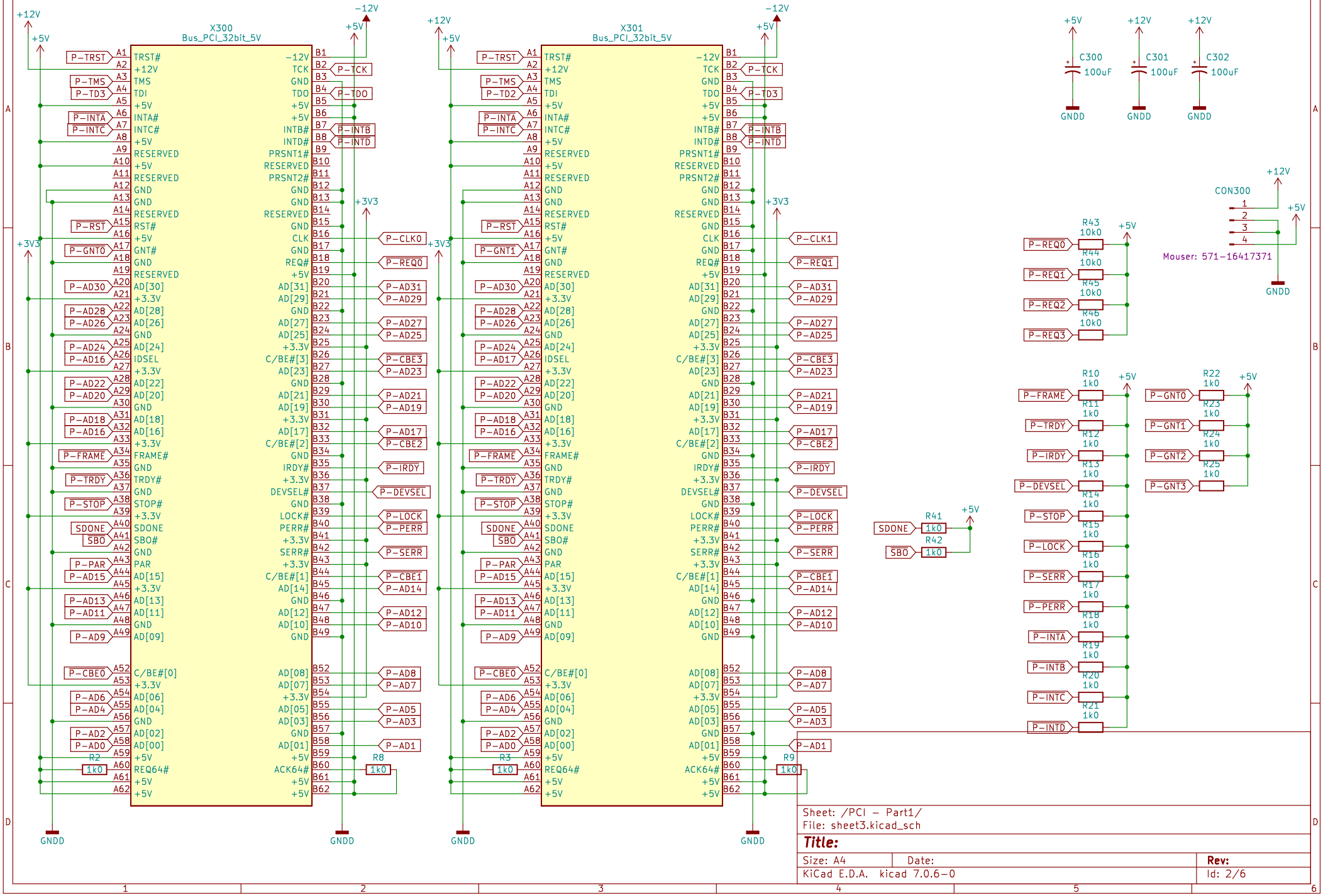
CPLD_H

File: sheet5.kicad_sch

Sheet: /
File: Prometheus.kicad_sch

Title:

Size: A4	Date:	**Rev:**
KiCad E.D.A. kicad 7.0.6-0		Id: 1/6


X300
Bus_PCI_32bit_5V
X301
Bus_PCI_32bit_5V
TRST#
+12V
TMS
TDI
+5V
INTA#
INTC#
RESERVED
RST#
GNT#
AD[30]
+3.3V
AD[28]
AD[26]
AD[24]
IDSEL
FRAME#
TRDY#
STOP#
SDONE
SBO#
PAR
AD[15]
AD[13]
AD[11]
AD[09]
C/BE#[0]
AD[06]
AD[04]
AD[02]
AD[00]
REQ64#
-12V
TCK
GND
TDO
INTB#
INTD#
PRSNT1#
PRSNT2#
CLK
REQ#
AD[31]
AD[29]
AD[27]
AD[25]
C/BE#[3]
AD[23]
AD[21]
AD[19]
AD[17]
C/BE#[2]
IRDY#
DEVSEL#
LOCK#
PERR#
SERR#
C/BE#[1]
AD[14]
AD[12]
AD[10]
AD[08]
AD[07]
AD[05]
AD[03]
AD[01]
ACK64#
P-TRST
P-TCK
P-TMS
P-TDI
P-TD2
P-TDO
P-TD3
P-INTA
P-INTB
P-INTC
P-INTD
P-RST
P-CLK0
P-CLK1
P-GNT0
P-GNT1
P-REQ0
P-REQ1
P-AD30
P-AD31
P-AD29
P-AD28
P-AD26
P-AD27
P-AD25
P-AD24
P-AD17
P-CBE3
P-AD23
P-AD22
P-AD20
P-AD21
P-AD19
P-AD18
P-AD16
P-AD17
P-CBE2
P-FRAME
P-IRDY
P-TRDY
P-DEVSEL
P-STOP
P-LOCK
P-PERR
P-SERR
SDONE
SBO
P-PAR
P-AD15
P-CBE1
P-AD14
P-AD13
P-AD11
P-AD12
P-AD10
P-AD9
P-CBE0
P-AD6
P-AD4
P-AD8
P-AD7
P-AD5
P-AD3
P-AD2
P-AD0
P-AD1
R2
R3
R8
R9
1k0
C300
C301
C302
100uF
GNDD
+12V
+5V
+3V3
-12V
CON300
Mouse: 571-16417371
R41
R42
R43
R44
R45
R46
10k0
R10
R11
R12
R13
R14
R15
R16
R17
R18
R19
R20
R21
R22
R23
R24
R25
Sheet: /PCI - Part1/
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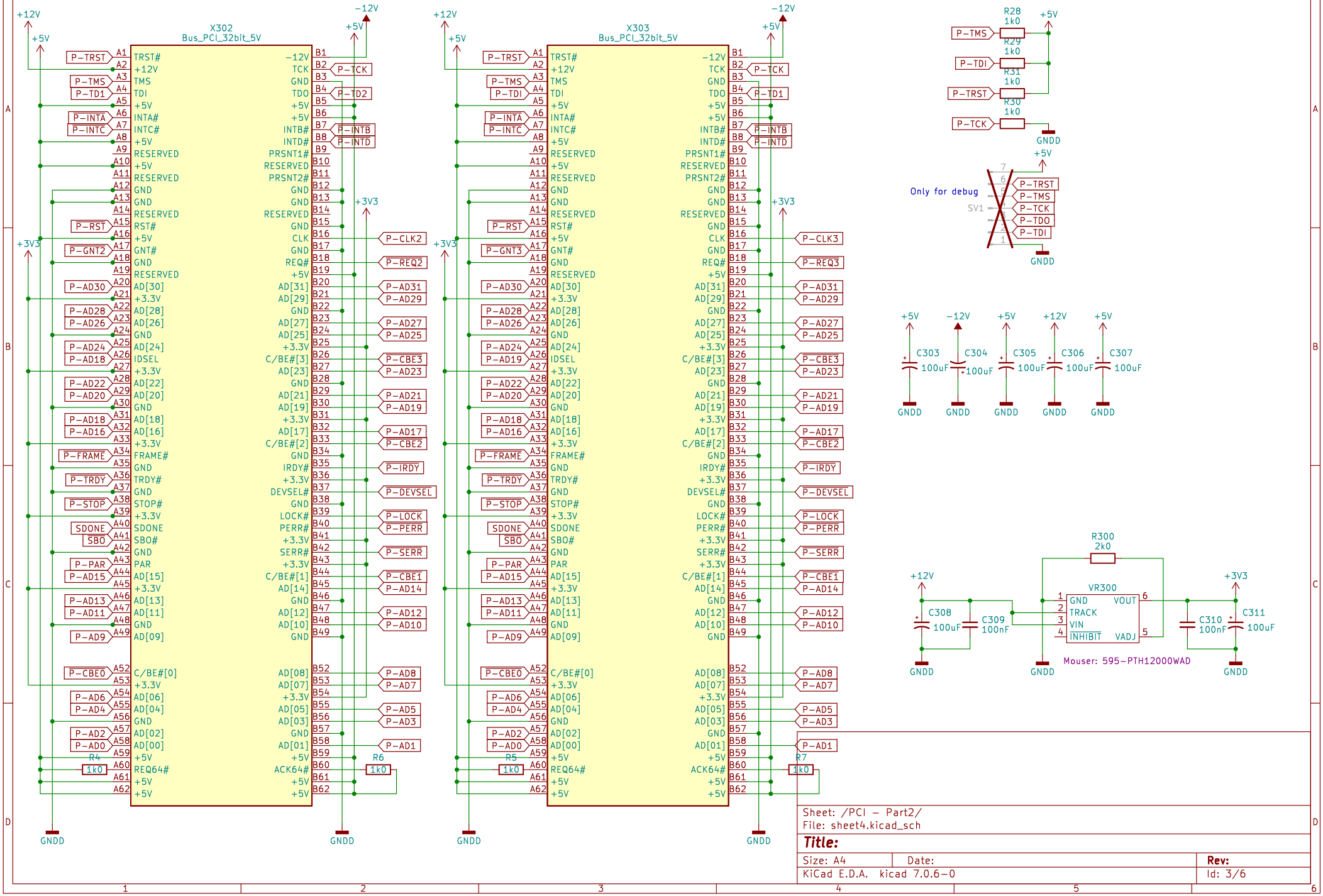
Title:
Size: A4
Date:
Rev:
KiCad E.D.A. kicad 7.0.6-0
Id: 2/6



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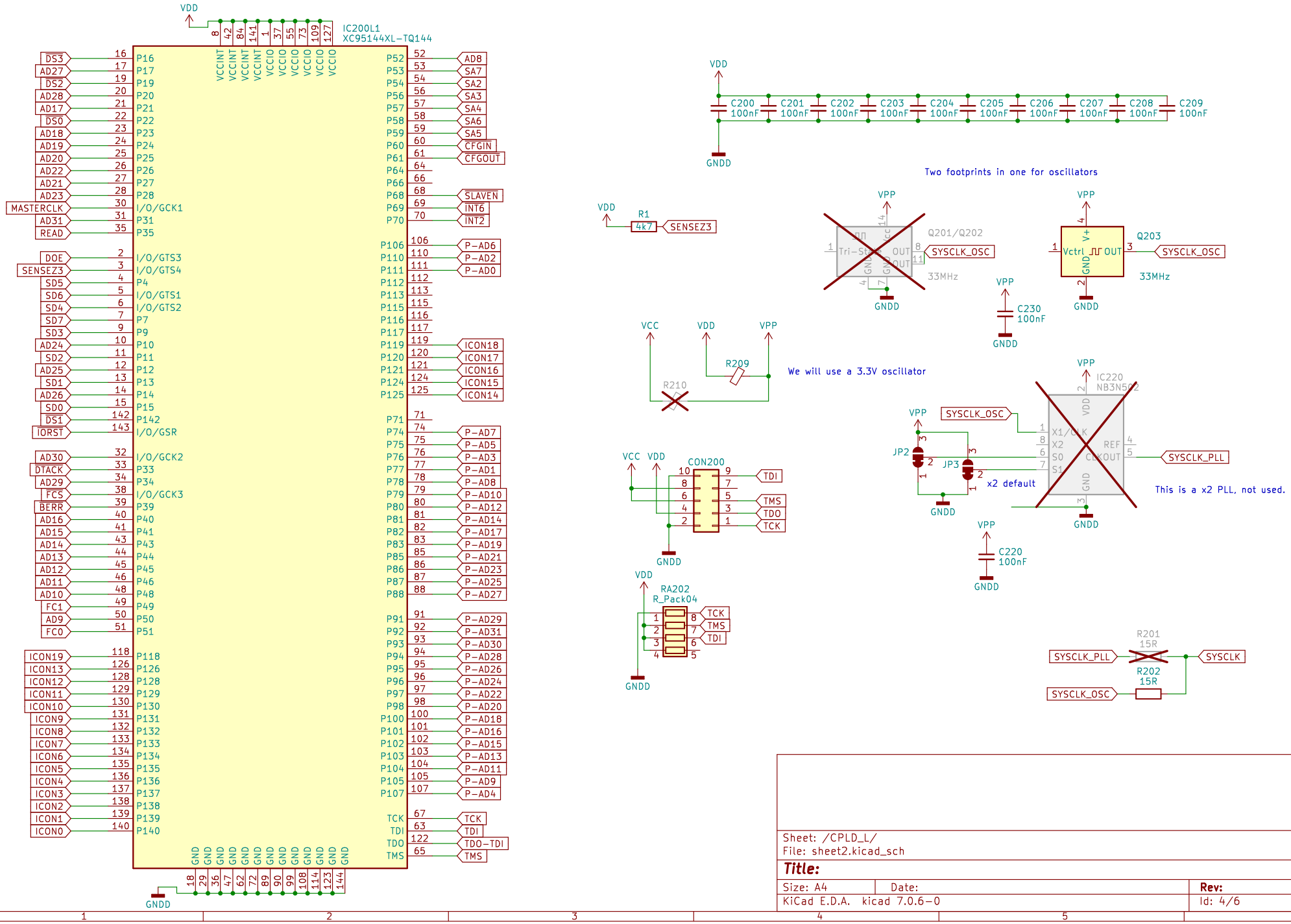
Size: A4	Date:	**Rev:**
KiCad E.D.A. kicad 7.0.6-0		Id: 3/6


IC200L1
XC95144XL-TQ144
VDD
GNDD
VCCINT
VCCIO
GND
P16
P17
P19
P20
P21
P22
P23
P24
P25
P26
P27
P28
I/O/GCK1
P31
P35
I/O/GTS3
I/O/GTS4
P4
I/O/GTS1
I/O/GTS2
P7
P9
P10
P11
P12
P13
P14
P15
P142
I/O/GSR
I/O/GCK2
P33
P34
I/O/GCK3
P39
P40
P41
P43
P44
P45
P46
P48
P49
P50
P51
P118
P126
P128
P129
P130
P131
P132
P133
P134
P135
P136
P137
P138
P139
P140
P52
P53
P54
P56
P57
P58
P59
P60
P61
P64
P66
P68
P69
P70
P106
P110
P111
P112
P113
P115
P116
P117
P119
P120
P121
P124
P125
P71
P74
P75
P76
P77
P78
P79
P80
P81
P82
P83
P85
P86
P87
P88
P91
P92
P93
P94
P95
P96
P97
P98
P100
P101
P102
P103
P104
P105
P107
TCK
TDI
TDO
TMS
DS3
AD27
DS2
AD28
AD17
DS0
AD18
AD19
AD20
AD22
AD21
AD23
MASTERCLK
AD31
READ
DOE
SENSEZ3
SD5
SD6
SD4
SD7
SD3
AD24
SD2
AD25
SD1
AD26
SD0
DS1
IORST
AD30
DTACK
AD29
FCS
BERR
AD16
AD15
AD14
AD13
AD12
AD11
AD10
FC1
AD9
FC0
ICON19
ICON13
ICON12
ICON11
ICON10
ICON9
ICON8
ICON7
ICON6
ICON5
ICON4
ICON3
ICON2
ICON1
ICON0
AD8
SA7
SA2
SA3
SA4
SA6
SA5
CFGIN
CFGOUT
SLAVEN
INT6
INT2
P-AD6
P-AD2
P-AD0
ICON18
ICON17
ICON16
ICON15
ICON14
P-AD7
P-AD5
P-AD3
P-AD1
P-AD8
P-AD10
P-AD12
P-AD14
P-AD17
P-AD19
P-AD21
P-AD23
P-AD25
P-AD27
P-AD29
P-AD31
P-AD30
P-AD28
P-AD26
P-AD24
P-AD22
P-AD20
P-AD18
P-AD16
P-AD15
P-AD13
P-AD11
P-AD9
P-AD4
TCK
TDI
TDO-TDI
TMS
C200 100nF
C201 100nF
C202 100nF
C203 100nF
C204 100nF
C205 100nF
C206 100nF
C207 100nF
C208 100nF
C209 100nF
Two footprints in one for oscillators
R1
4k7
SENSEZ3
VPP
Q201/Q202
Tri-State
OUT
SYSCLK_OSC
33MHz
Q203
Vctrl
C230 100nF
VCC
R209
R210
We will use a 3.3V oscillator
CON200
RA202
R_Pack04
IC220
NB3N502
JP2
JP3
x2 default
SYSCLK_PLL
This is a x2 PLL, not used.
C220 100nF
R201 15R
R202 15R
SYSCLK
Sheet: /CPLD_L/
File: sheet2.kicad_sch
Title:
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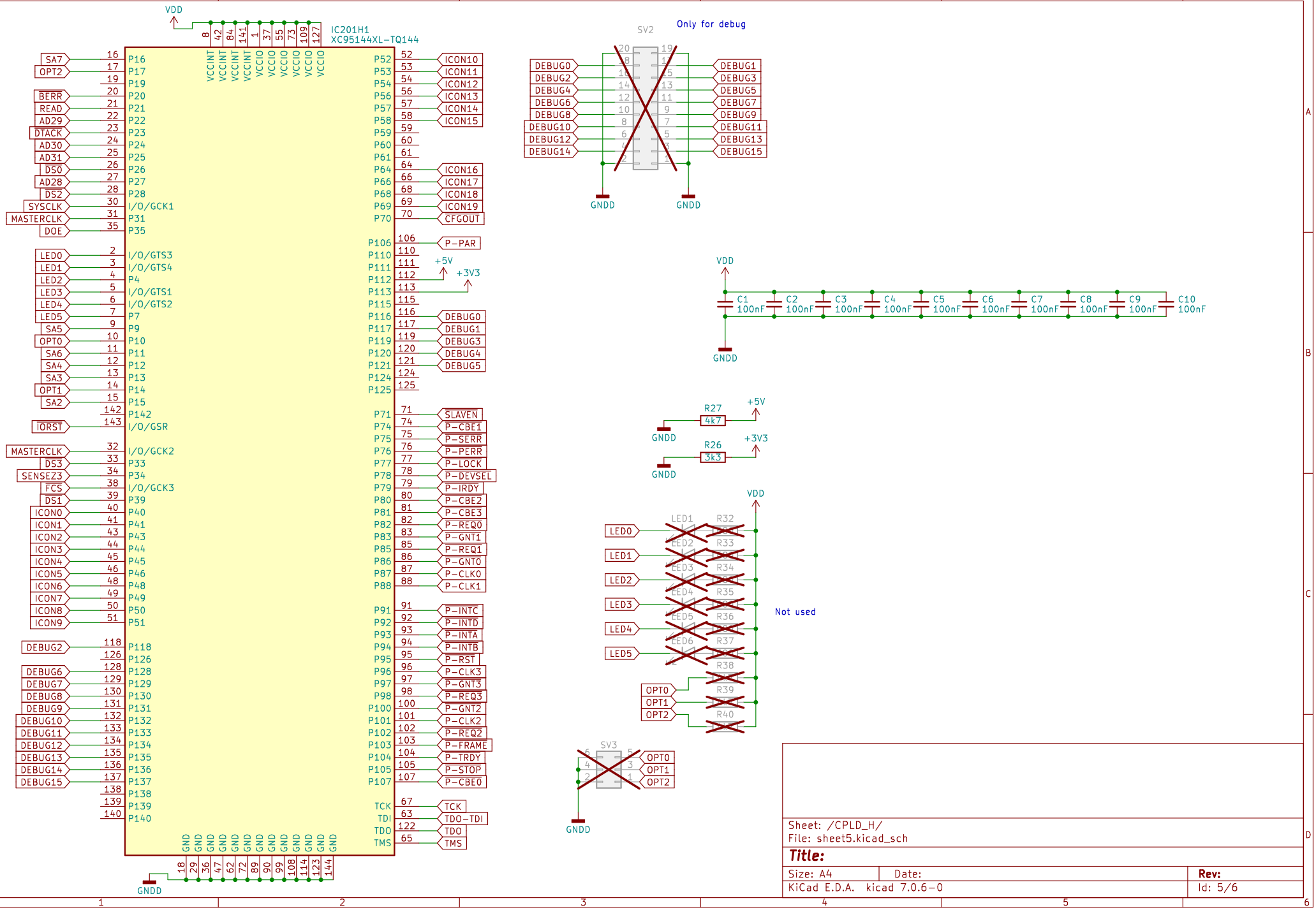
VDD
IC201H1
XC95144XL-TQ144
SA7
OPT2
BERR
READ
AD29
DTACK
AD30
AD31
DS0
AD28
DS2
SYSCLK
MASTERCLK
DOE
LED0
LED1
LED2
LED3
LED4
LED5
SA5
OPT0
SA6
SA4
SA3
OPT1
SA2
IORST
MASTERCLK
DS3
SENSEZ3
FCS
DS1
ICON0
ICON1
ICON2
ICON3
ICON4
ICON5
ICON6
ICON7
ICON8
ICON9
DEBUG2
DEBUG6
DEBUG7
DEBUG8
DEBUG9
DEBUG10
DEBUG11
DEBUG12
DEBUG13
DEBUG14
DEBUG15
ICON10
ICON11
ICON12
ICON13
ICON14
ICON15
ICON16
ICON17
ICON18
ICON19
CFGOUT
P-PAR
+5V
+3V3
DEBUG0
DEBUG1
DEBUG3
DEBUG4
DEBUG5
SLAVEN
P-CBE1
P-SERR
P-PERR
P-LOCK
P-DEVSEL
P-IRDY
P-CBE2
P-CBE3
P-REQ0
P-GNT1
P-REQ1
P-GNT0
P-CLK0
P-CLK1
P-INTC
P-INTD
P-INTA
P-INTB
P-RST
P-CLK3
P-GNT3
P-REQ3
P-GNT2
P-CLK2
P-REQ2
P-FRAME
P-TRDY
P-STOP
P-CBE0
TCK
TDO-TDI
TDO
TMS
GNDD
SV2
Only for debug
DEBUG0
DEBUG2
DEBUG4
DEBUG6
DEBUG8
DEBUG10
DEBUG12
DEBUG14
DEBUG1
DEBUG3
DEBUG5
DEBUG7
DEBUG9
DEBUG11
DEBUG13
DEBUG15
GNDD
GNDD
VDD
C1 100nF
C2 100nF
C3 100nF
C4 100nF
C5 100nF
C6 100nF
C7 100nF
C8 100nF
C9 100nF
C10 100nF
GNDD
R27 4k7
+5V
GNDD
R26 3k3
+3V3
GNDD
VDD
LED0
LED1
LED2
LED3
LED4
LED5
R32
R33
R34
R35
R36
R37
R38
R39
R40
OPT0
OPT1
OPT2
Not used
SV3
OPT0
OPT1
OPT2
GNDD
Sheet: /CPLD_H/
File: sheet5.kicad_sch
Title:
Size: A4
Date:
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KiCad E.D.A. kicad 7.0.6-0
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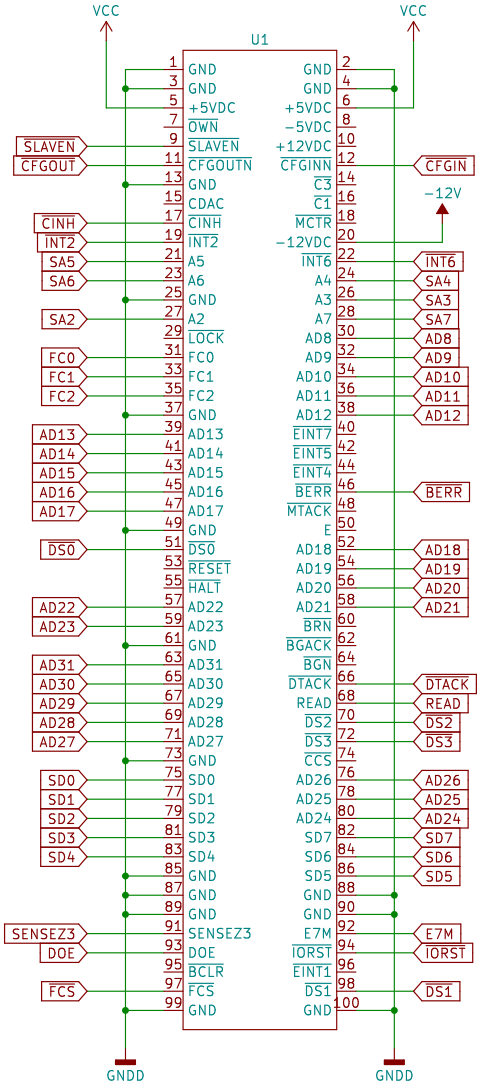
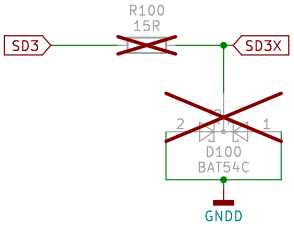
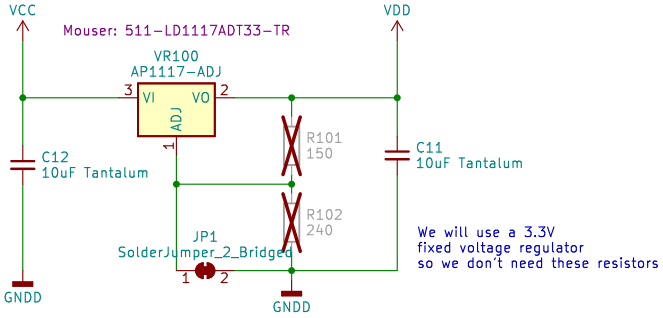




This was in the open source PCB. Deleted.



FID1
Fiducial

FID2
Fiducial

Sheet: /ZORRO/ File: sheet1.kicad_sch		
Title:		
Size: A4	Date:	**Rev:**
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