High Performance Computing

Shantanu Tyagi, Kirtan Delwadia

201801015, 201801020 Lab Date: 07/04/2021

CS-301 Prof. Bhaskar Chaudhry Due Date: 08/04/2021

Assignment 5

Hardware Details:

• Architecture: x86_64

• Byte Order: Little Endian

• CPU(s): 24(master), 16(slave)

• Sockets: 2

• Cores per socket: 6(master), 8(slave)

• Threads per core: 2(master), 1(slave)

• Nodes: 2

• Model name: Intel(R) Xeon(R) CPU E5-2620 v3 @ 2.40GHz

• L1d cache: 32K

 \bullet L1i cache: 32K

• L2 cache: 256K

• L3 cache: 15360K(master), 20480K(slave)

• Language: C

• Compiler: mpirun

Question 1: Trapezoidal Integration

Implementation Details:

1. Description about the Serial implementation

We numerically evaluate π using trapezoidal method using a single loop that runs for the entire length of the problem giving us the value of π at the end of the loop.

2. Description about the Parallel implementation

In parallel programming, we divide the entire problem into sub problems according to the number of cores/processors available and each processor calculates the result of the sub problem assigned to it. The processors calculate their local values of the integration and then send it to the master processor for addition. The CPU provided to us has cores divided equally among nodes. We have done 2 different implementations in MPI and compared it with the previously done calculations using OpenMP.

1. Using the 6 basic MPI calls

This method is more efficient on lesser number of processors since here, the master processor sends the problem to all the processors one by one and after the processors are done calculating, they send the data to the master processor one by one thus making it less useful for larger number of processors.

```
MPI_Status status;
    MPI_Init(&argc, &argv);
    t_start = MPI_Wtime();
   MPI_Comm_rank(MPI_COMM_WORLD, &my_rank);
    MPI_Comm_size(MPI_COMM_WORLD, &p);
    h = ((double)(b - a) / (double)n);
   local_n = n / p;
    local_a = a + local_n * h * my_rank;
    local_b = local_a + local_n * h;
10
11
    integral = (f(local_a) + f(local_b)) / 2;
12
    for (i = 1; i < local_n; i++) {
13
            double temp = local_a + i * h;
14
15
            integral = integral + f(temp);
    }
16
17
    integral = integral * h;
18
    if (my_rank == 0) {
19
            total = integral;
20
            for (source = 1; source < p; source++) {</pre>
21
                    MPI_Recv(&integral,1,MPI_DOUBLE,source,tag,MPI_COMM_WORLD,&status);
22
                    total = total + integral;
            }
24
    }
25
    else {
26
```

2. Using MPI_Bcast, MPI_Reduce

Here, the master processor does not send the problem data to all the processors. Rather it sends it to only a few processors and these processors then forward it to other remaining processors. This reduces the time when the number of processors is large.

```
MPI_Init(&argc, &argv);
   MPI_Comm_size(MPI_COMM_WORLD, &numprocs);
   MPI_Comm_rank(MPI_COMM_WORLD, &myid);
   n = atoll(argv[1]):
   start = MPI_Wtime();
   MPI_Bcast(&n, 1, MPI_INT, 0, MPI_COMM_WORLD);
      = 1.0 / (double) n;
8
   sum = 0.0;
9
   for (i = myid + 1; i <= n; i += numprocs) {
10
           x = h * ((double)i - 0.5);
11
            sum += 4.0 / (1.0 + x * x);
12
   }
13
   mypi = h * sum;
14
15
   MPI_Reduce(&mypi, &pi, 1, MPI_DOUBLE, MPI_SUM, 0, MPI_COMM_WORLD);
16
   if (myid == 0)
17
18
            printf("%llu %.16f\n", n, MPI_Wtime() - start);
19
20
   MPI_Finalize();
```

Complexity:

1. Complexity of serial code:

The time varies linearly with the problem size since we just have one loop which runs for n times. When the problem size is n, we have the time complexity as O(n).

2. Complexity of parallel code:

Here again we have a problem with size n, however it is divided into p processors each of which runs in parallel. This means that the loop runs $\frac{n}{p}$ times on each processor giving us a time complexity of $O(\frac{n}{p})$.

3. Cost of Parallel algorithm

Speedup is calculated as ratio of time taken to execute the code serially to time taken to execute the code parallelly. The expected speed up is p but this if often not achieved due to memory access, parallel overhead, and thread synchronisation.

4. Theoretical Speed Up:

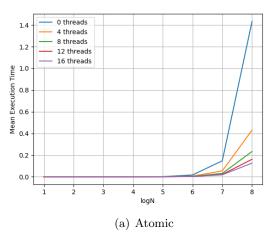
For a problem of size n and total p processors, we have theoretical speed up as $\frac{n}{(\frac{n}{p})} = p$. Thus for 1 processor, we get $\frac{n}{(\frac{n}{p})} = 1$

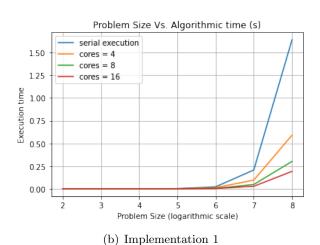
Thus for 1 processor, we get $\frac{n}{(\frac{n}{1})} = 1$ and for 4 processors, we get $\frac{n}{(\frac{n}{4})} = 4$ Speedup is calculated as ratio of time taken to execute the code serially to time taken to execute the code parallelly. The expected speed up is p but this if often not achieved due to memory access, parallel overhead, and thread synchronisation.

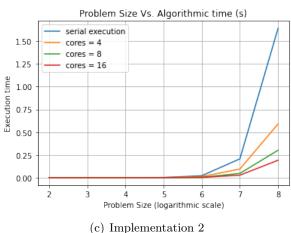
Curve Related Analysis:

1. Time Curve Related analysis:

As the problem size increases, the time taken also increases for both serial and parallel implementations. This is because with increased problem size, more calculations need to be done and the loop runs longer. For a given problem size, as the number of cores increase, the execution time decreases since the work gets divided between the cores. The second implementation gives slightly better results for larger problem size because of the reasons mentioned earlier. However, the OpenMP implementation gave lesser execution times compared to the MPI implementations for all problem sizes and also for different cores taken.

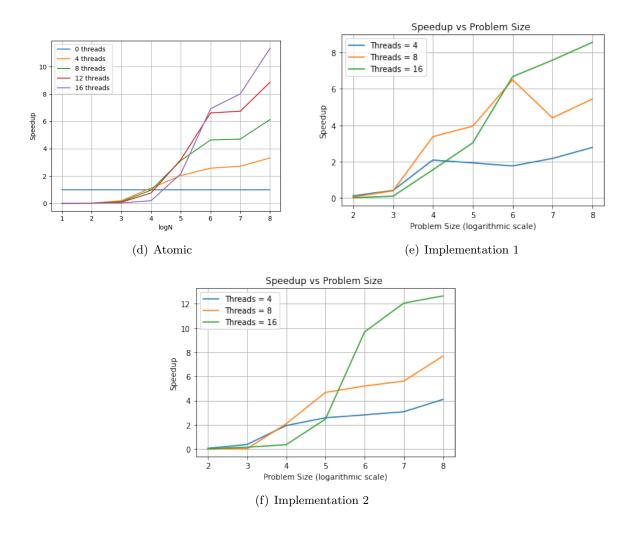






2. Speedup Curve Related analysis:

The speed up increases as the number of processes increases on which the code runs. However, if we increase the threads beyond a certain threshold than the cost to create and synchronize threads will become significant giving poorer results. As the problem size increases, the speed up reaches closer to number of processors involved. The deviation in these values from the theoretical values depicts that for large problem size the memory access time also comes into consideration. For larger problem sizes, implementation 2 gives better results than the other. OpenMP performs better than the first MPI implementation and worse then the second MPI implementation.



3. Efficiency Curve Related analysis:

Efficiency is speedup divided by the number of threads, p. It is also observed that with greater number of processors working together the efficiency also decreases. The greater number of threads lesser will be the efficiency because managing so many threads also requires time and the parallel overhead starts dominating. Cost of creating more and more threads is not able to balance how much more performance it gives as compared to lesser number of threads. As the problem size increases the efficiency also gradually starts increasing for a given implementation and gradually reaches 1. For larger problem sizes, implementation 2 gives better results than the other. For larger problem sizes, implementation 2 gives better results than the other.

OpenMP performs better than the first MPI implementation and worse then the second MPI implementation.

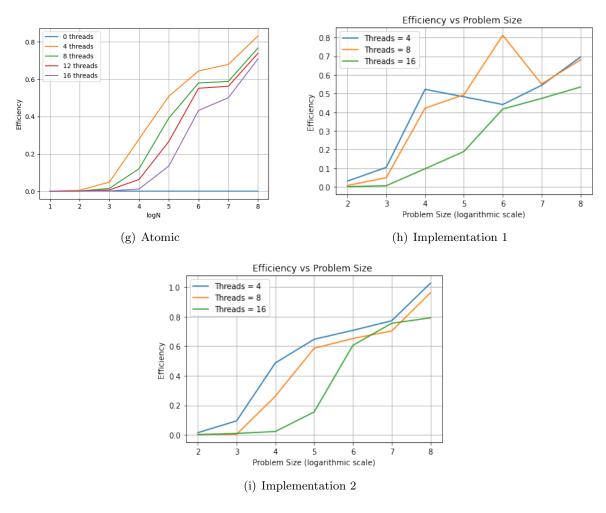


Figure 1: Efficiency

4. Conclusion:

OpenMP is a shared memory device program where as MPI is a distributed memory device program, which means that the overheads in OpenMP are less compared to MPI. The communication between nodes is difficult and expensive compared to communication between shared memory systems.