

Yang Cui

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Current Status: Fresh graduate | Objective: (Internship) Digital IC Design Engineer

EDUCATION

Heilongjiang University 2018.09 - 2022.07
Integrated circuit design and integrated systems Bachelor Harbin
GPA:3.04(top 40%) CET-6:505

RESEARCH AND COMPETITION EXPERIENCE

Graduation Project: System level testability structure design of IP core s349 2021.12 - Present

A project which bases on IEEE1500 test standard, finish the internal testability circuit structure design of IP core s349 and complete the function simulation verification

National College Students Integrated Circuit Innovation and Entrepreneurship Competition(5th)

Read chip manual, be responsible for test vector and CPP writing

The 5th Internet + College Student Innovation and Entrepreneurship Competition

Develop a knowledge-based Android application, complete the business plan and research report

Scores of important specialized courses 2018.09 - Present

C++ advanced programming (81), digital electronic circuits (83), VLSI test technology (79), ASIC design synthesis and timing analysis (85), semiconductor device principles(79)

PROJECT EXPERIENCE

The course design of digital integrated circuits 2021.09 - 2021.10

Vending machine program with verilog HDL. To realize corresponding shipping and change functions with FSM.

Digital electronic circuit basics 2021.01 - Present

During the preparation for the postgraduate entrance examination, I have learned a lot of knowledge of digital electronics, and have a very solid foundation of digital electronics. I have mastered all the basic sequential logic circuits and combined logic circuits and most of typical circuits can be expressed in Verilog language by me

Integrated circuit design software training

Search the relevant comparator design papers in English and Chinese. Design the comparator part of SAR

HDLBits--Verilog language practice 2021.11 - Present

I have done a lot of Verilog writing practice on HDLBits, CSDN and other websites. So I have strong Verilog writing ability and digital circuit design experience.

COMMUNITY AND ORGANIZATIONAL EXPERIENCE

Tracking car design which bases on 51MCU 2018.10 - 2019.03

Science and Technology Innovation Association, College of Electronic Engineering, Heilongjiang University

Get some MCU design related knowledges, enhanced the programming ability

SKILLS, CERTIFICATIONS & OTHERS

- **Skills:** C/C++, Office, Image Processing, Reviewing Literature, Video Editing
- **Certifications:** NCRE band 2, Driving licence,
- **Languages:** CET-6, Mandarin Level 2
- **Interests:** Movie
- **Activities:** Voluntary blood donation and donation